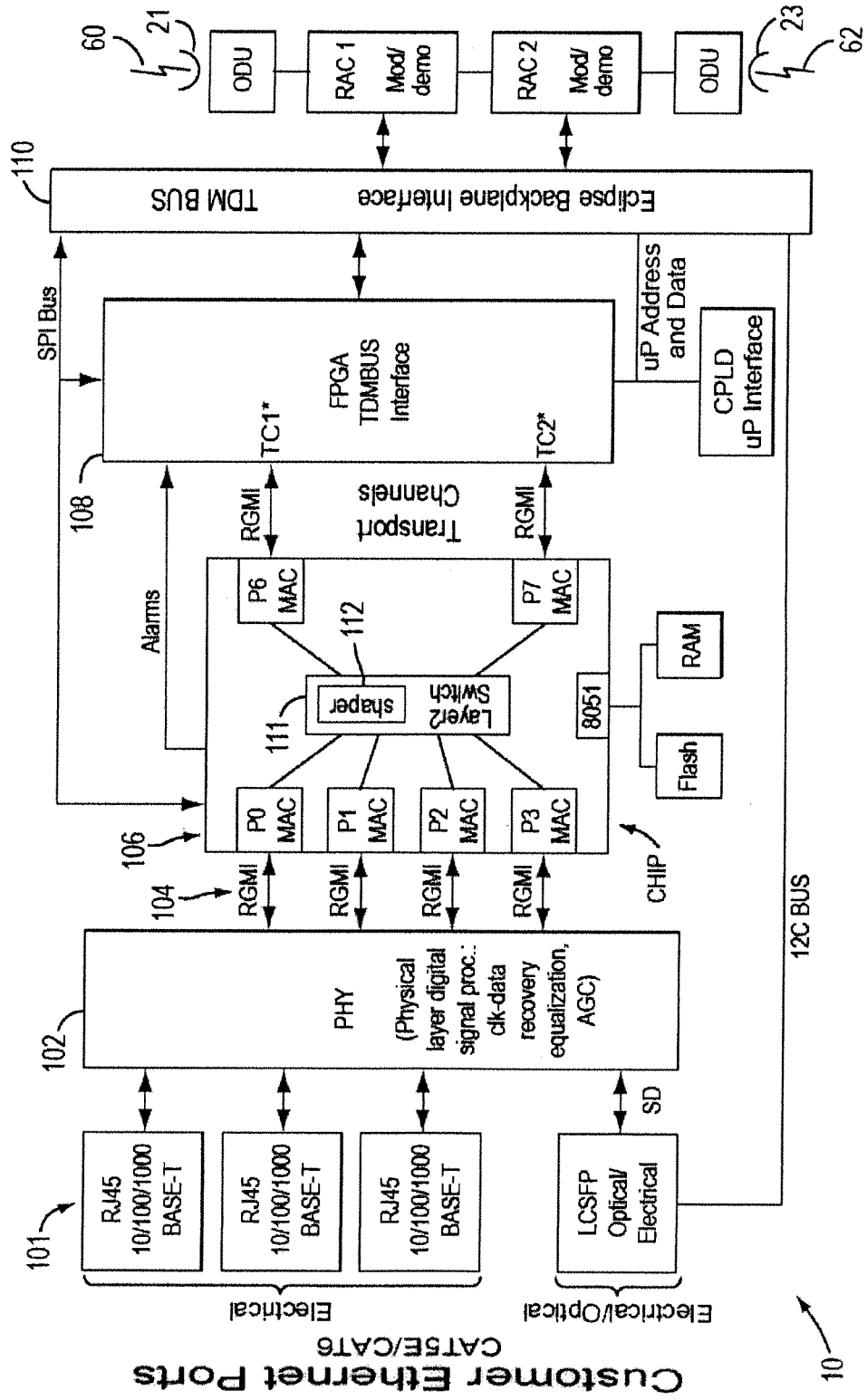
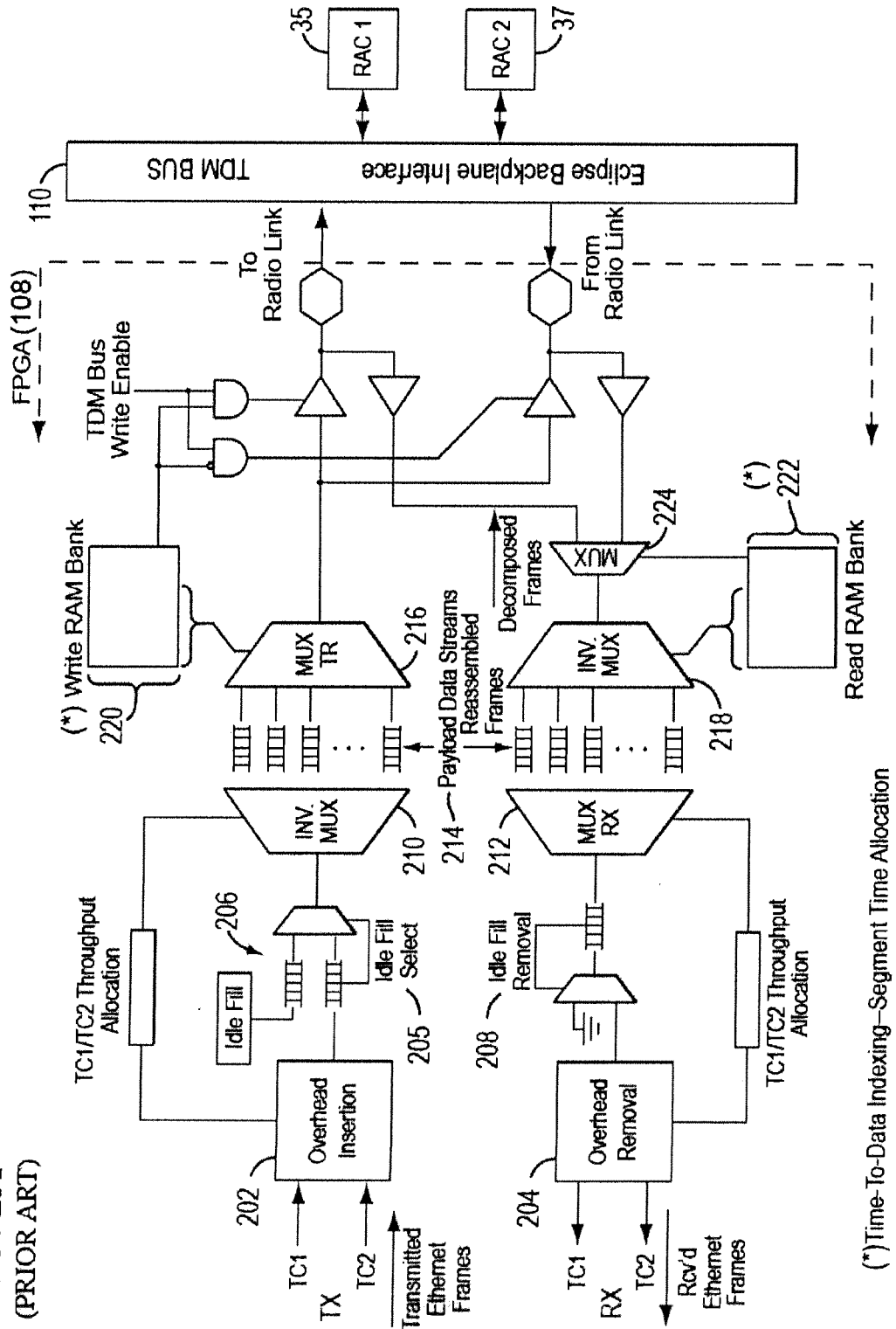


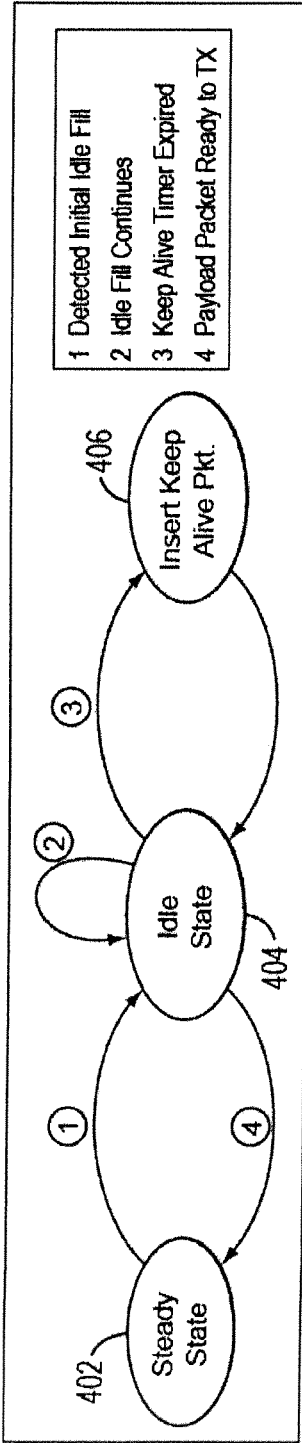
FIG. 1
(PRIOR ART)



Bidirectional Dual Transport Channel Packet Processing Mechanism

FIG. 2A
(PRIOR ART)





Keep Alive Message Insertion State Diagram
FIG. 2B



Mode of Operation

☒ Transparent

☒ No Aggregation

☐ Aggregate C1 & C2

☐ Aggregate C1, C2 & P1

☐ Aggregate C1, C2 & P4

☐ Mixed

☐ VLAN

P1

P2

P3

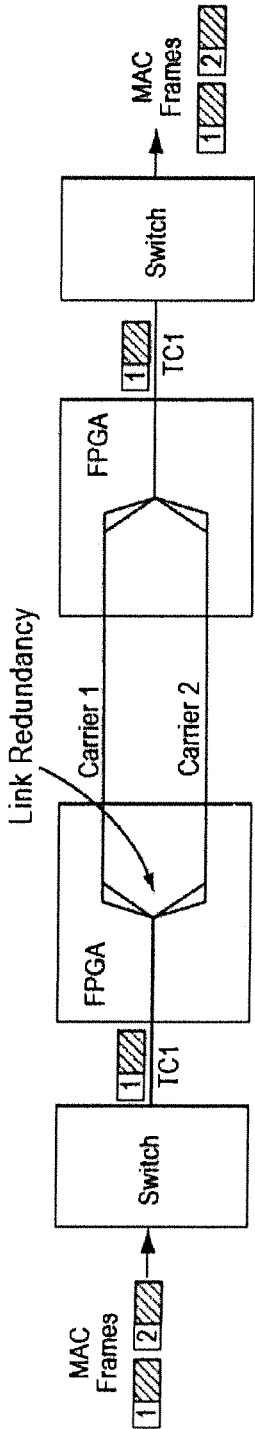
P4

C1

C2

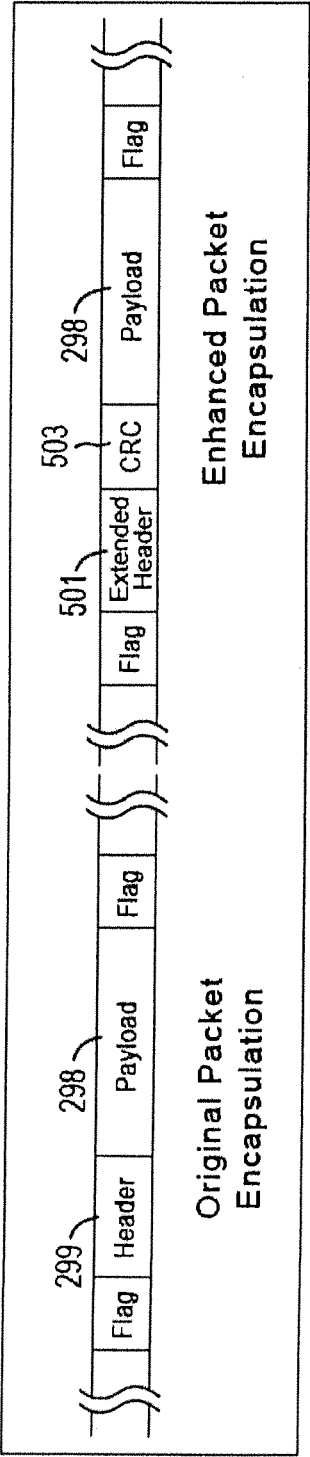
Physical Layer Link Aggregation Configuration

FIG. 3A



Physical Layer Aggregation

FIG. 3B



Modified Header for Rapid Failure Detection

FIG. 5

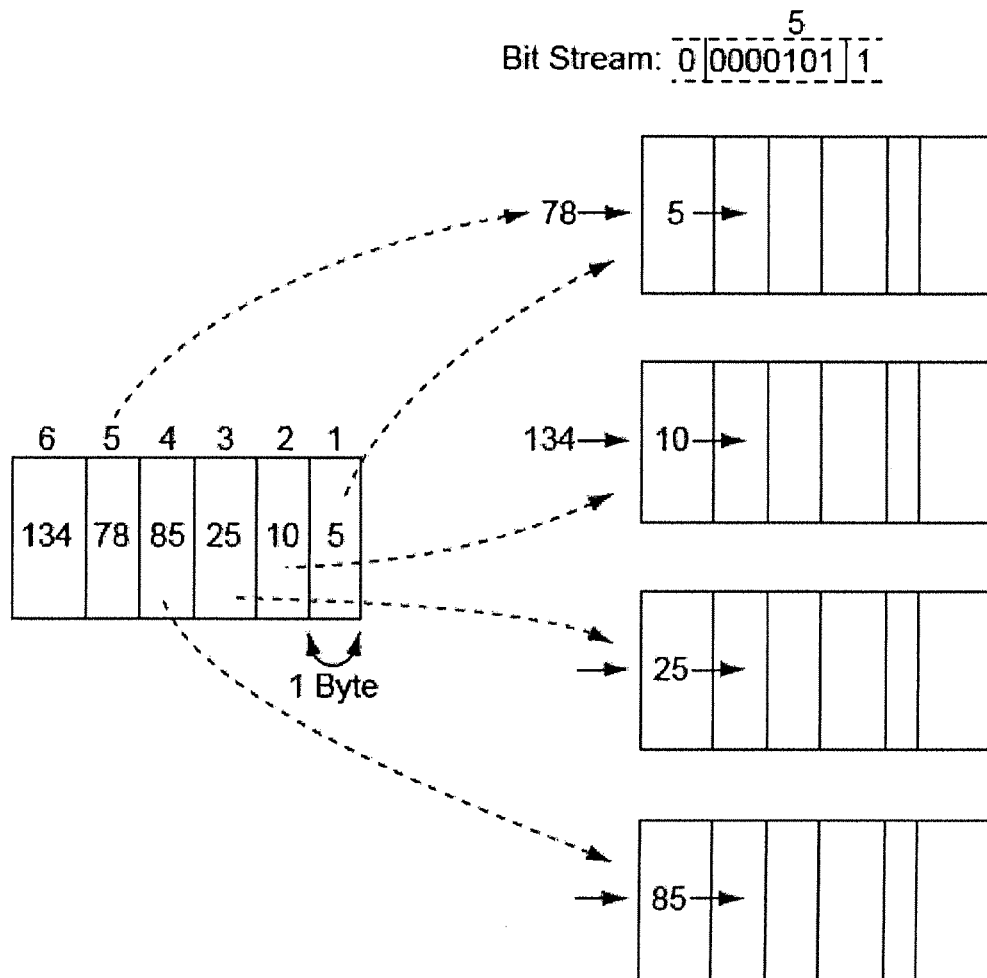


FIG. 6

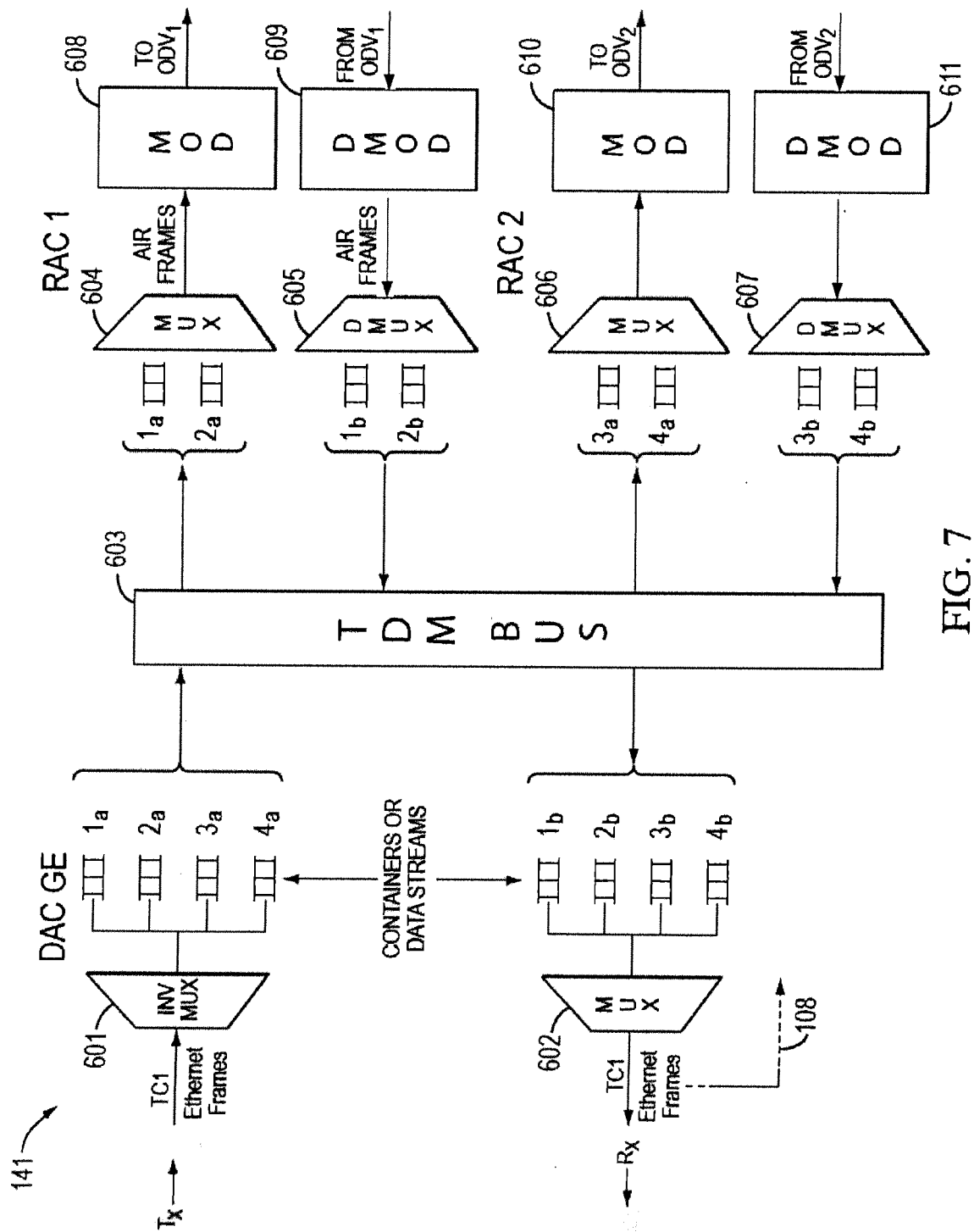


FIG. 7

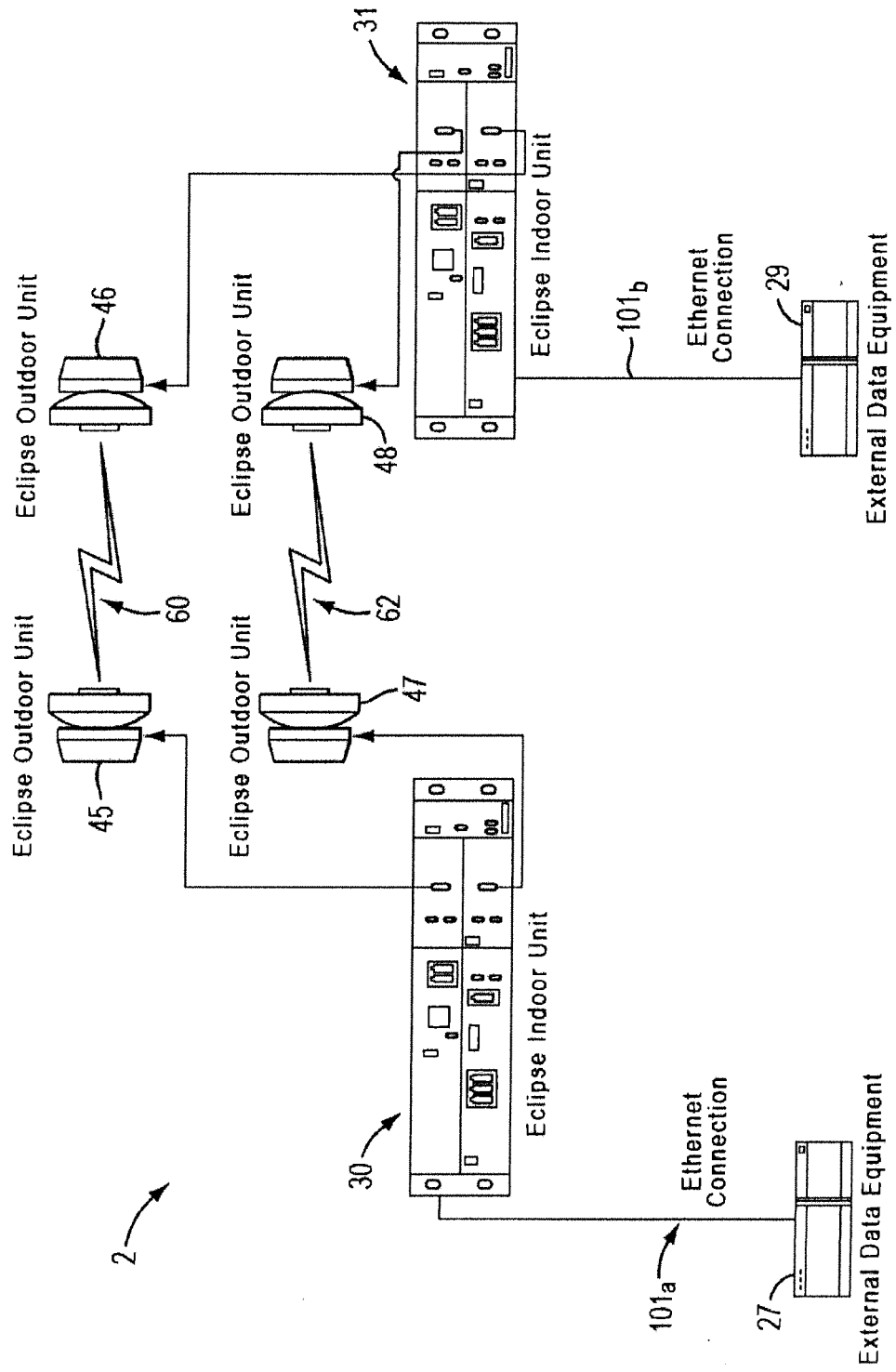


FIG. 8

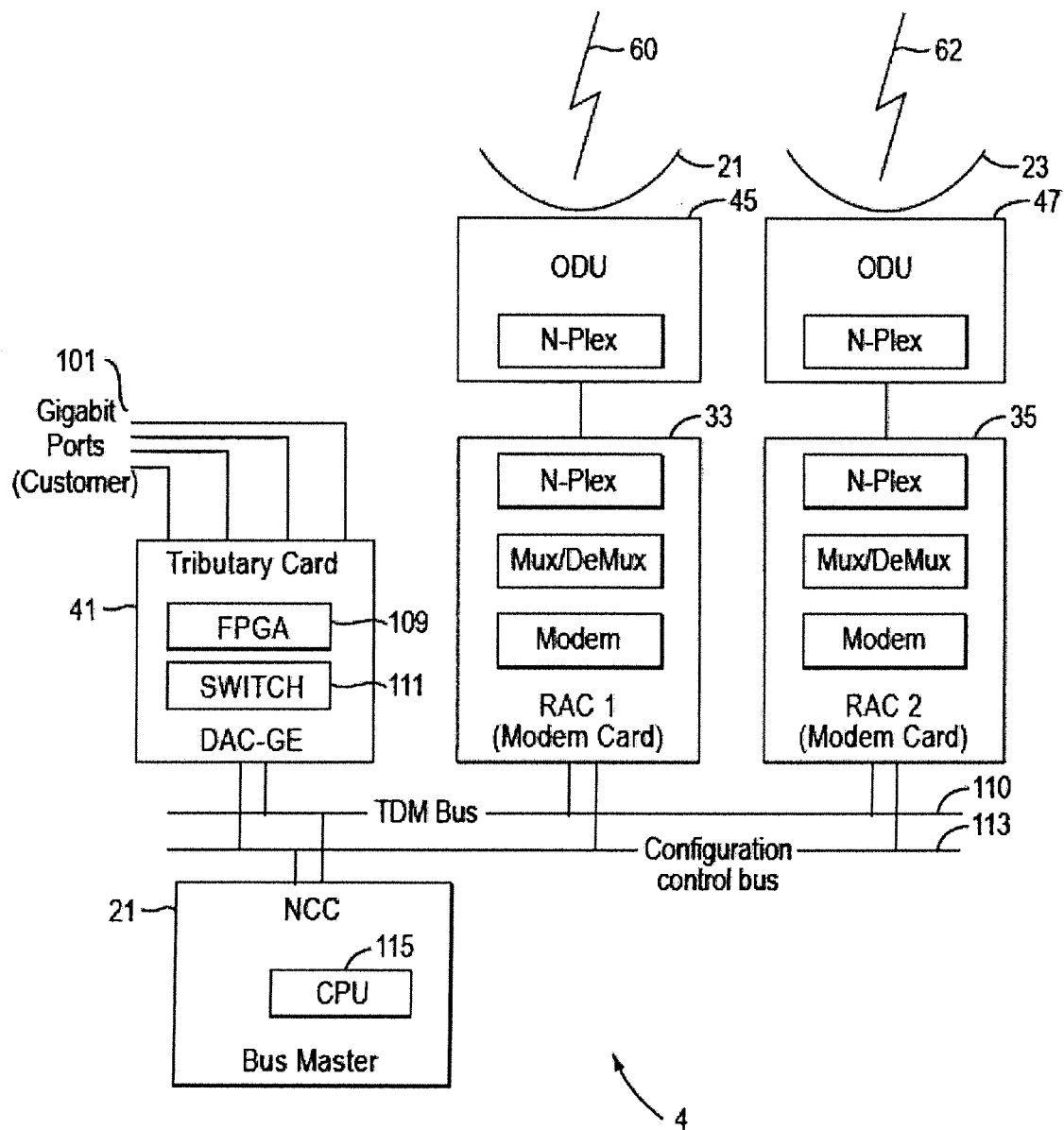
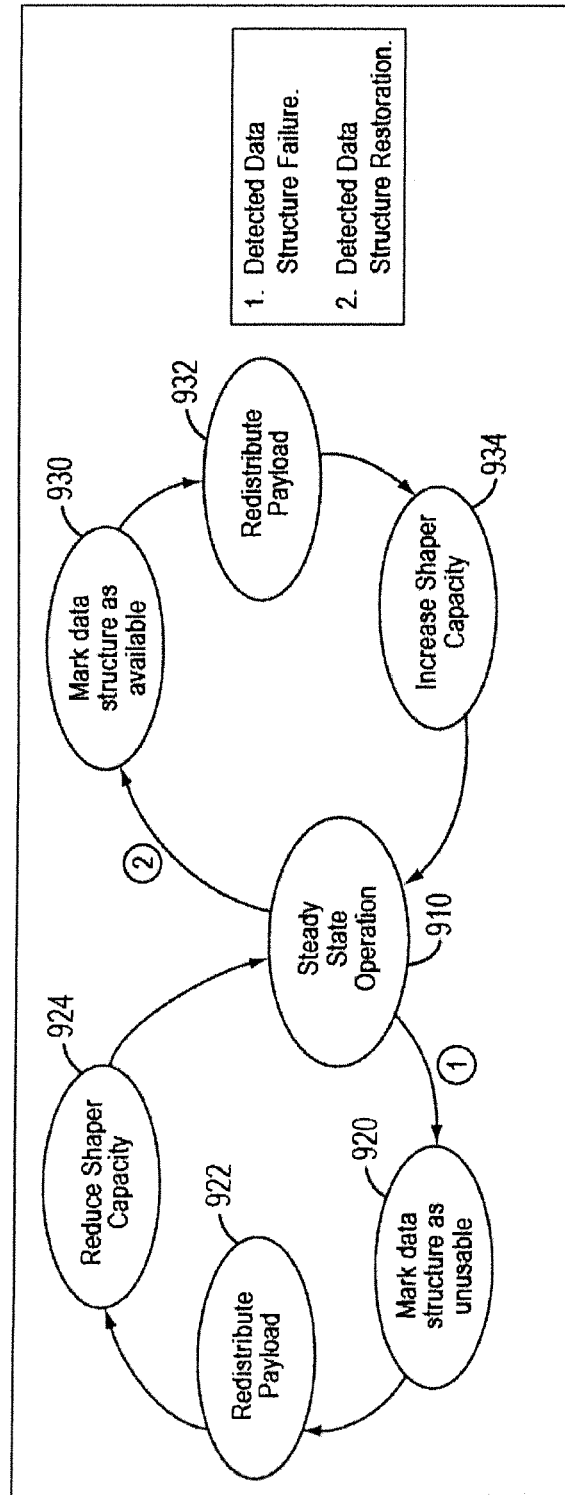
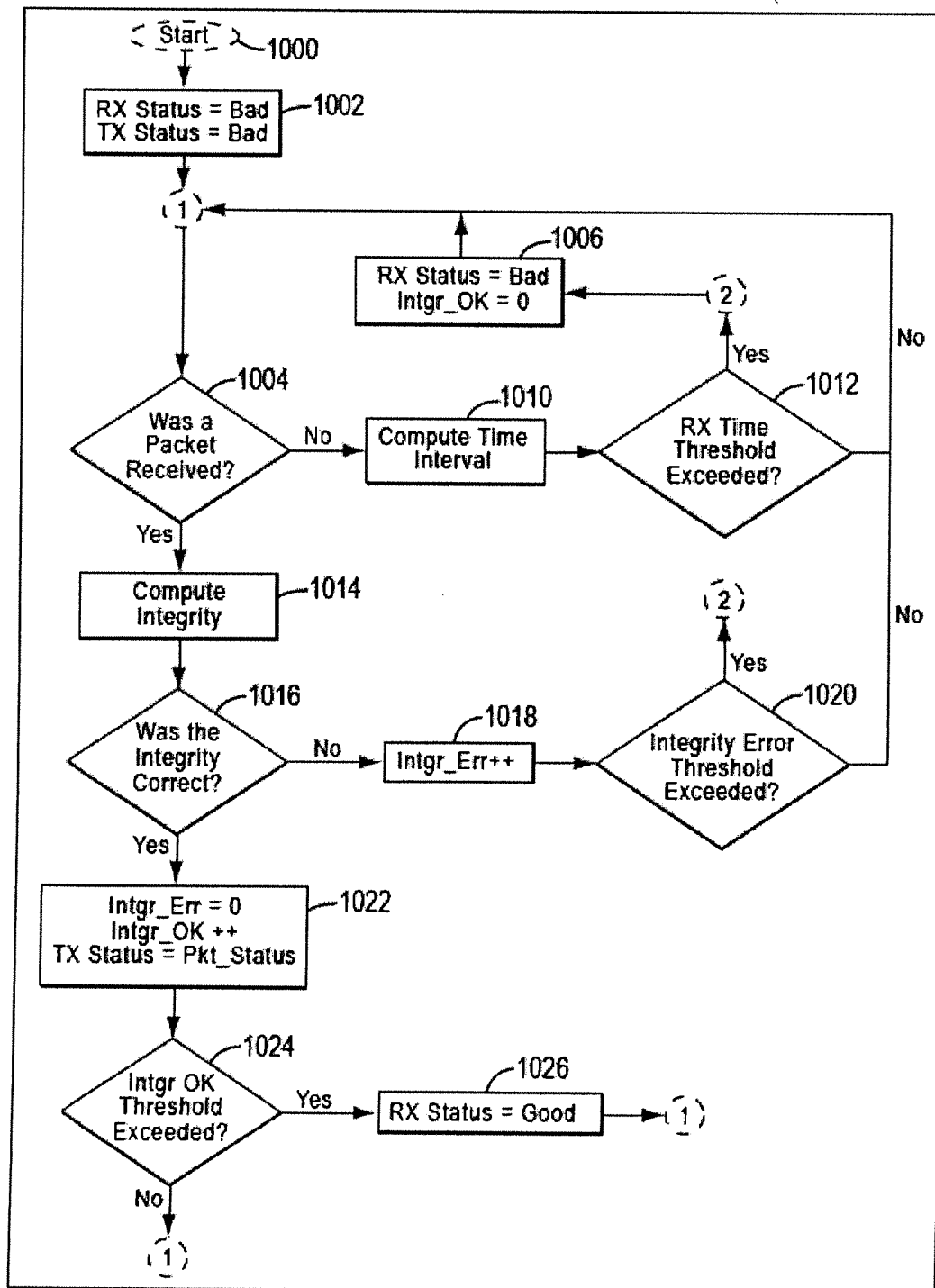


FIG. 10



Link Aggregation Payload Redistribution State Diagram

FIG. 11



Failure Detection and Recovery Flow Chart

FIG. 13