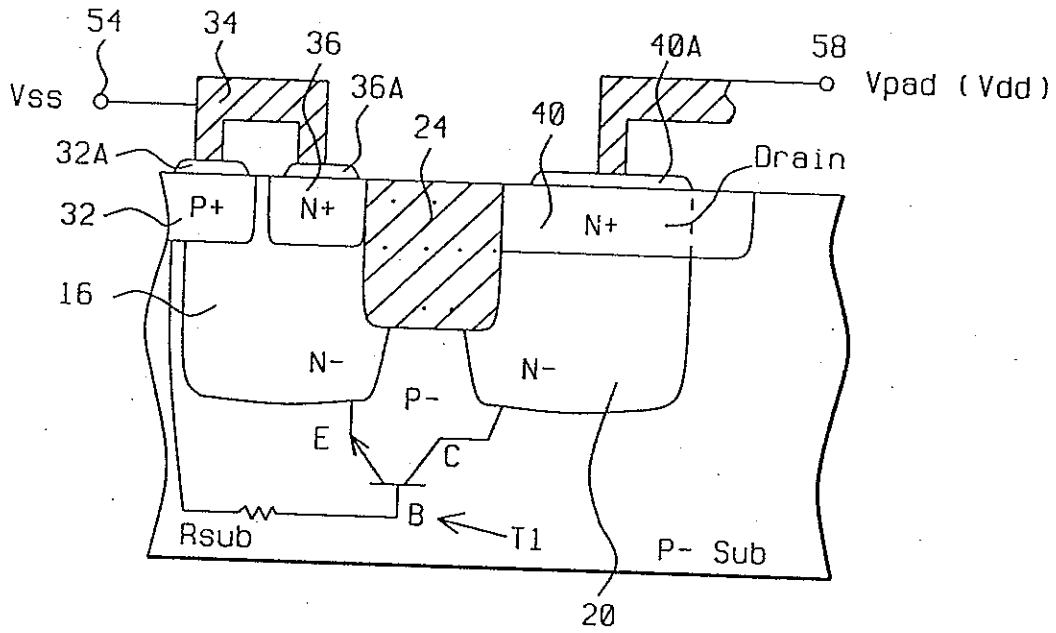

54
34
36
40A
58
Vss
36A
Vpad (Vdd)
40
24
32A
Drain
32
P+
N+
N+
16
N-
N-
P-
E
C
B
T1
Rsub
P- Sub
20

FIG. 1

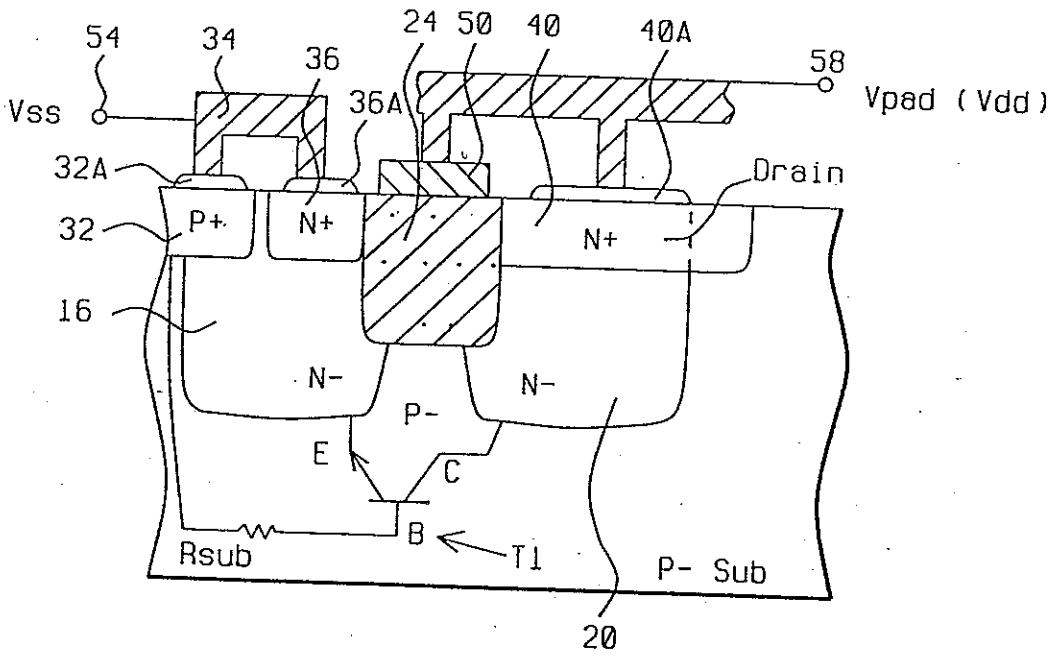

54
34
24
50
40
40A
58
36
Vss
36A
Vpad (Vdd)
32A
Drain
32
P+
N+
N+
16
N-
N-
P-
E
C
B
T1
Rsub
P- Sub
20

FIG. 2

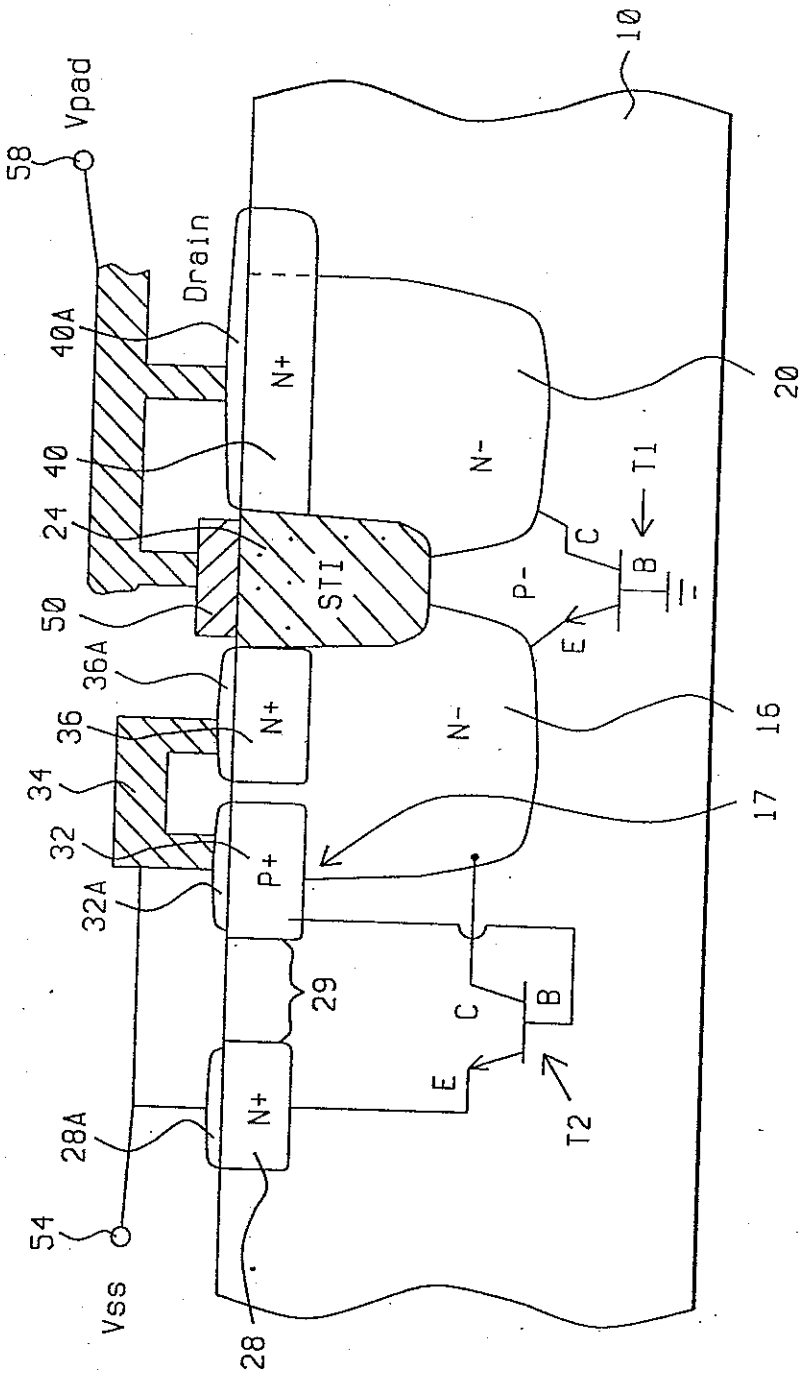

10
58
Vpad
Drain
40A
40
24
50
36A
36
34
32
32A
28A
54
Vss
28
N+
N+
N+
P+
STI
N-
N-
P-
20
16
17
29
T1
T2
B
C
E
B
C
E

FIG. 3

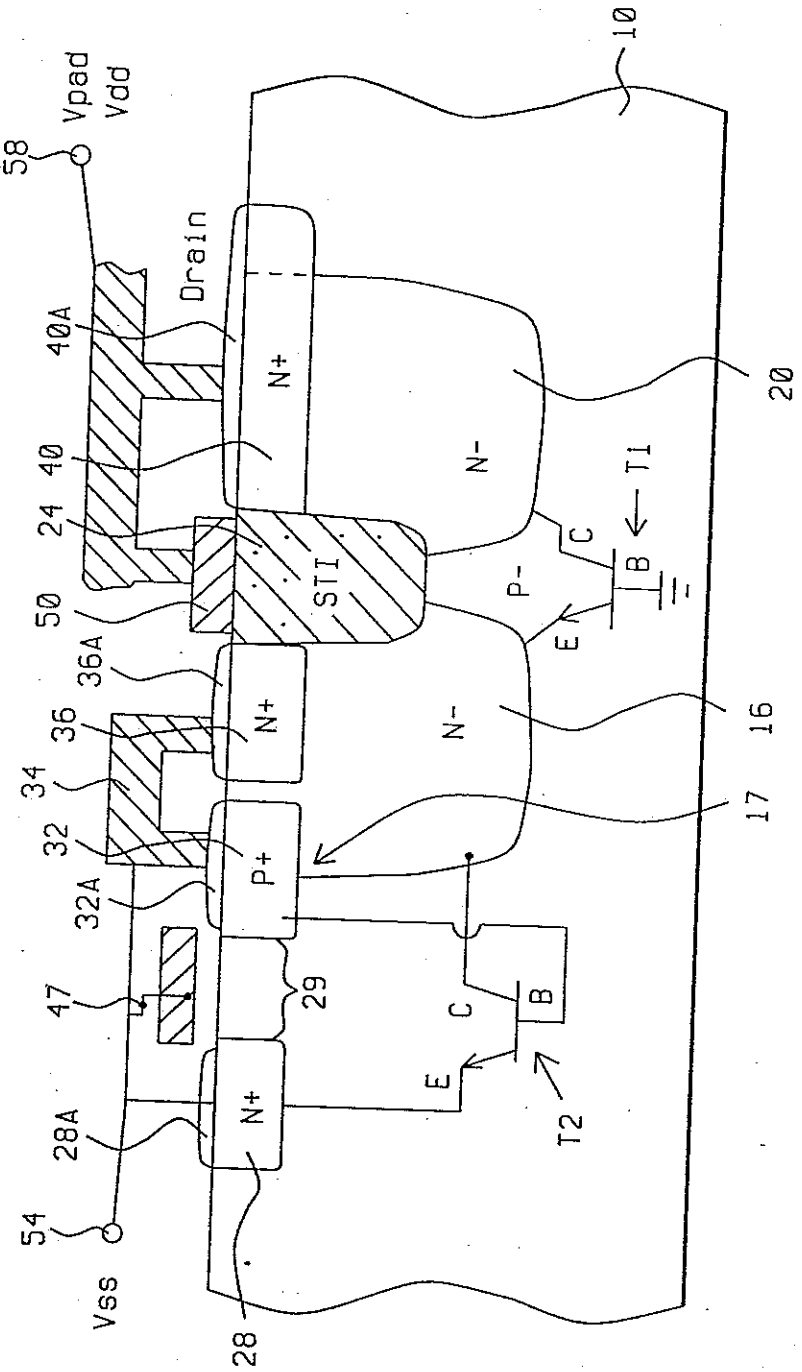

10
Vpad
Vdd
58
Drain
40A
N+
20
40
N-
T1
24
C
B
STI
P-
50
E
36A
N+
36
N-
16
34
17
32
P+
32A
29
47
C
B
E
T2
28A
N+
54
Vss
28

FIG. 4

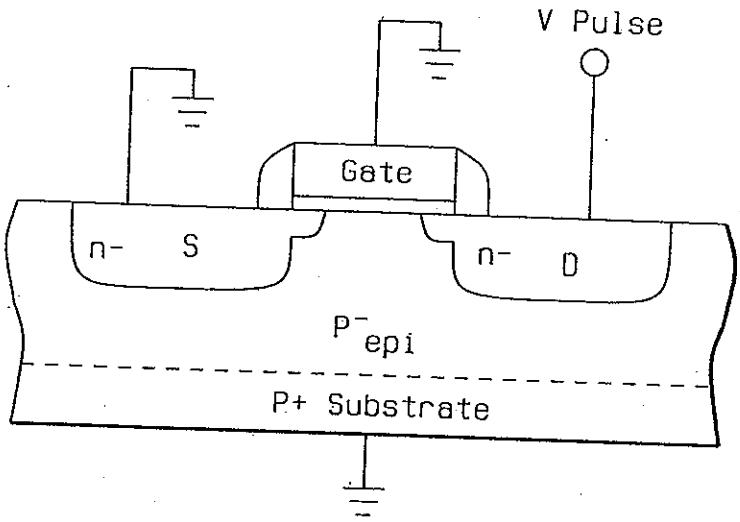

V Pulse
Gate
n- S
n- D
P- epi
P+ Substrate

FIG. 5A

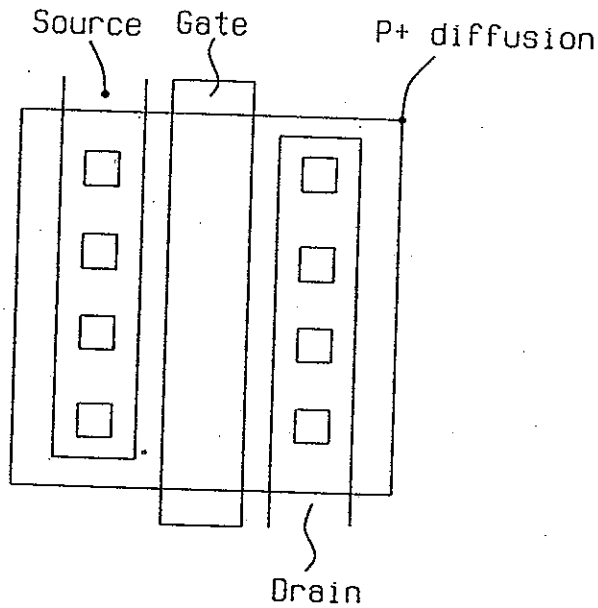

Source
Gate
P+ diffusion
Drain

FIG. 5B

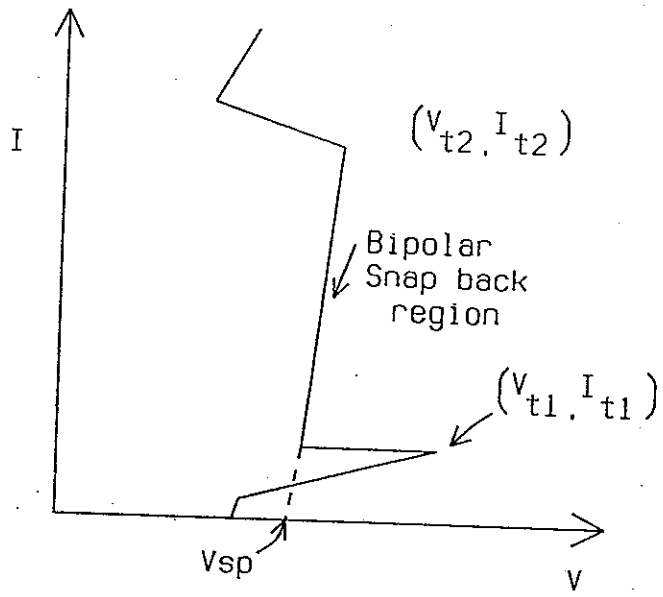


FIG. 5C – Prior Art

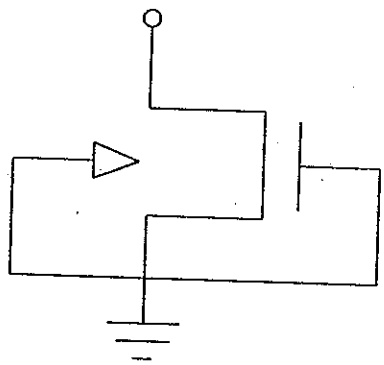


FIG. 5D – Prior Art