**WHAT IS CLAIMED IS:**

1. A system comprising:

a first flight control computer (FCC) (112) of two or more FCCs (112, 113);

a second FCC (113) of the two or more FCCs;

at least one selector (182) in communication with the first FCC; and

at least one watchdog window (180) in communication with the at least one selector (182), wherein the at least one watchdog window monitors a performance of the first FCC (112) based on an electrical pulse (192, 194, 196) emitted by the first FCC (112);

wherein the at least one watchdog window is configured to detect a fault pulse of the electrical pulse emitted by the first FCC; ~~and~~

wherein the selector is configured to toggle to the second FCC based on the detected fault pulse emitted by the first FCC; and

wherein the detected fault pulse is a pulse that is outside a preferred range.

~~2. The system of claim 1, wherein the detected fault pulse is a pulse that is outside a preferred range.~~

~~3.~~2. The system of claim 1, wherein the detected fault pulse is a pulse that skips a beat.

~~4.~~3. The system of claim 1, wherein the detected fault pulse is a pulse that has a frequency and amplitude outside of a preferred range of a baseline pulse.

~~5.~~4. The system of claim 1, wherein the selector is further configured to reset power to the first FCC.

~~6.~~5. The system of claim ~~5~~4, wherein the selector is configured to toggle to the first FCC after power has been reset to the first FCC.

~~7.~~6. The system of claim ~~6~~5, wherein the at least one watchdog window is further configured to monitor the performance of the first FCC after the first FCC is toggled by the selector.

~~8.~~7. A method comprising:

monitoring, via a watchdog window (180), a performance of a first flight control computer (FCC) (112) of two or more FCCs, wherein the performance is based on an electrical pulse (192, 194, 196) emitted by the first FCC;

detecting, via the watchdog window, a fault pulse of the electrical pulse emitted by the first FCC; and

toggling, by a selector in communication with the watchdog window, to a second FCC (113) based on the detected fault pulse emitted by the first FCC, wherein the detected fault pulse is a pulse that is outside a preferred range.

~~9. The method of claim 8, wherein the detected fault pulse is a pulse that is outside a preferred range.~~

~~10.~~8. The method of claim ~~8~~7, wherein the detected fault pulse is a pulse that skips a beat.

~~11.~~9. The method of claim ~~8~~7, wherein the detected fault pulse is a pulse that has a frequency and amplitude outside of a preferred range of a baseline pulse.

~~12.~~10. The method of claim ~~8~~7, further comprising:

resetting, via the selector, power to the first FCC.

~~13.~~11. The method of claim ~~12~~10, further comprising:

toggling, by the selector, to the first FCC after power has been reset to the first FCC.

~~14.~~12. The system of claim ~~13~~11, further comprising:

monitoring, via the watchdog window, the performance of the first flight control computer (FCC) after the first FCC is toggled by the selector.

~~15. A flight control computer (112) comprising:~~

~~a field programmable gate array (FPGA) (152);~~

~~a flight control computer (FCC) processor (153) in communication with the FPGA via an FCC bus (160);~~

~~a plurality of serial ports (170) in communication with the FPGA and FCC processor; and~~

~~a controller chip in communication with the plurality of serial ports, wherein the controller chip is configured to transform a parallel output of the FCC bus into a serial form for transmission though a serial port of the plurality of serial ports.~~