

ABSTRACT

A pluggable memory apparatus includes a plurality of memory sockets, a slot, and a main controller. The slot connects with an external device. The main controller is communicably coupled to the
5 plurality of memory sockets and the slot, and has error correction function. The main controller sets at least one data processing mode among a plurality of data processing modes based on error rates of a plurality of memory modules mounted in the plurality of memory sockets, and remaps input/output data according to the at least one
10 data processing mode.

FIG. 3