

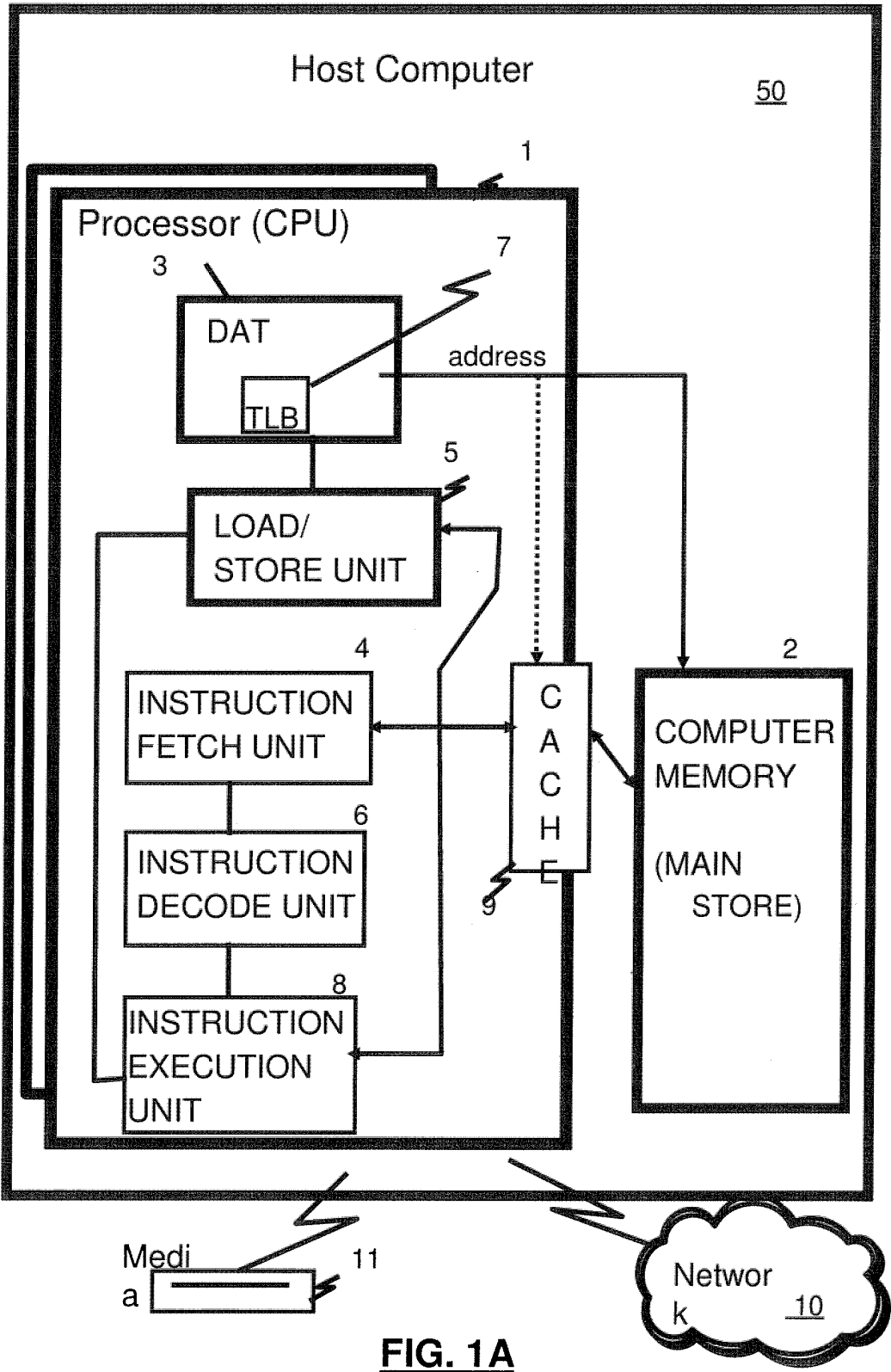
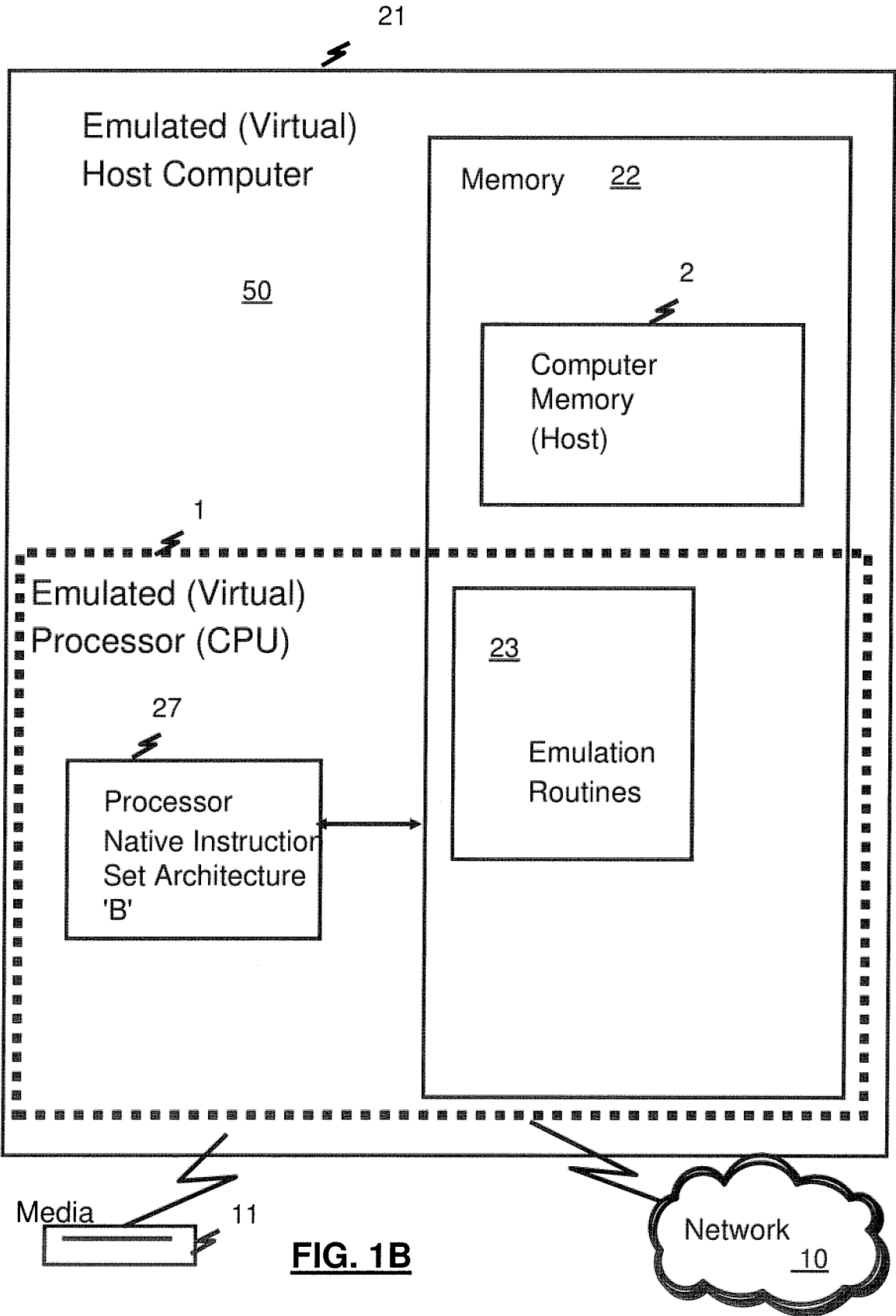


FIG. 1A



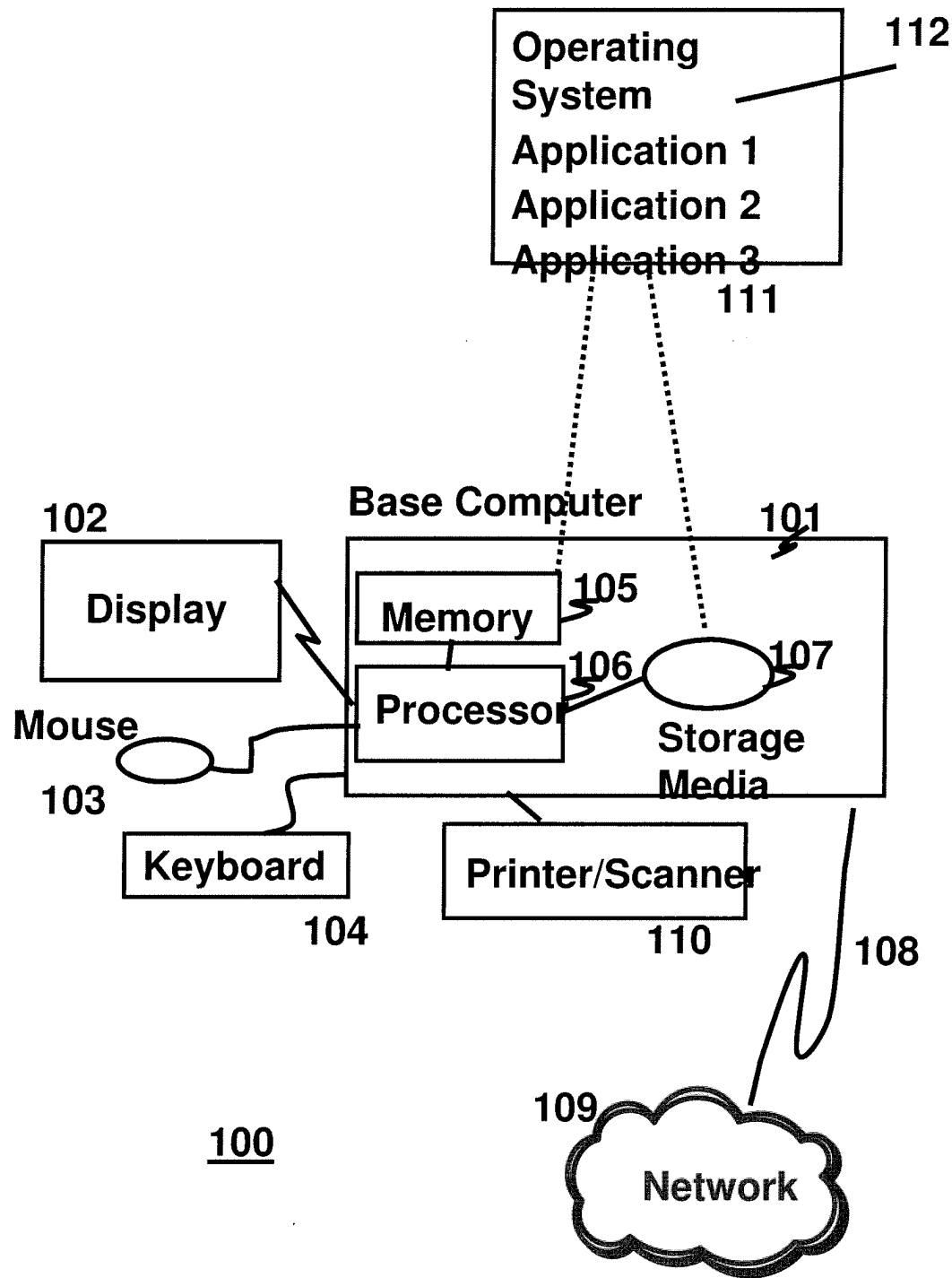
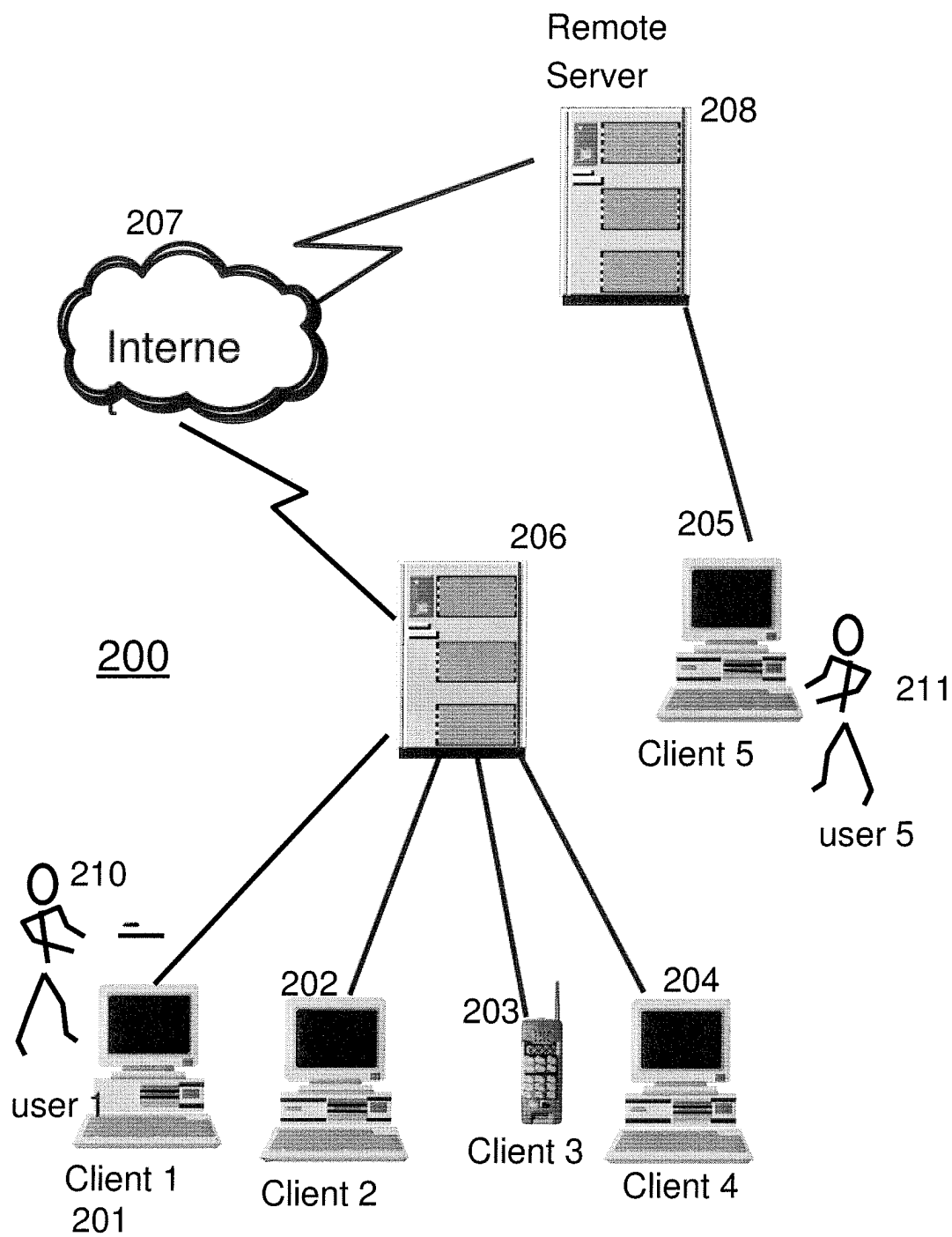
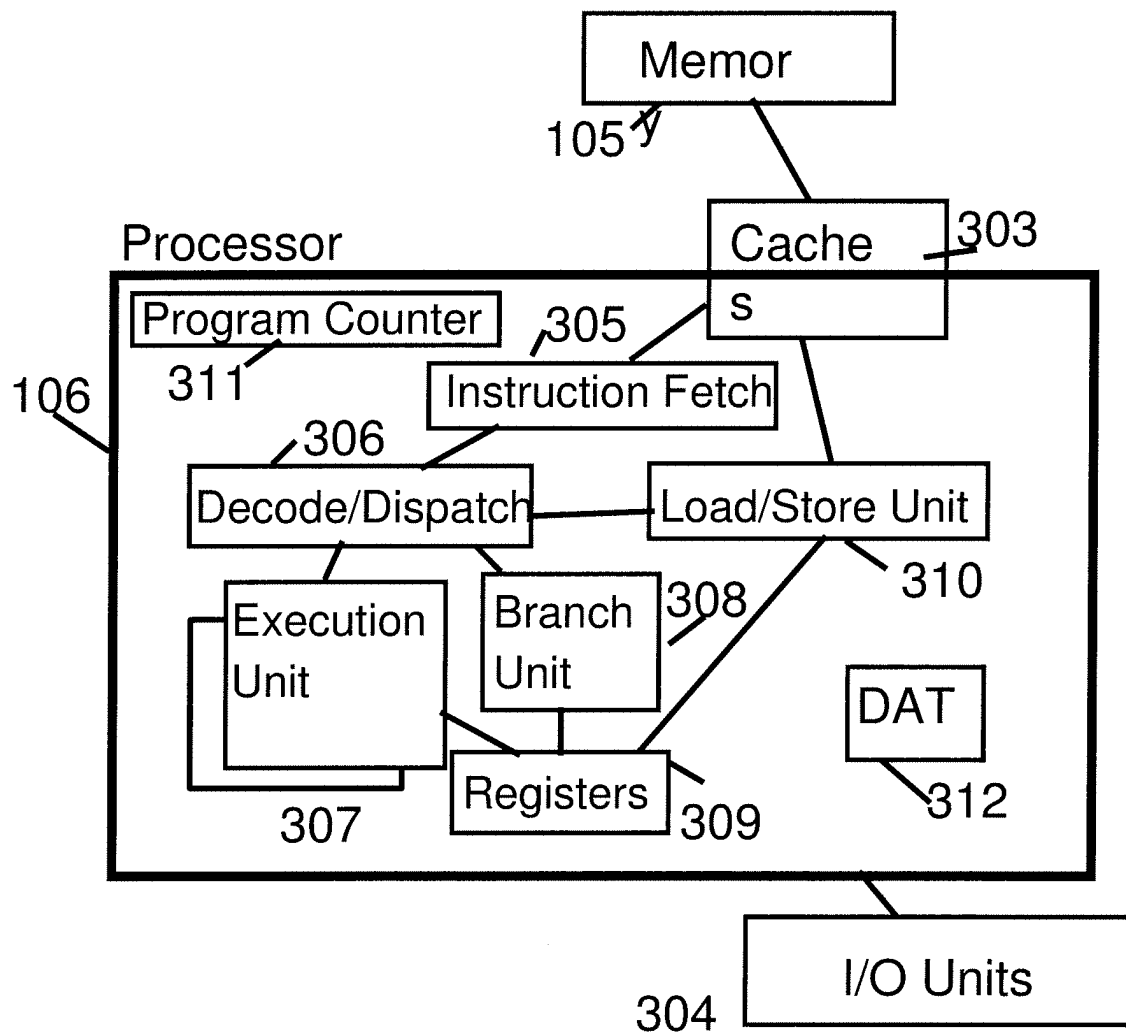


FIG. 1C

**FIG. 2**

**FIG. 3**

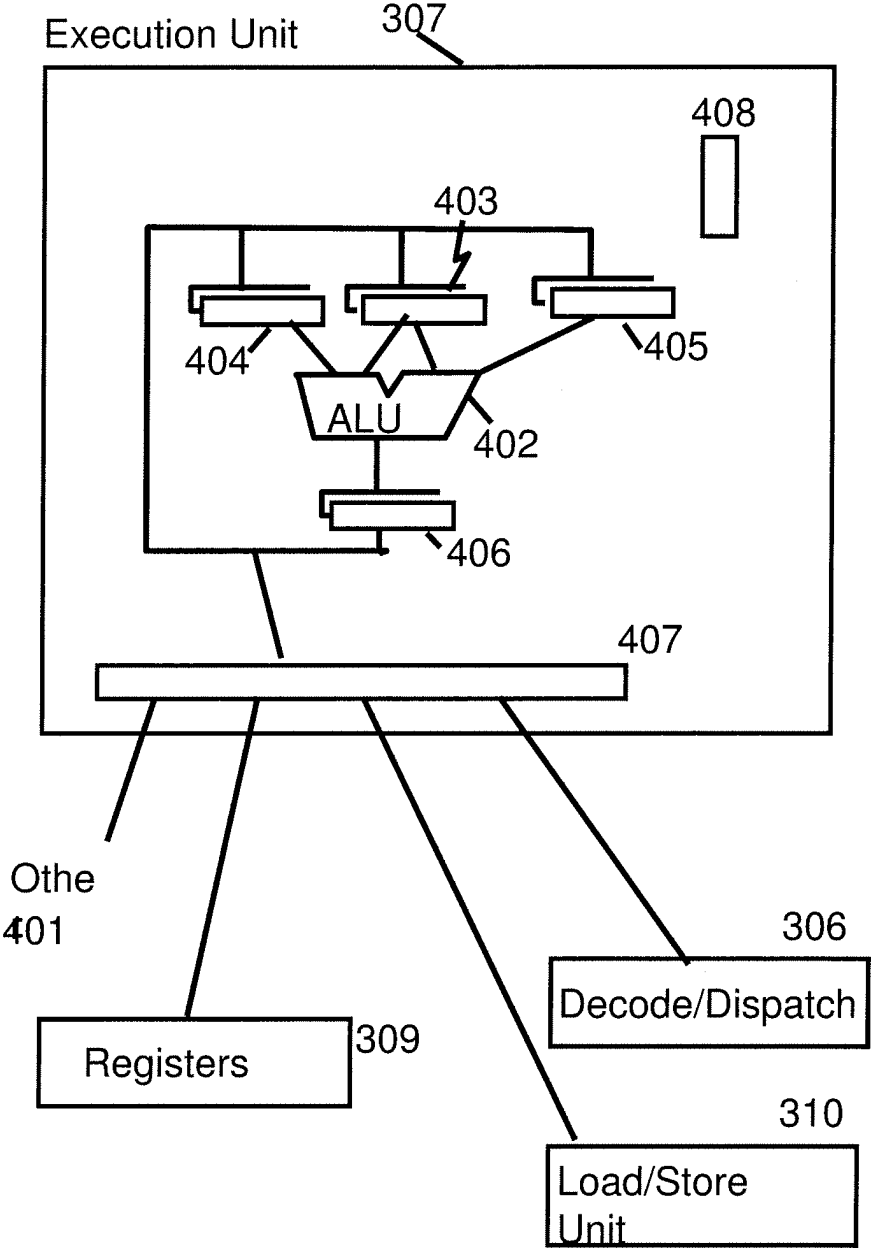


FIG. 4A

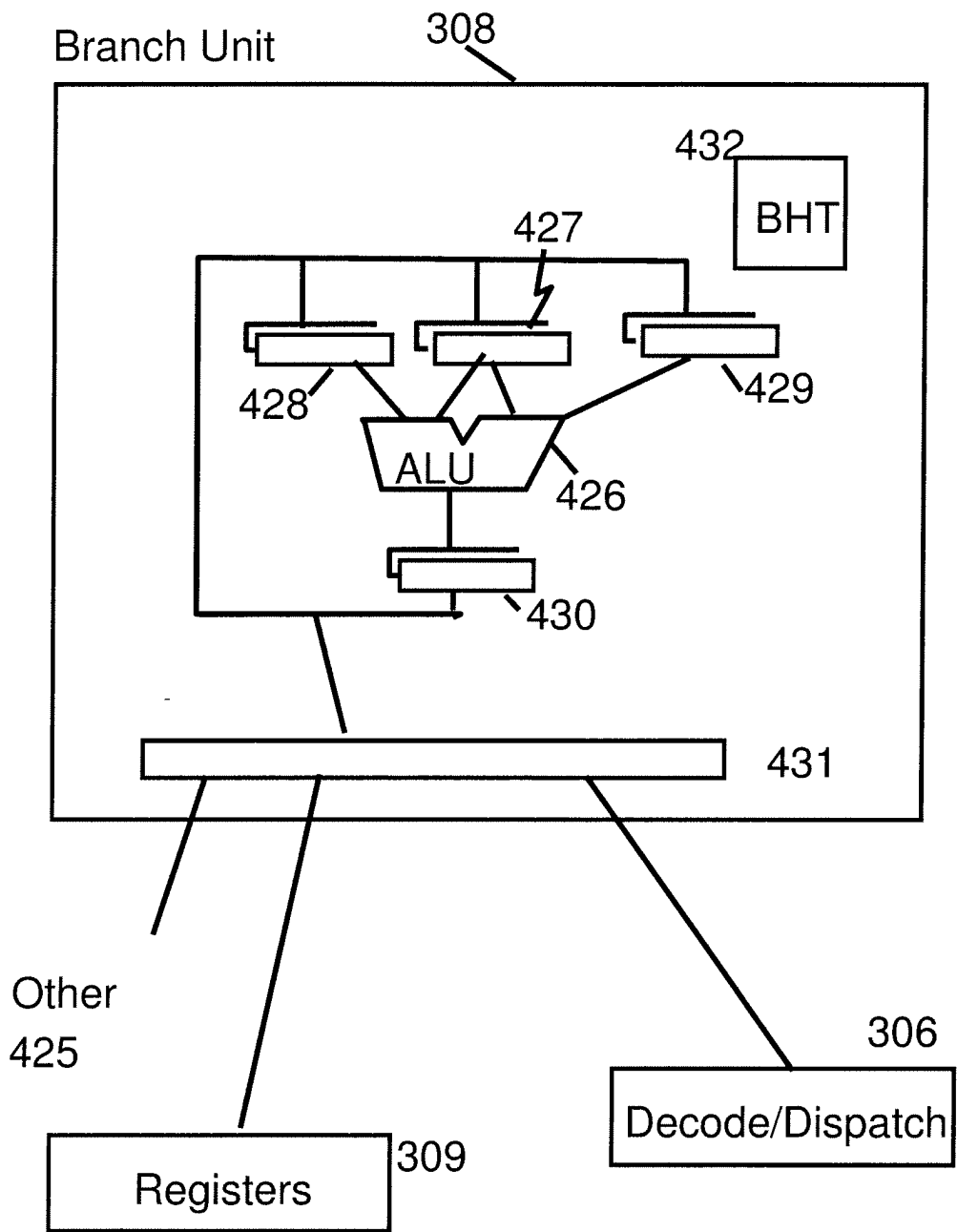


FIG. 4B

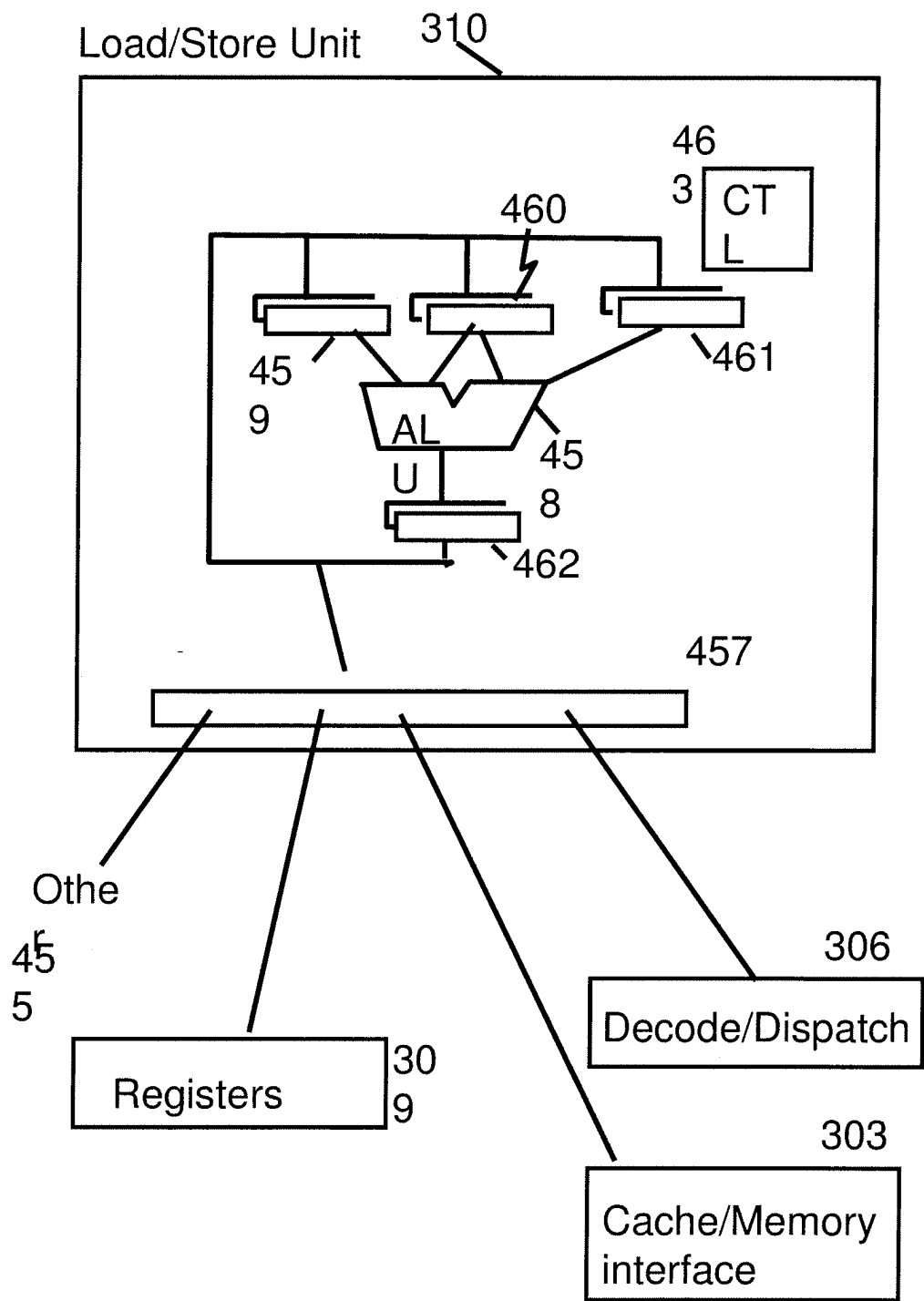


FIG. 4C

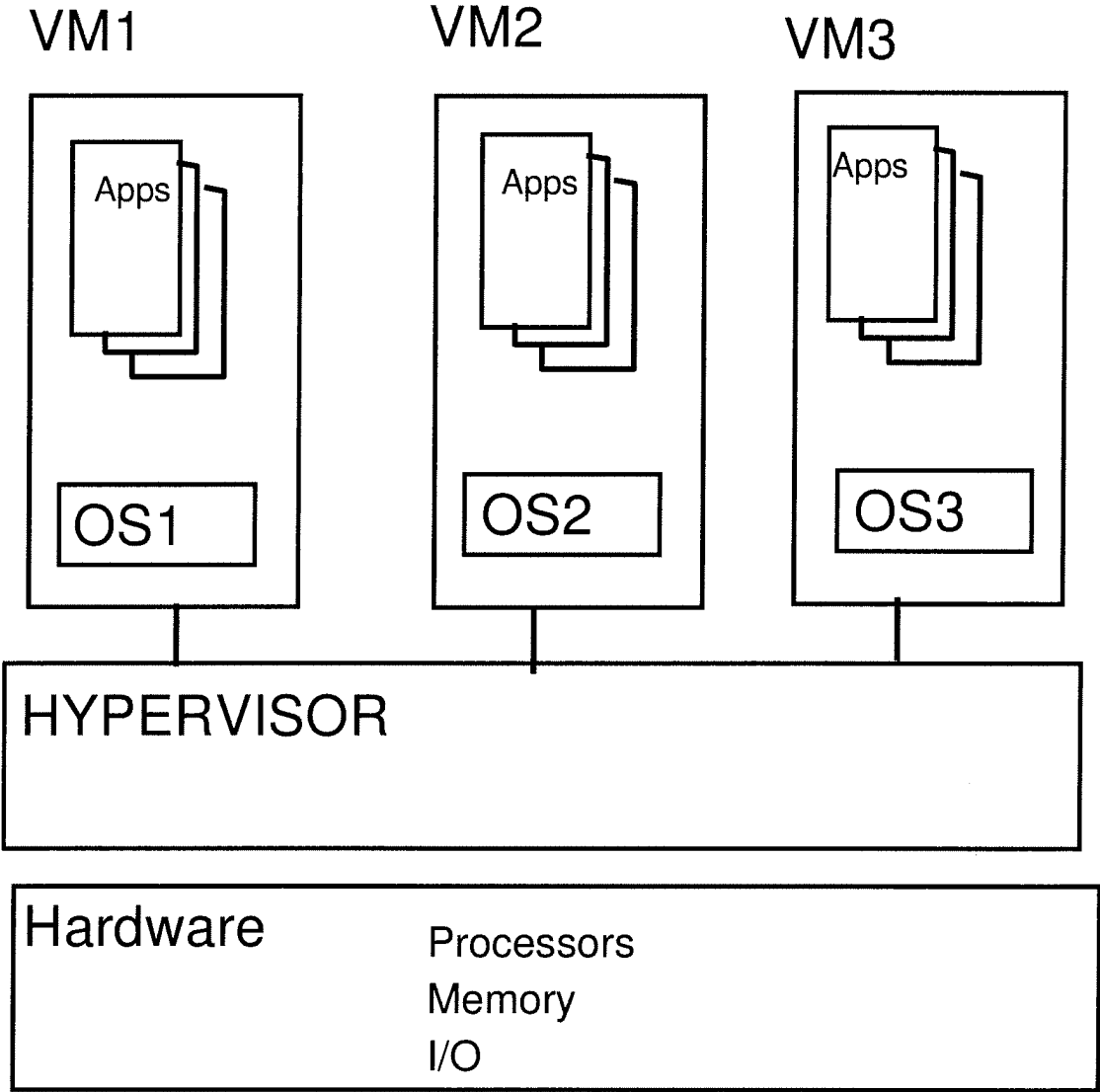


FIG. 5

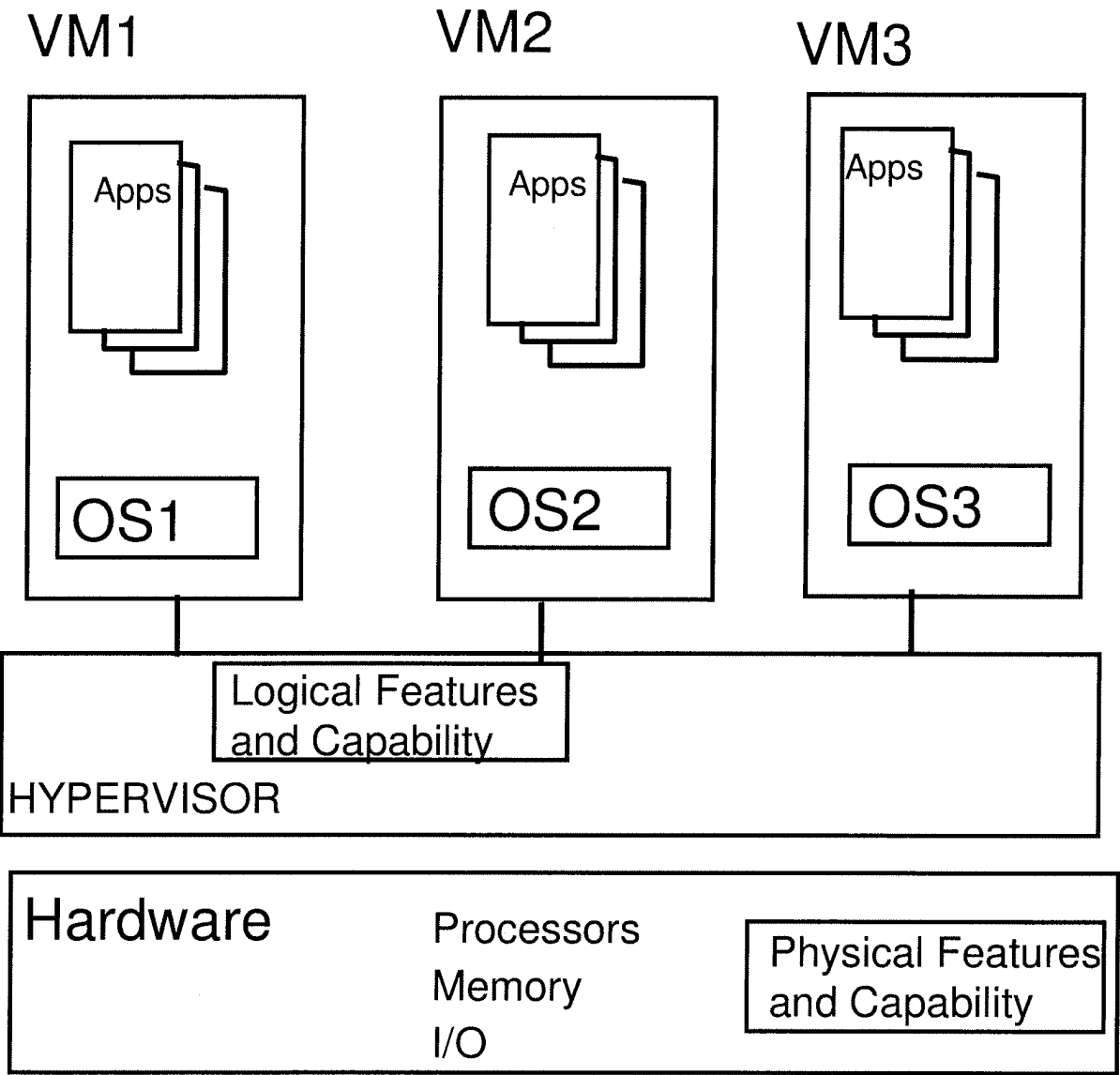


FIG. 6

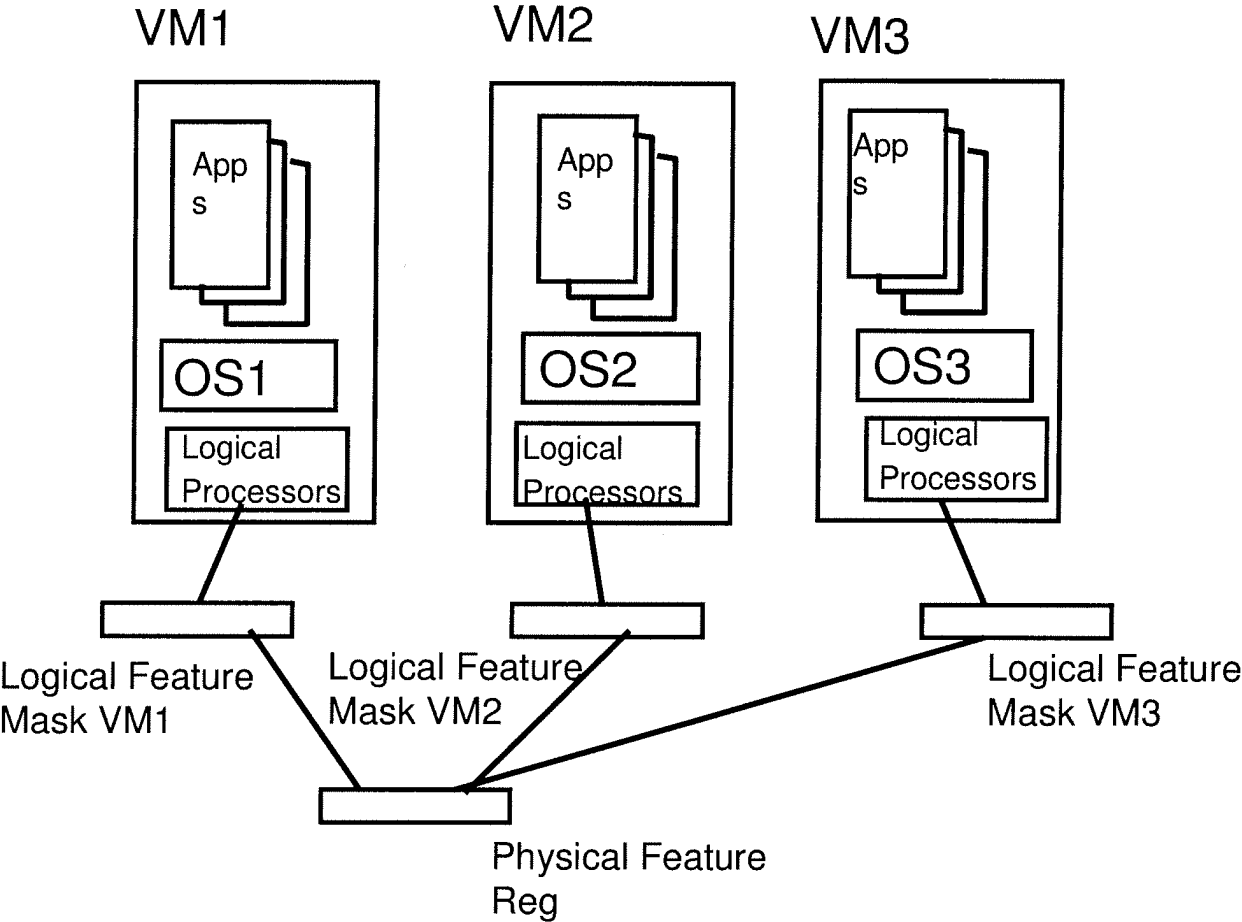
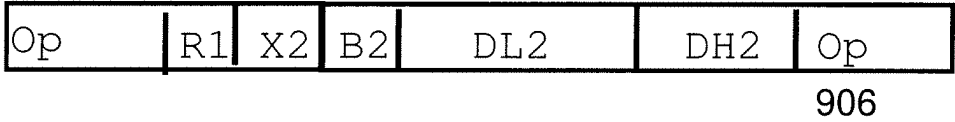
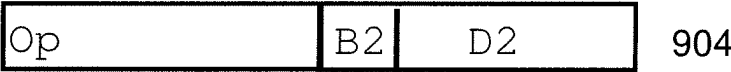
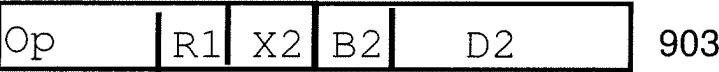


FIG. 7

Example
z/Architecture
Instruction
Formats



----	ML	FCx	
----	ML	FCx	
----	ML	FCx	
----	ML	FCx	
----	ML	FCx	
----	ML	FCx	
----	ML	FCx	
----	ML	FCx	

Opcode Table 907

FIG. 8

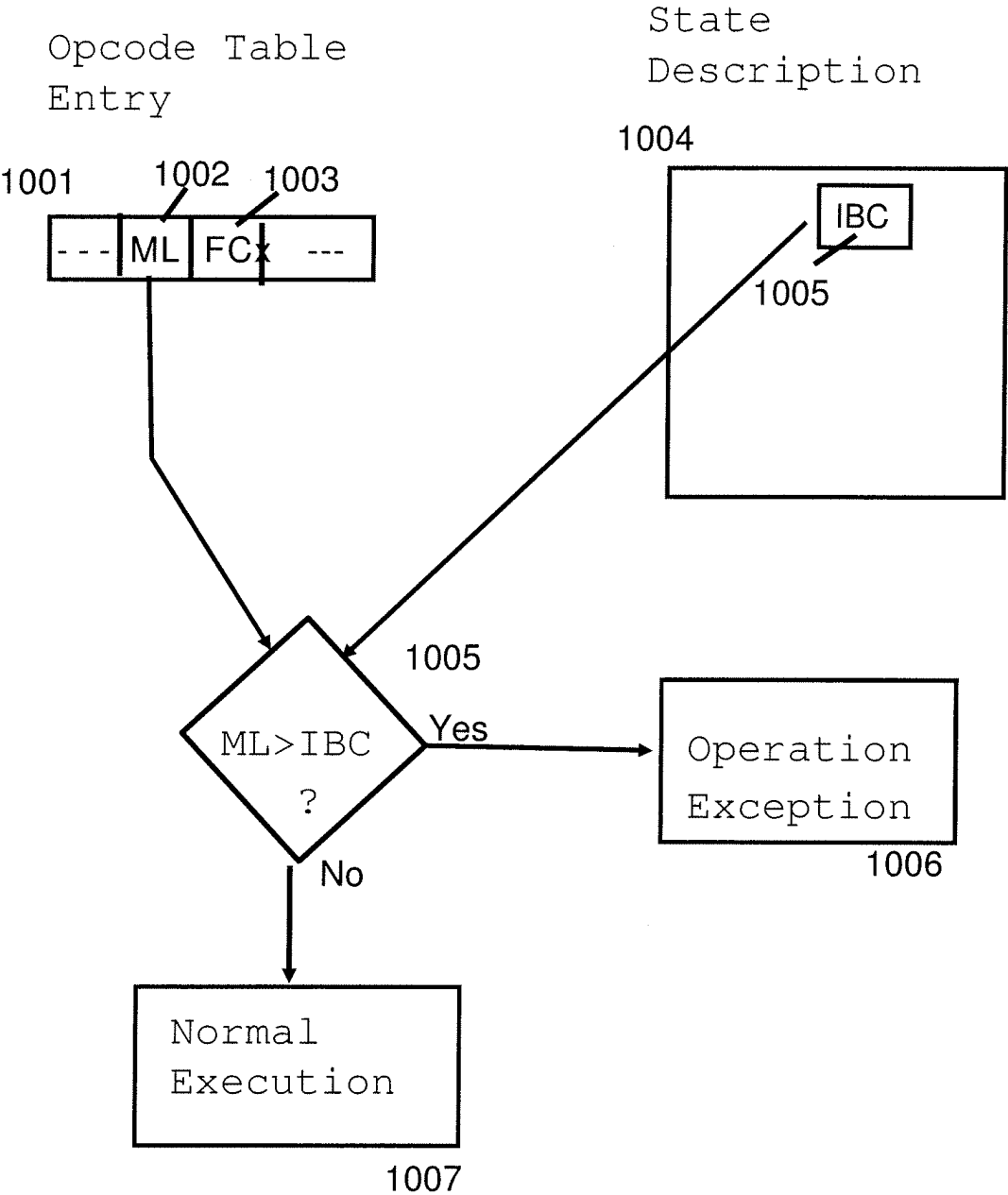
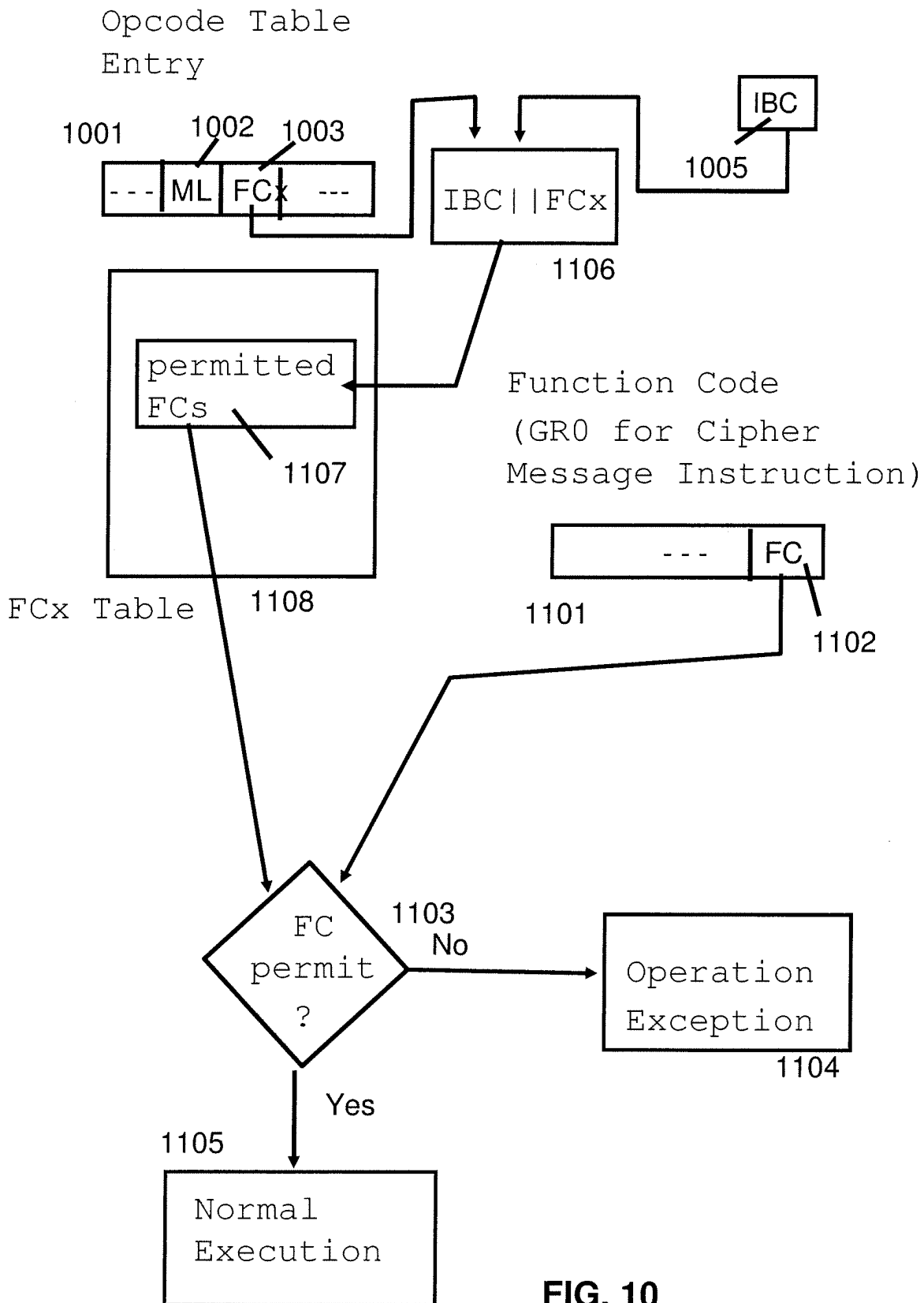


FIG. 9



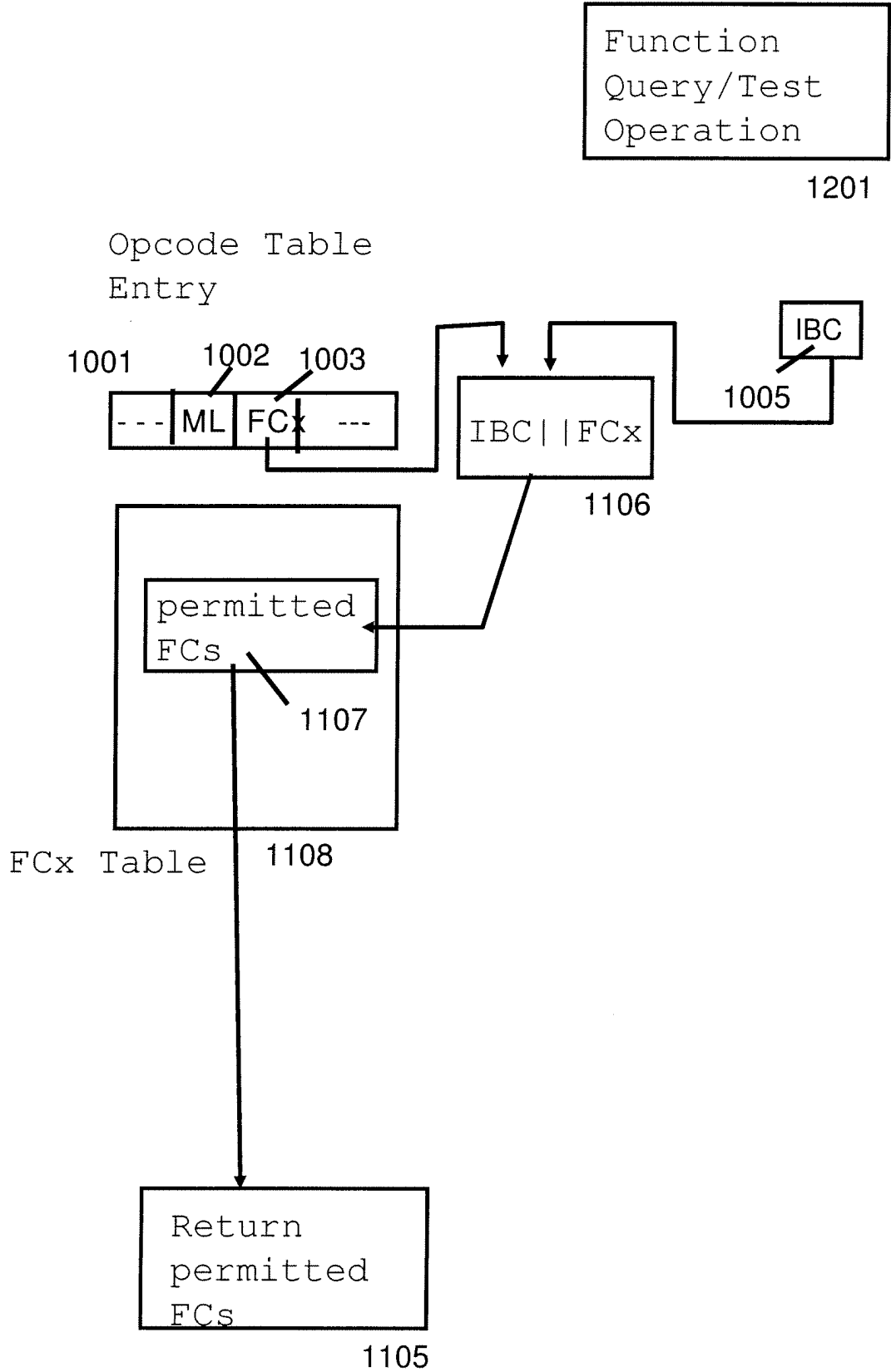
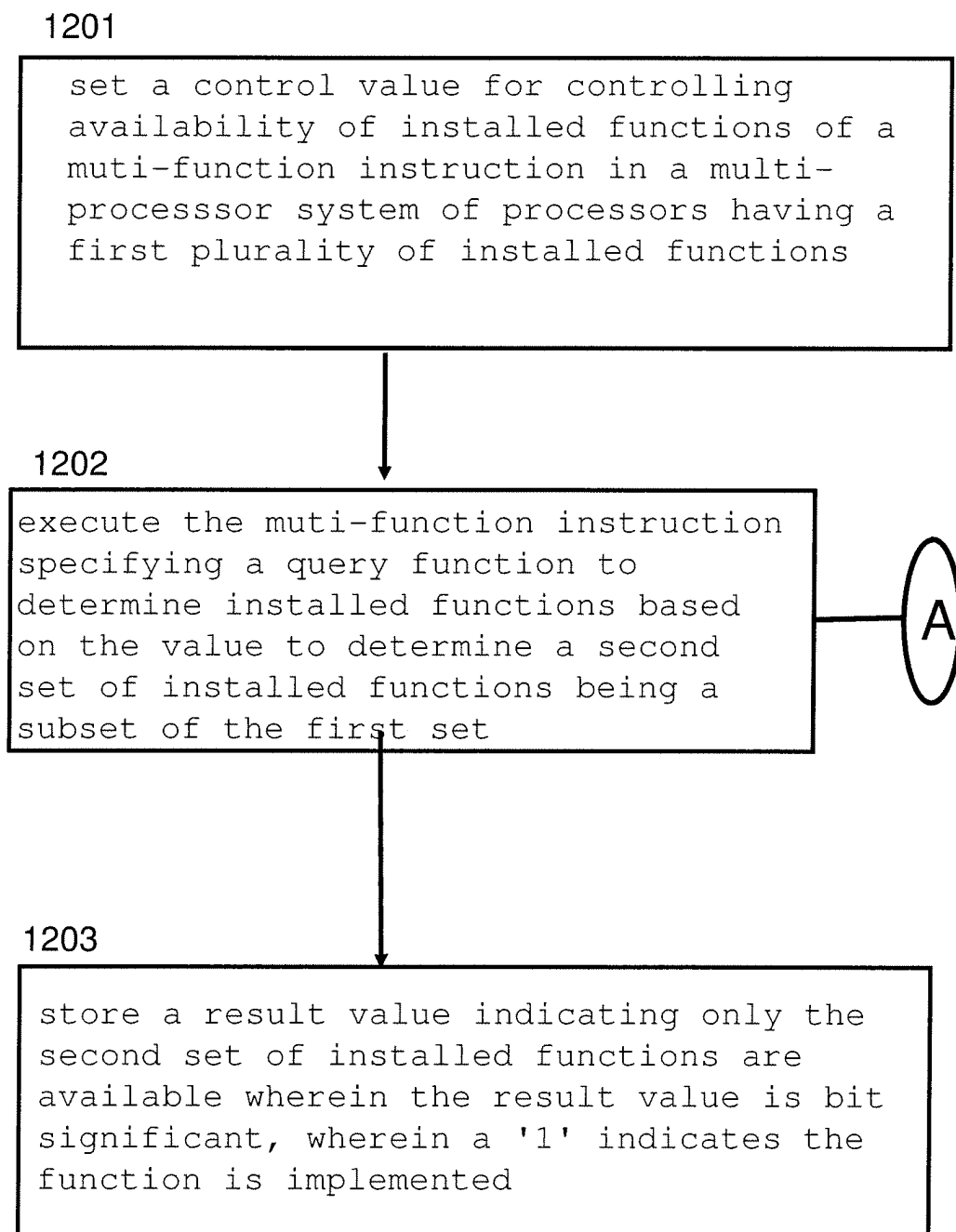


FIG. 11

**FIG. 12**

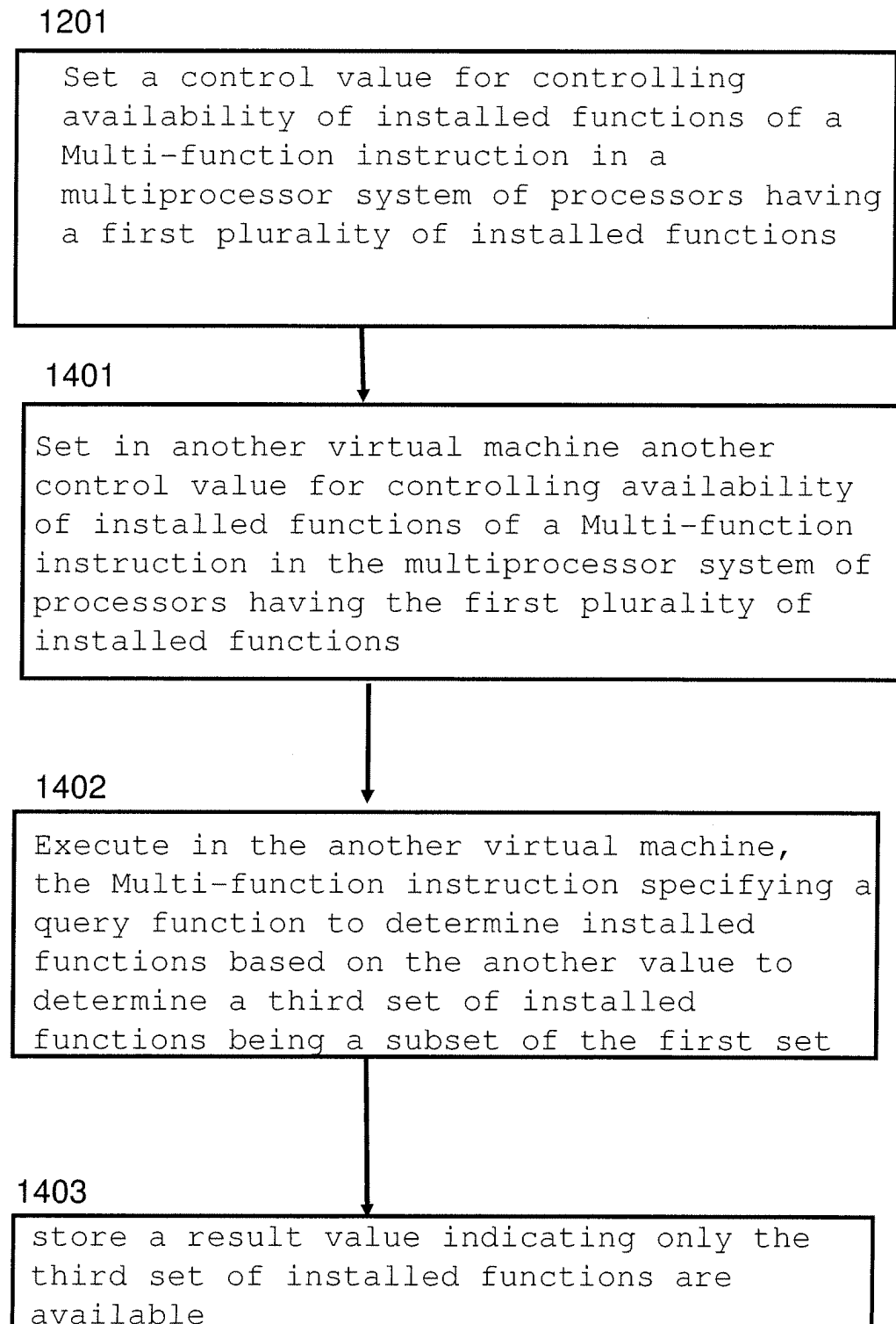
1301

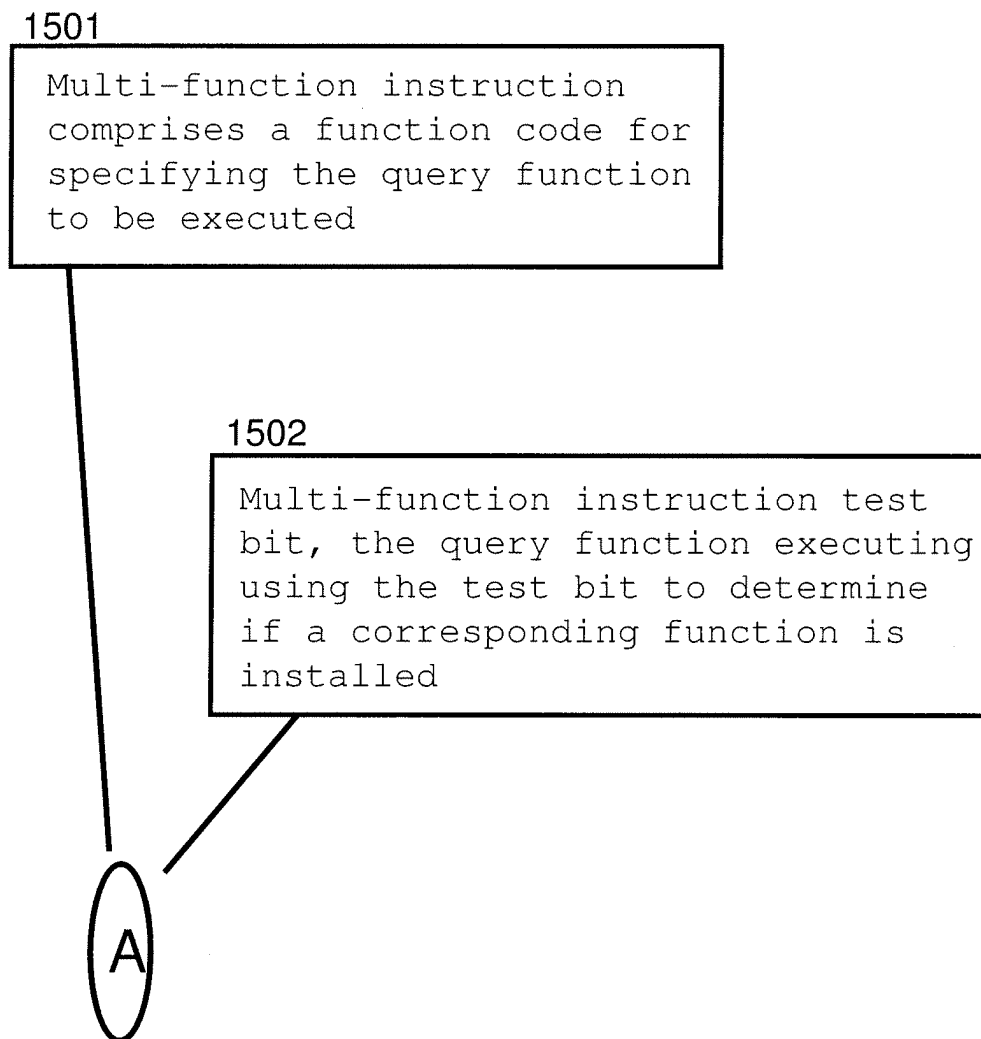
set the control value controlling function of a Multi-function instruction by a HYPERVISOR of a Host for a Virtual Machine having one or more logical processors assigned to one or more physical processors

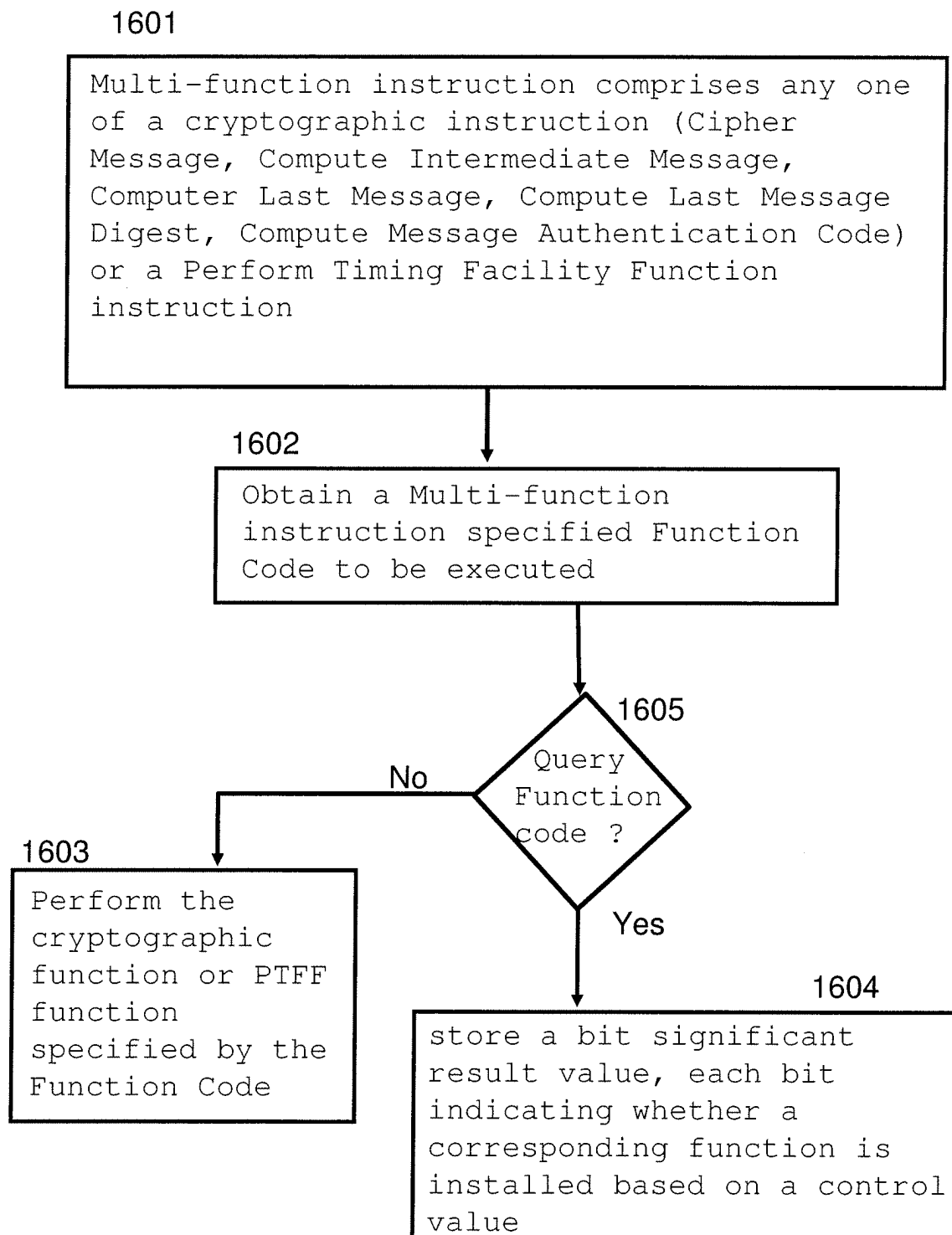
1302

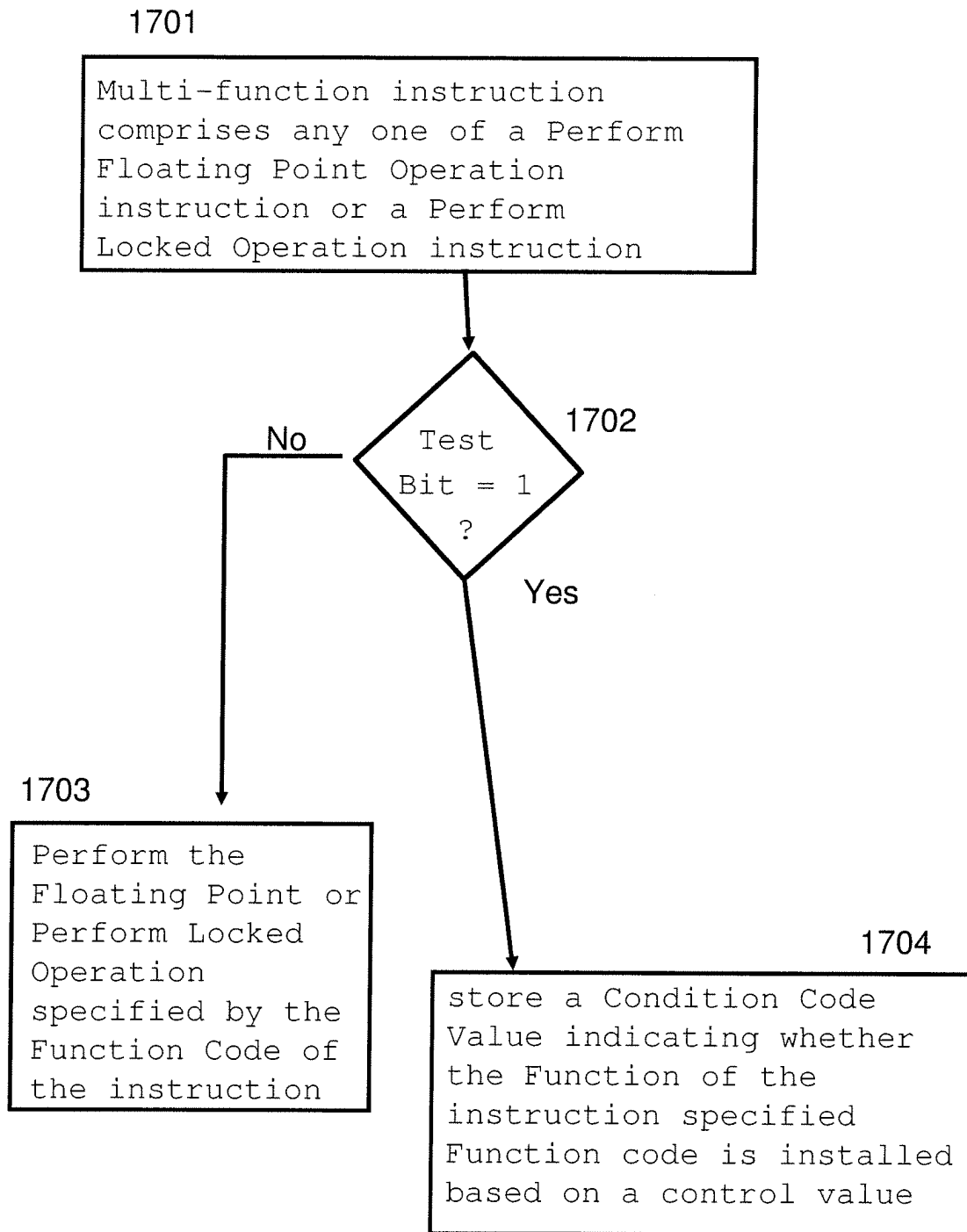
Determine the second installed functions based on the OpCode of the Multi-function instruction according to the control value

FIG. 13

**FIG. 14**

**FIG. 15**

**FIG. 16**

**FIG. 17**