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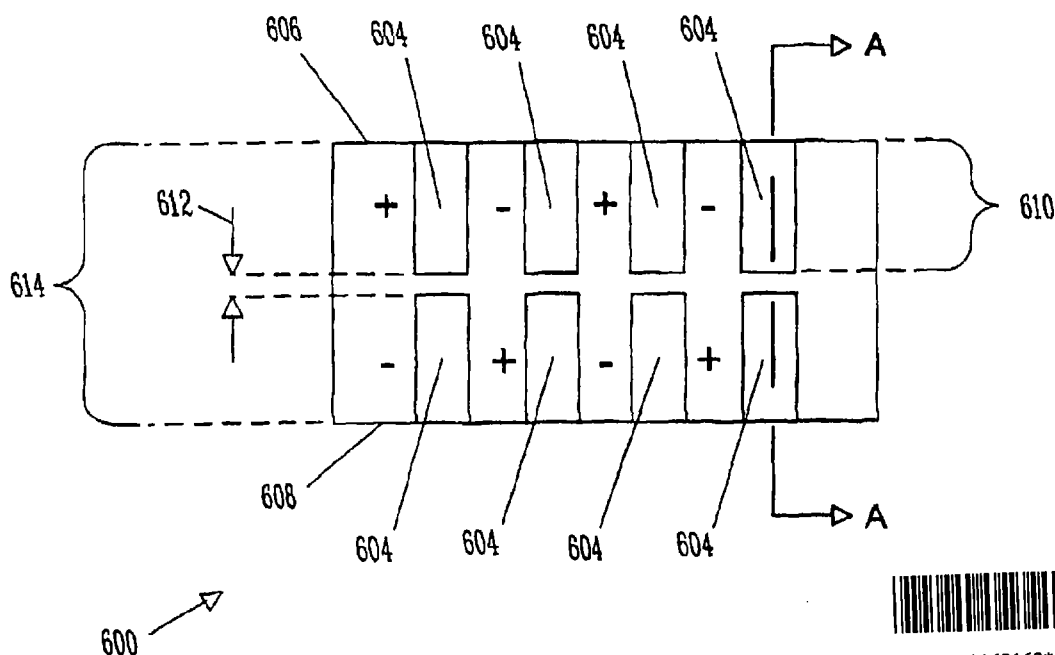
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(54) Title: CAPACITOR WITH EXTENDED SURFACE LANDS AND METHOD OF FABRICATION THEREFOR



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(57) Abstract: A capacitor (Figures 6-9) includes one or more extended surface lands (604, 704, 804, 904, Figures 6-9). In one embodiment, each extended surface land is a land on a top or bottom surface of the capacitor, having a land length that is equal to at least 30% of the width (614, Figure 6) of the capacitor or 20% of the length (914, figure 9) of the capacitor. When embedded within an integrated circuit package (1102, Figure 11), two or more vias (1112) can be electrically connected to the extended surface lands (1108).

WO 02/050852 A3

WO 02/050852 A3



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