

SIGNAL OUTPUT DEVICE, DRIVER CIRCUIT, AND SYSTEM AND METHOD FOR SENDING SIGNALS

ABSTRACT

To obtain high signal integrity at each possible data transfer rate. A signal output device that can set a plurality of different data transfer rates and can output a signal to a signal receiving device at a predetermined data transfer rate via an ATA cable (2). The signal output device is provided with a driver circuit (53) that can set a plurality of different slew rates and can output the above signal to an ATA cable (2) at a predetermined slew rate according to a slew rate control signal SRC and a controller for setting a slew rate of the driver circuit (53) according to the predetermined data transfer rate.

Fig 4.