

DELAYED DETECTION MRC DIVERSITY CIRCUIT

ABSTRACT

A delayed detection MRC diversity circuit of the invention which does not adjust synchronization in every reception branch (1-4) independently and has a simple circuit constitution and superior receiving characteristics. The delayed detection MRC diversity circuit is composed of a comparison circuit (5) for selecting a reception branch with maximum RSSI, a selector (6), a base band circuit such as synchronous circuit (7) for generating a regenerative clock while adjusting bit-synchronization in terms of the reception branch with the maximum RSSI, and a MRC diversity circuit section (15) for composing actually using the regenerative clock.

Fig. 3.