



Abstract

Adjustment and maintenance of a phase relationship between a video signal (on 11) and a scan synchronizing signal (on 67) to assure proper horizontal centering is provided in a horizontal deflection system (40). A first phase locked loop generates a first timing signal (on 17) at a first frequency, synchronously with a horizontal synchronizing component in a video signal. A presettable counting circuit (in 56) operates synchronously with the first timing signal for dividing a clock signal to generate a second timing signal (on 61) at a second frequency. A second phase locked loop (62) generates a scan synchronizing signal (on 67) from the second timing signal. A microprocessor (74) may supply different numbers to a register (in 56), the output of the register being coupled to the presettable counting circuit. Different numbers (from 74) change the relative phase between the first and second timing signals by incremental steps. The microprocessor (74) monitors a video source selection switch to gate one of alternative video sources as a video and synchronizing signal output and adjusts the relative phase between the horizontal synchronizing component of the selected video source and a synchronous timing signal by a factor appropriate for the selected video source. A manually operable circuit (72) may be coupled in a feedback path of the second phase locked loop (62) for adjusting the relative phase between the second timing signal (on 61) and the scan synchronizing signal (on 67), over a range corresponding to an incremental step.

Reference Indicia Are From Fig. 1