ABSTRACT OF THE DISCLOSURE

A fast data transfer system capable of eliminating the need to extremely increase the necessary transmission frequency band for the high-density recording and fast revolution rate of data recording and reproducing system has a recording and reproduced signal processor including an encoder/decoder, a disk enclosure including a parallel-to-serial converter, a record head, and a serial-to-parallel converter, and a transmission cable, and makes recording parallel data transfer from the encoder/decoder to a recording amplifier by shifting the phases of a plurality of recording signals and reproduced parallel data from the serial-to-parallel converter to the encoder/decoder by shifting the phases of a plurality of reproduced signals. The necessary transmission frequency on the transmission cable can be reduced by this parallel data transfer on the transmission cable. In addition, a record timing correction circuit can be provided on a parallel data transfer path.

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