SYSTEM AND METHOD FOR FLICKER DC OFFSET DETECTION AND CORRECTION

ABSTRACT

The present disclosure provides a method for detecting flicker DC voltage offset. The method includes receiving an output signal of an image sensor, the output signal comprising a reference signal and an image signal, and generating a combined signal by combining the image signal with a peak flicker DC voltage signal during a first time period. The method also includes performing an auto zero function in an auto zeroing comparator during the first time period between the reference signal and the combined signal and comparing the reference signal and the image signal with the auto zeroing comparator during a second time period subsequent to the first time period. The method further include storing a first charge corresponding to the image signal during a second time period and storing a second charge corresponding to a current peak flicker DC voltage signal. The method also includes comparing the image signal to the current peak flicker DC voltage signal, and producing the greater of the image signal and the stored peak flicker DC voltage signal as the peak flicker DC voltage signal.

FIG. 1