

FIG. 1A

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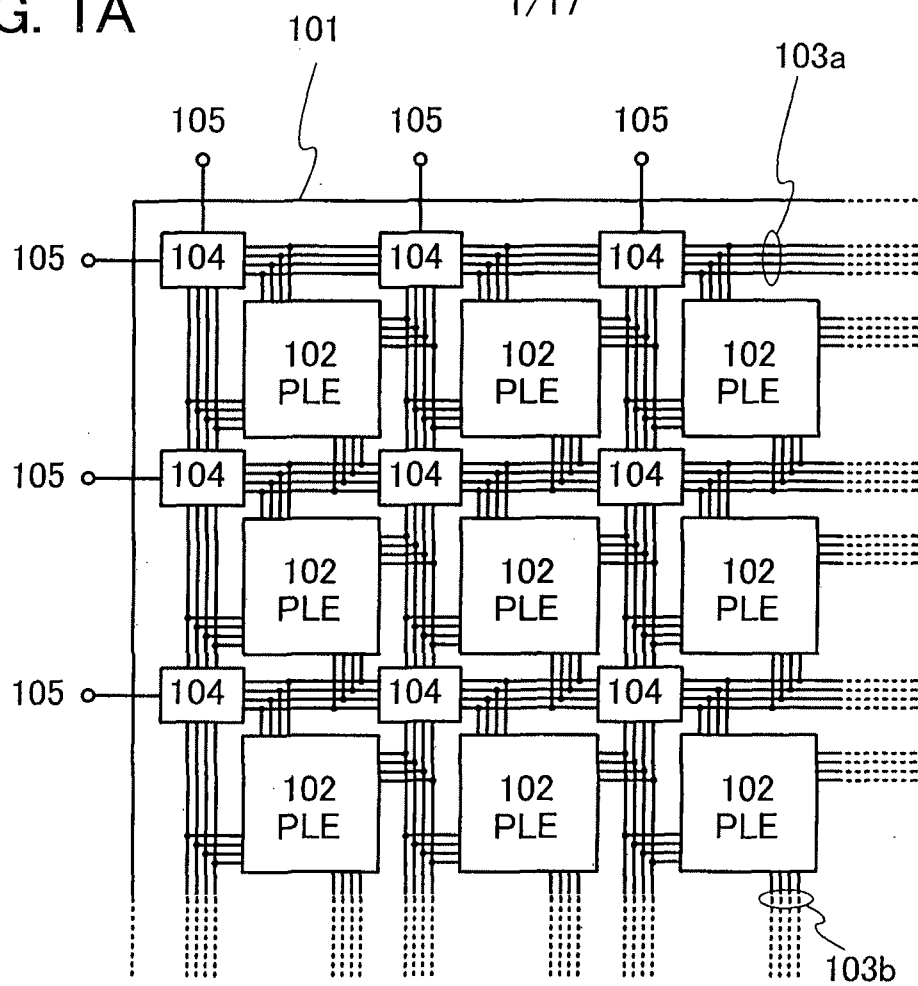


FIG. 1B

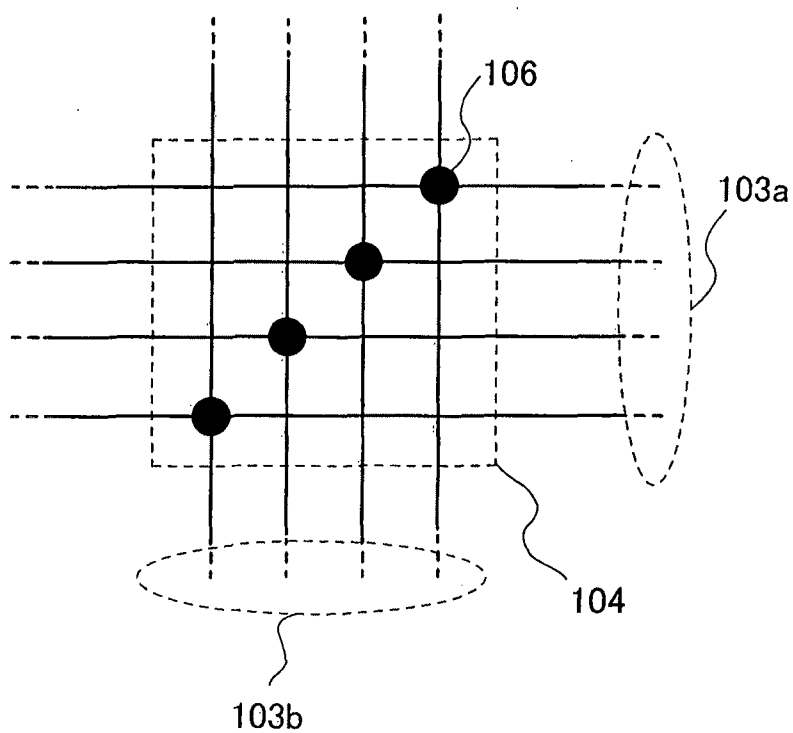


FIG. 2

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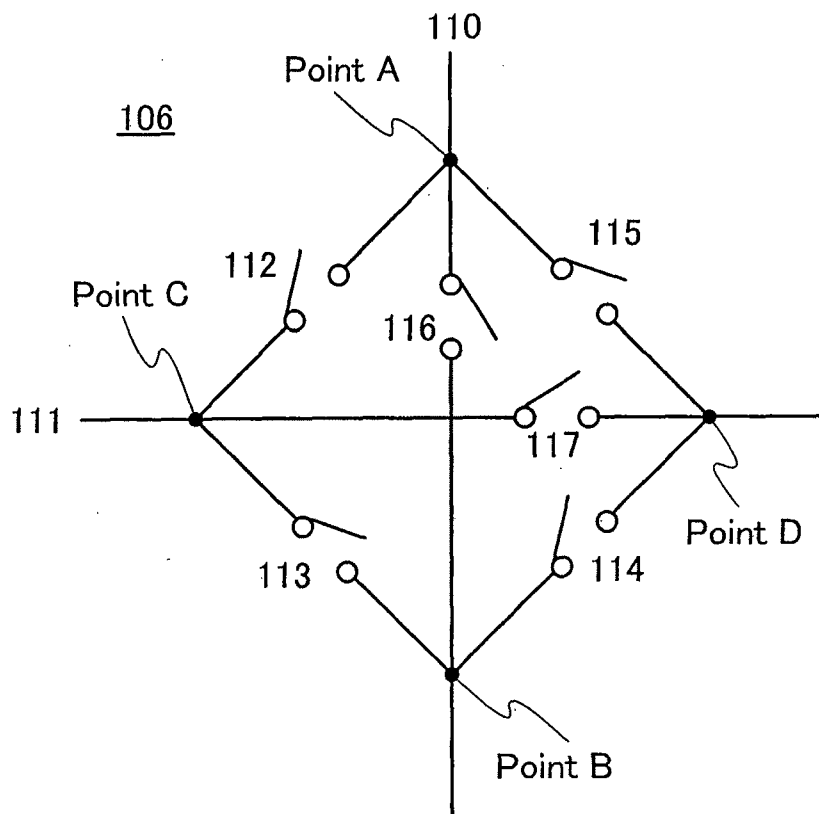
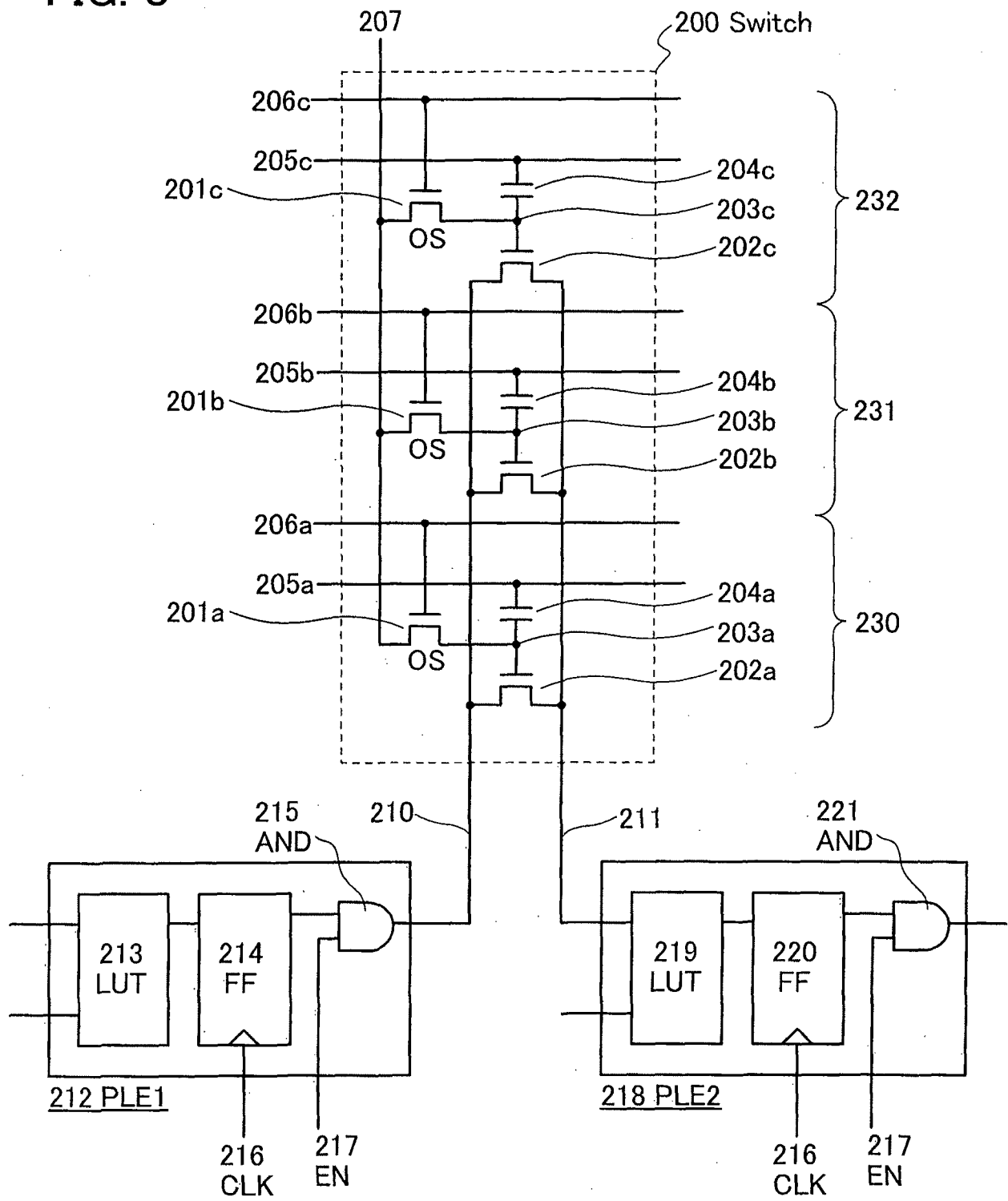


FIG. 3



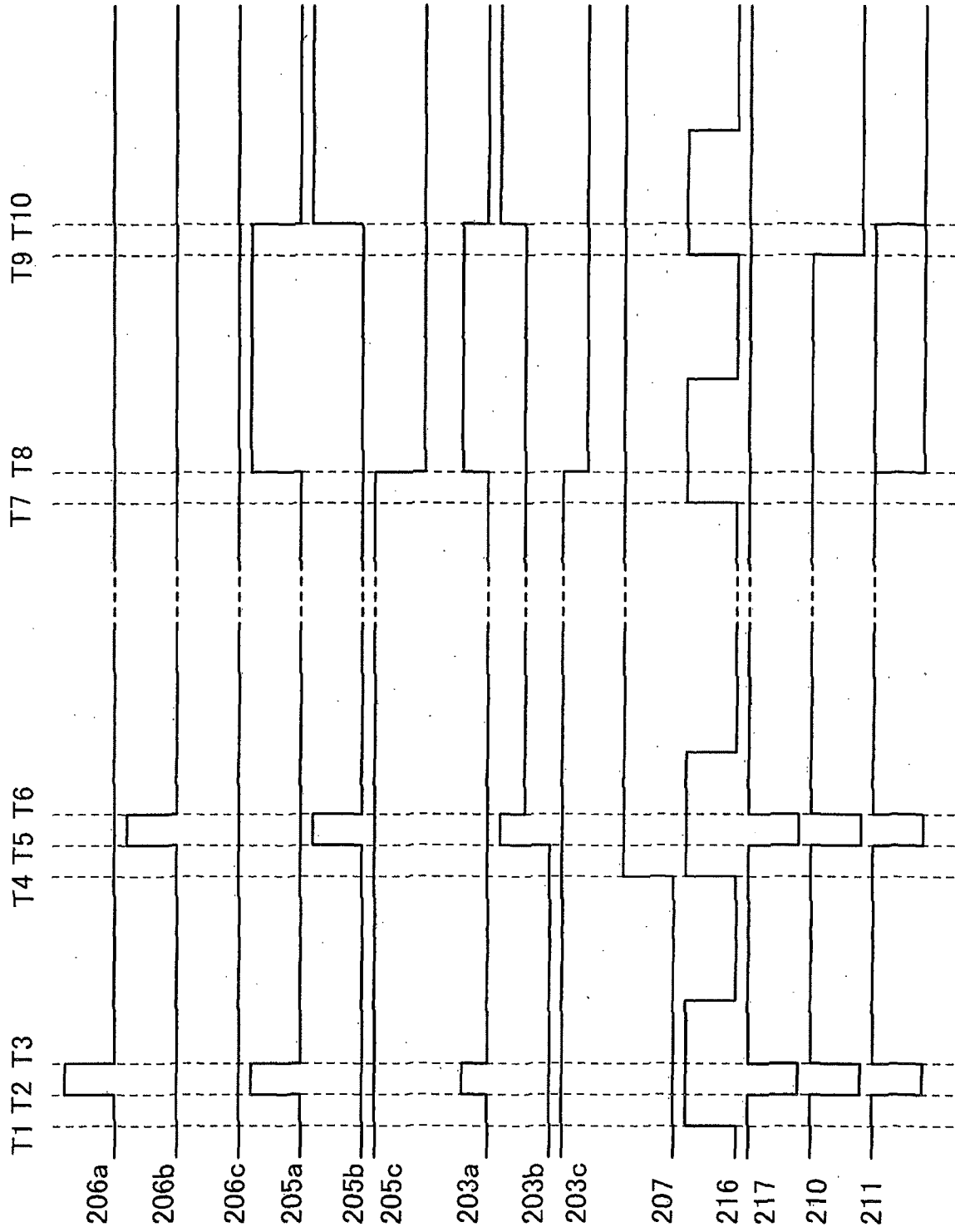


FIG. 5

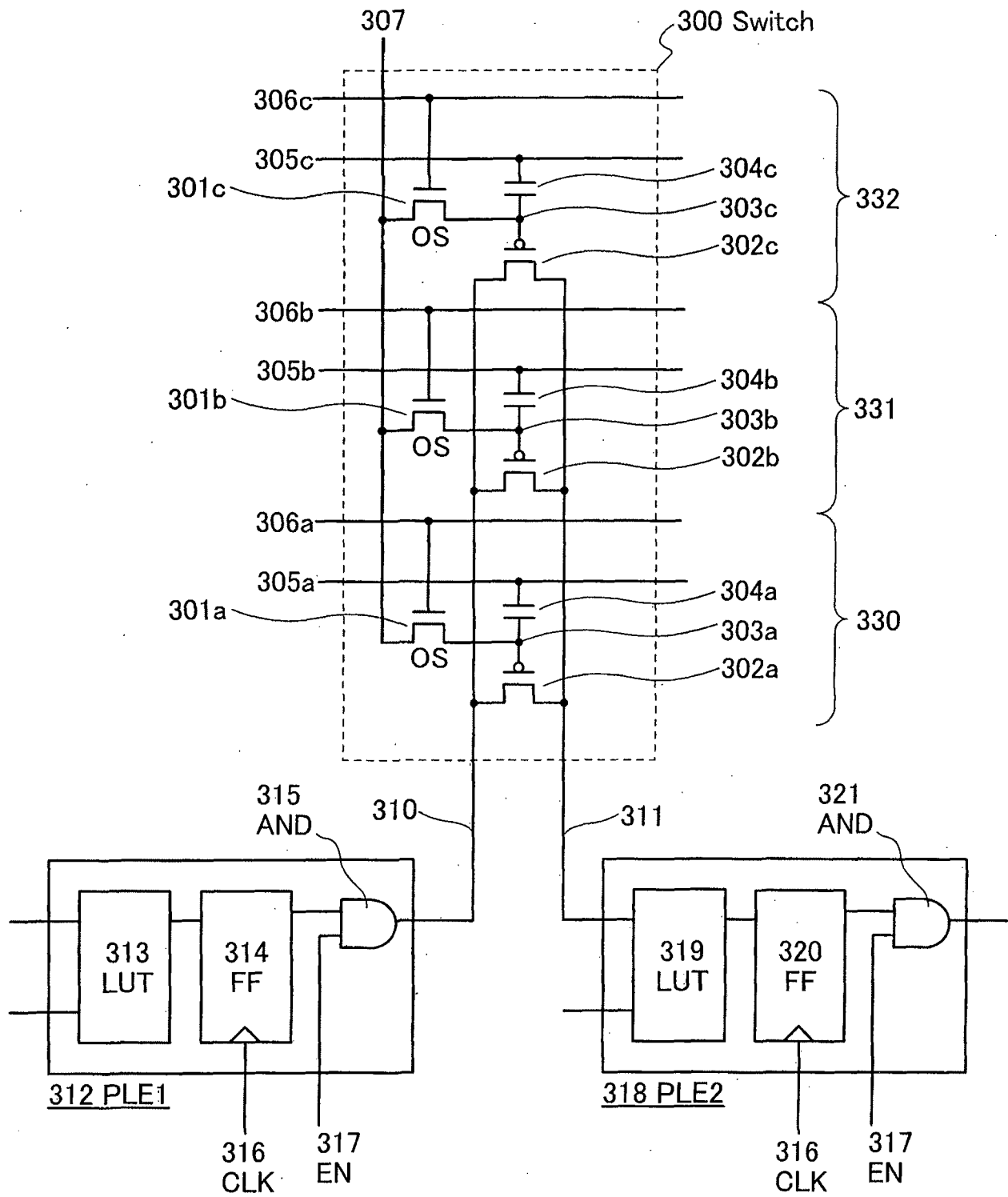


FIG. 6

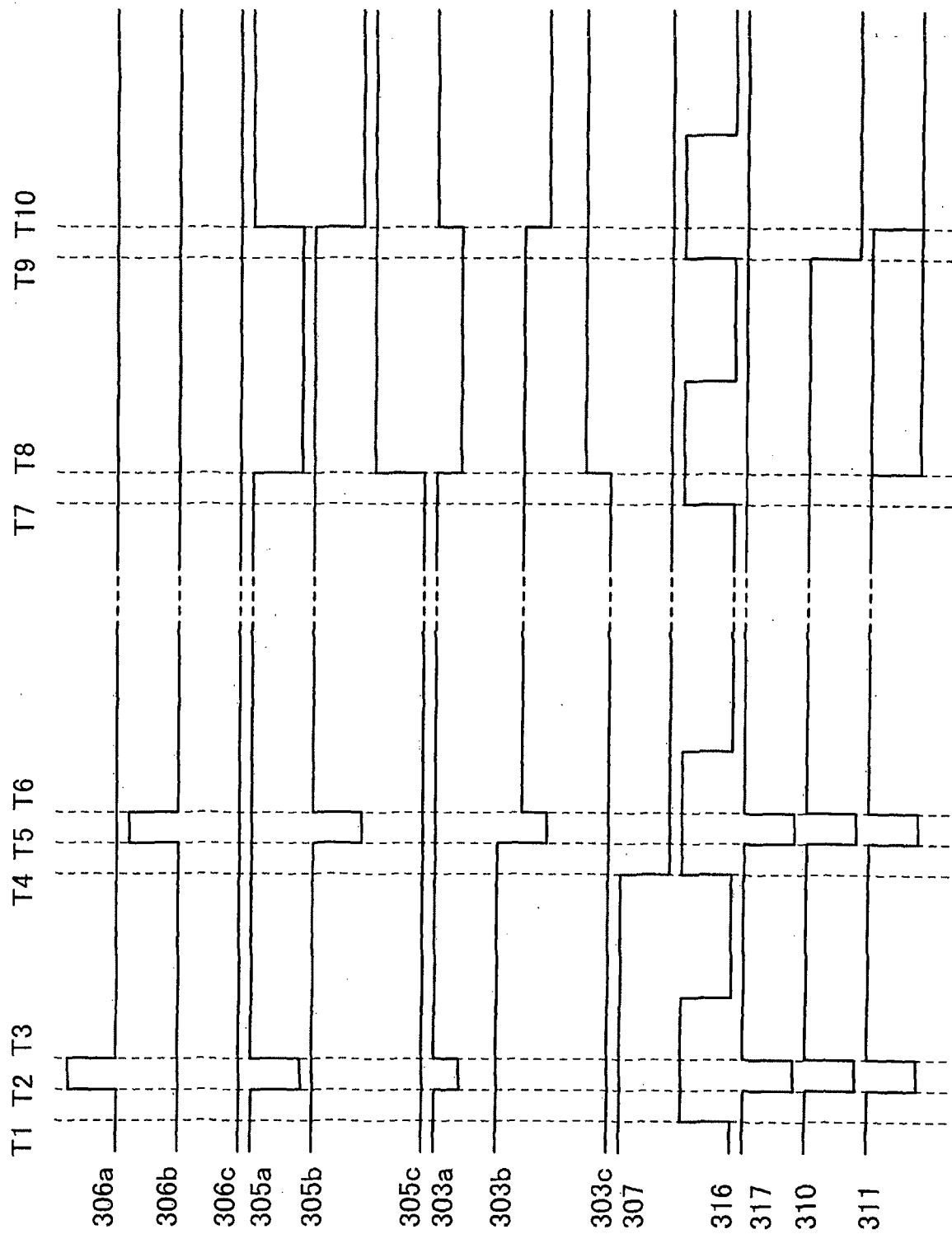


FIG. 7

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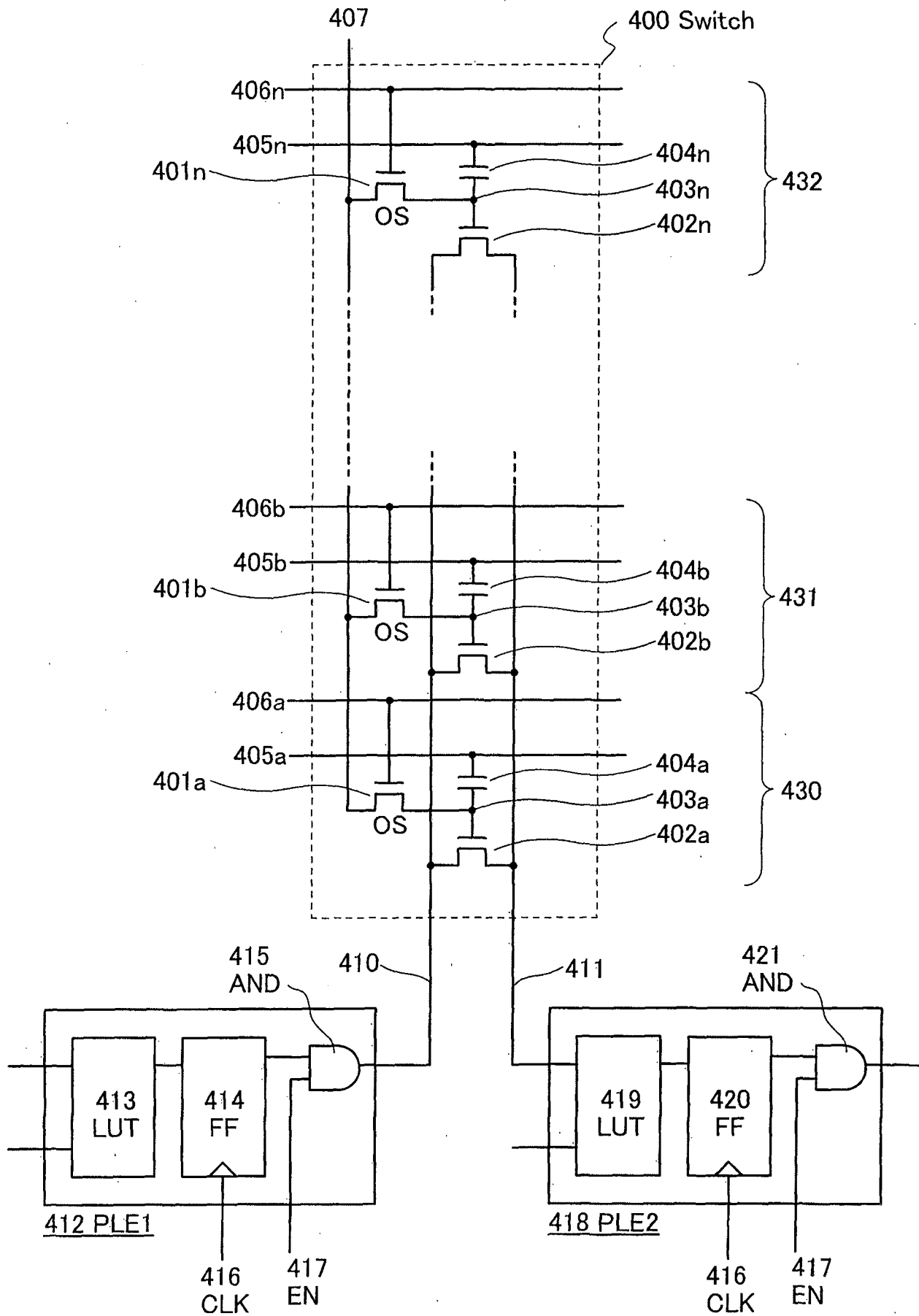


FIG. 8

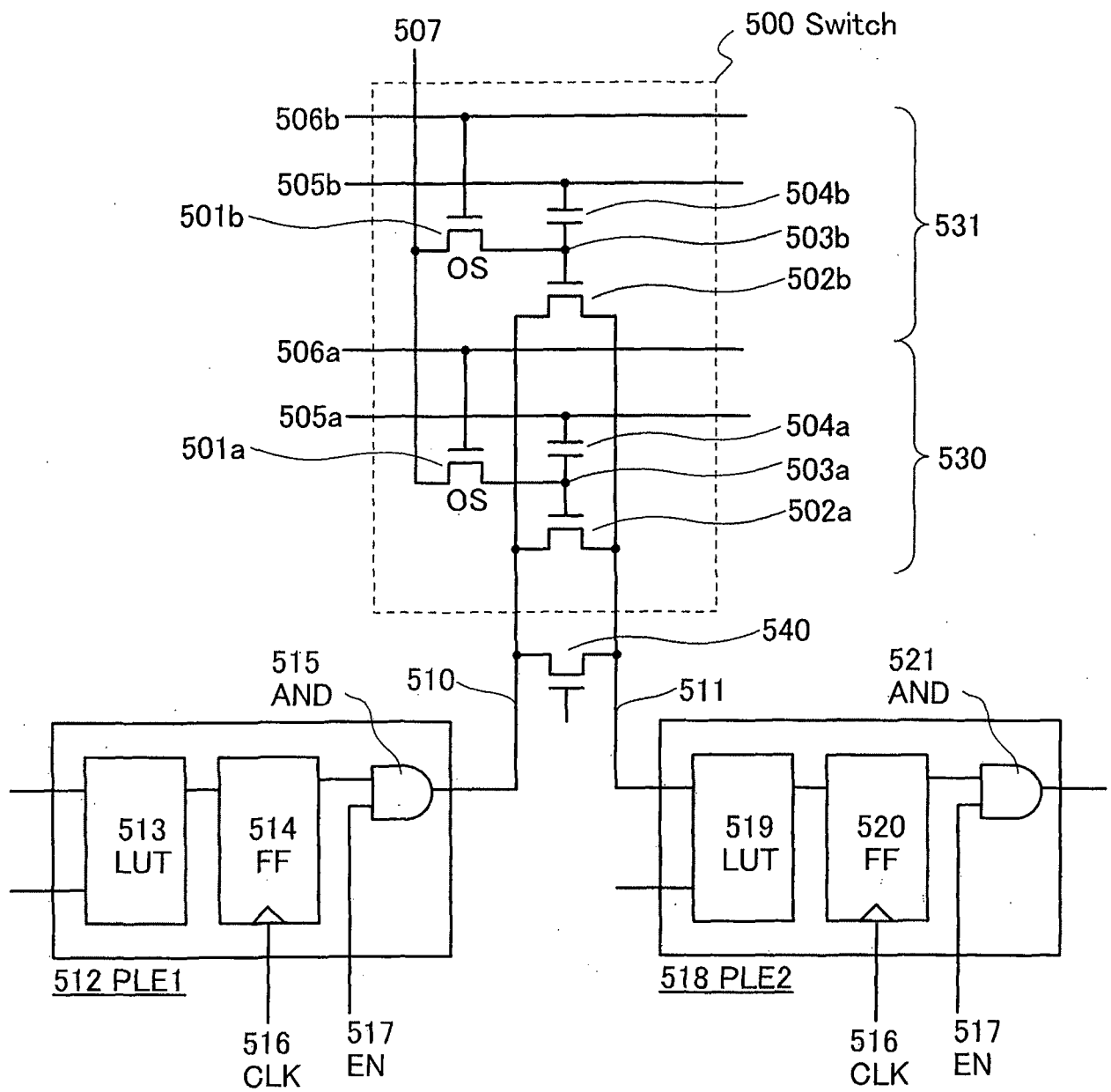


FIG. 9

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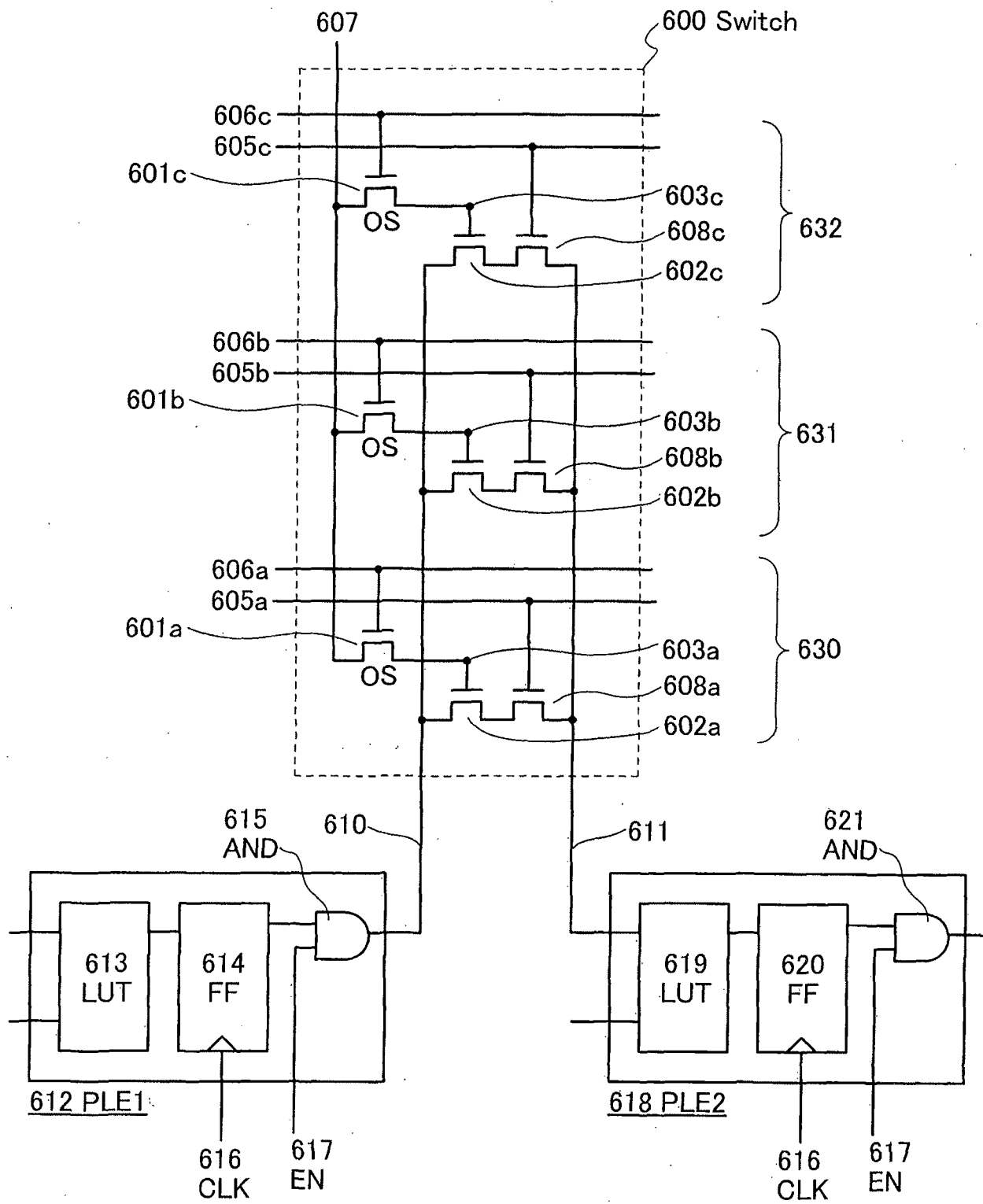


FIG. 10

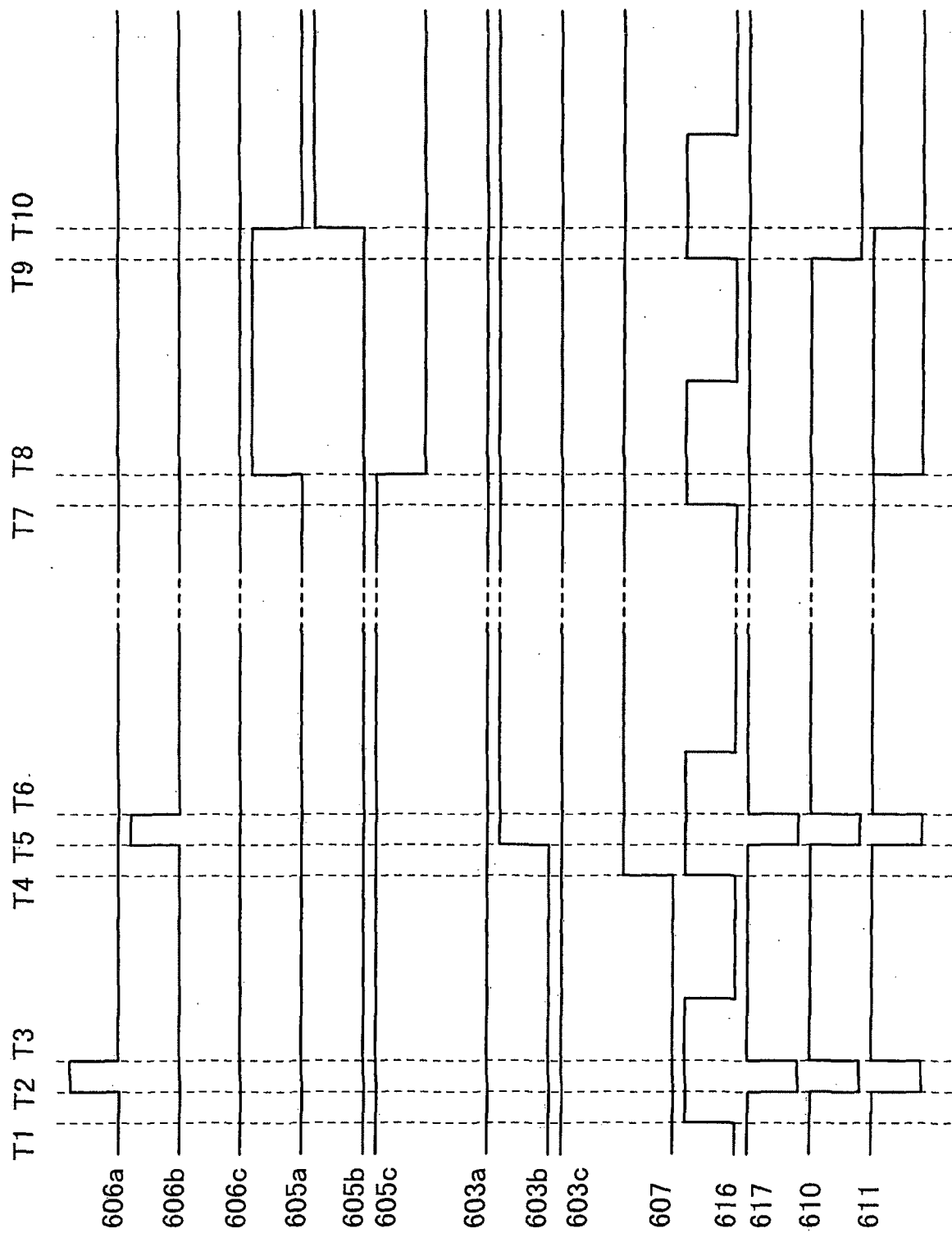


FIG. 11

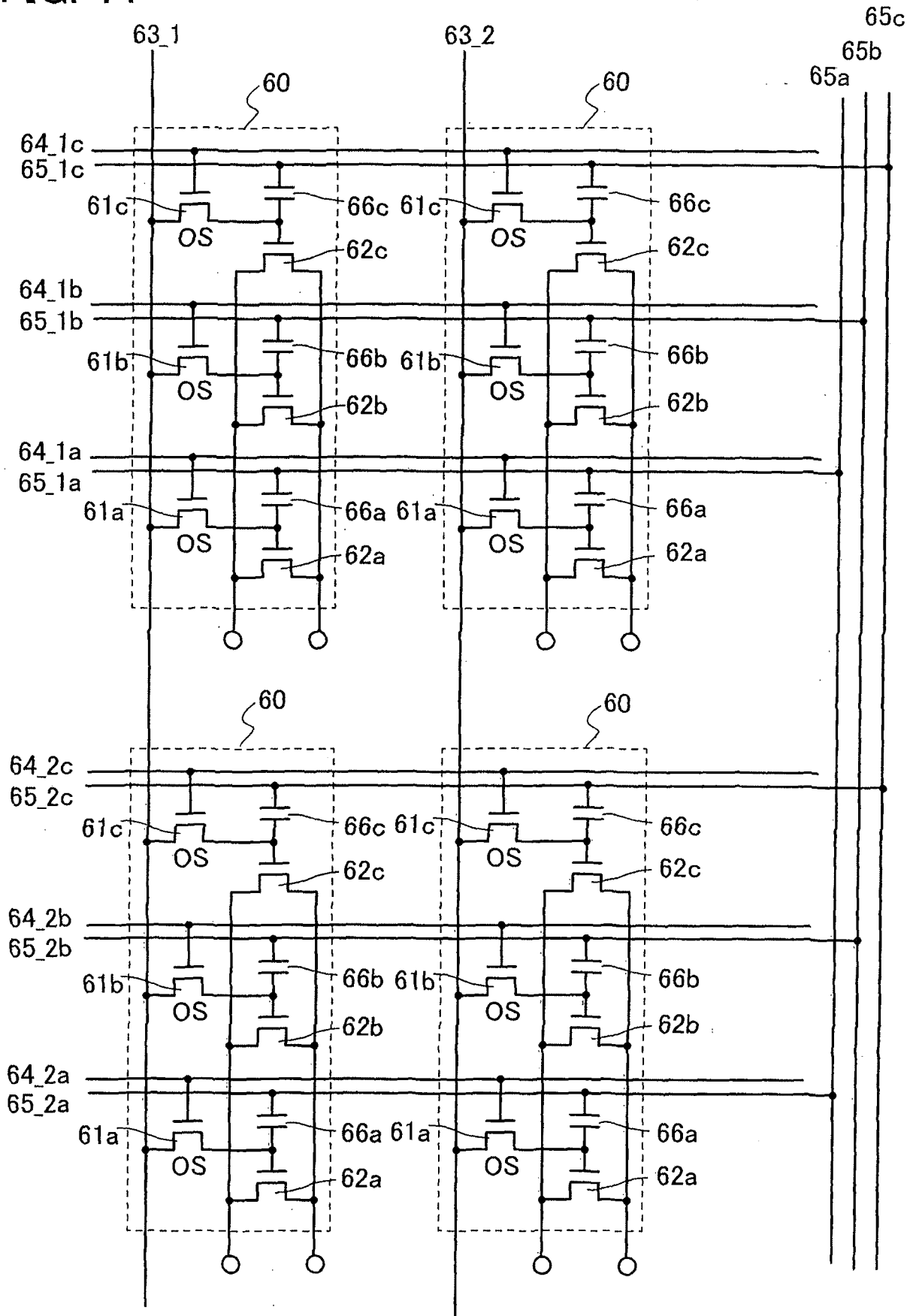


FIG. 12A

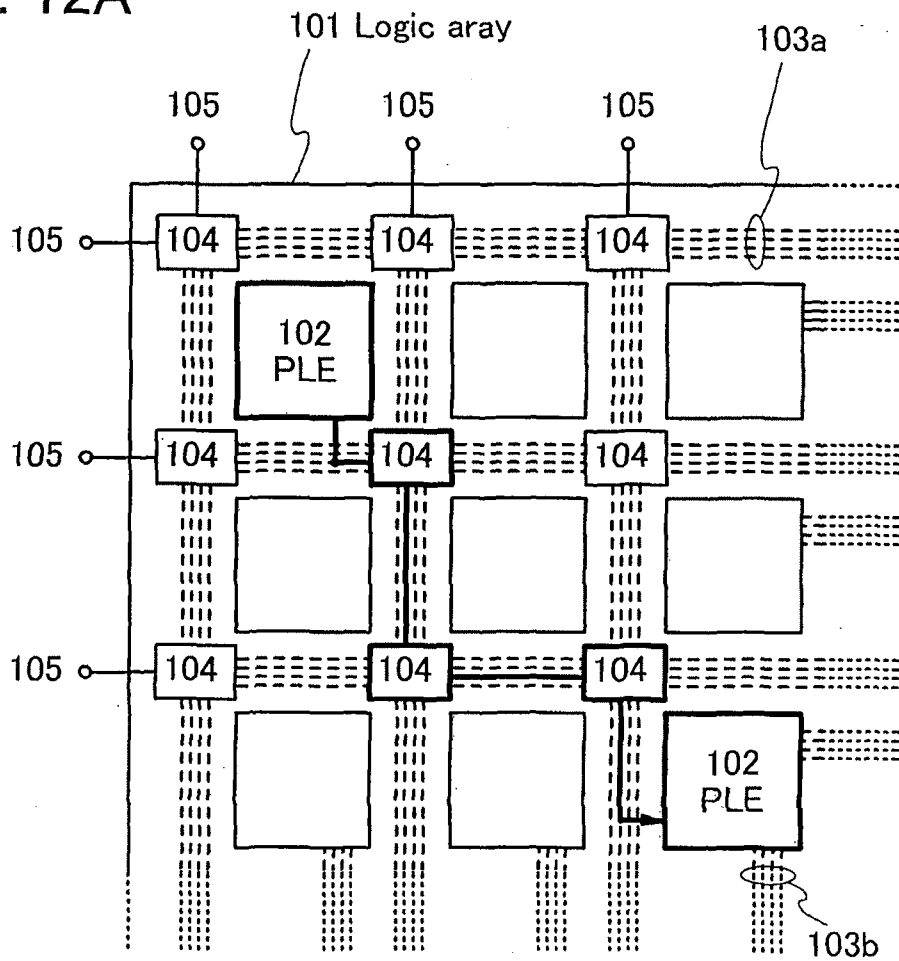


FIG. 12B

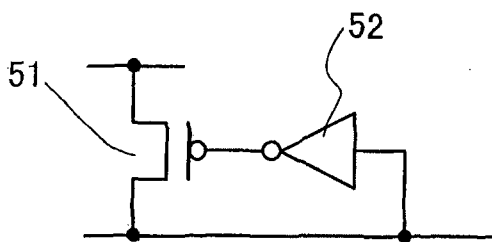


FIG. 12C

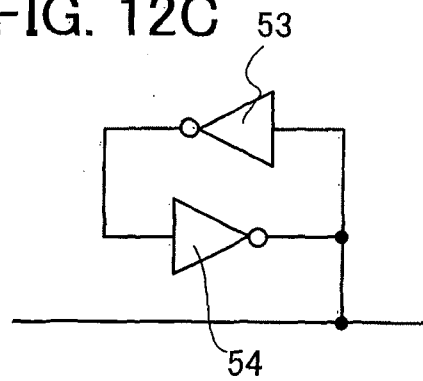


FIG. 13A

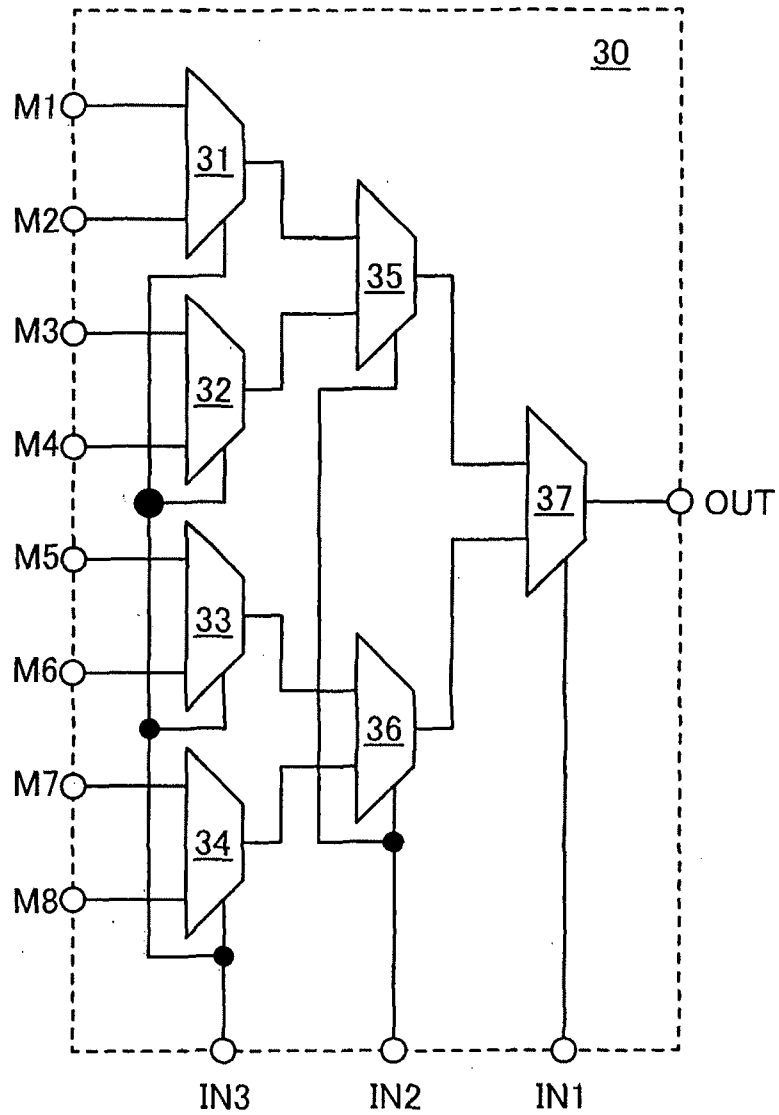


FIG. 13B

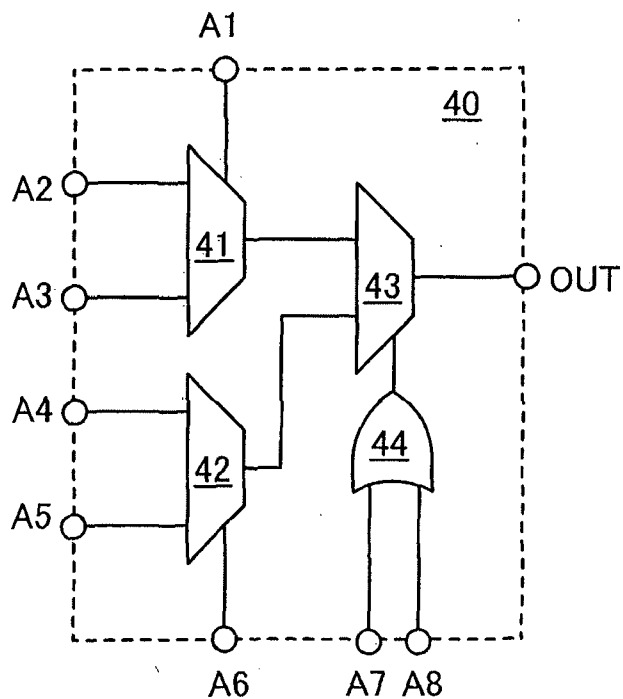


FIG. 13C



FIG. 14

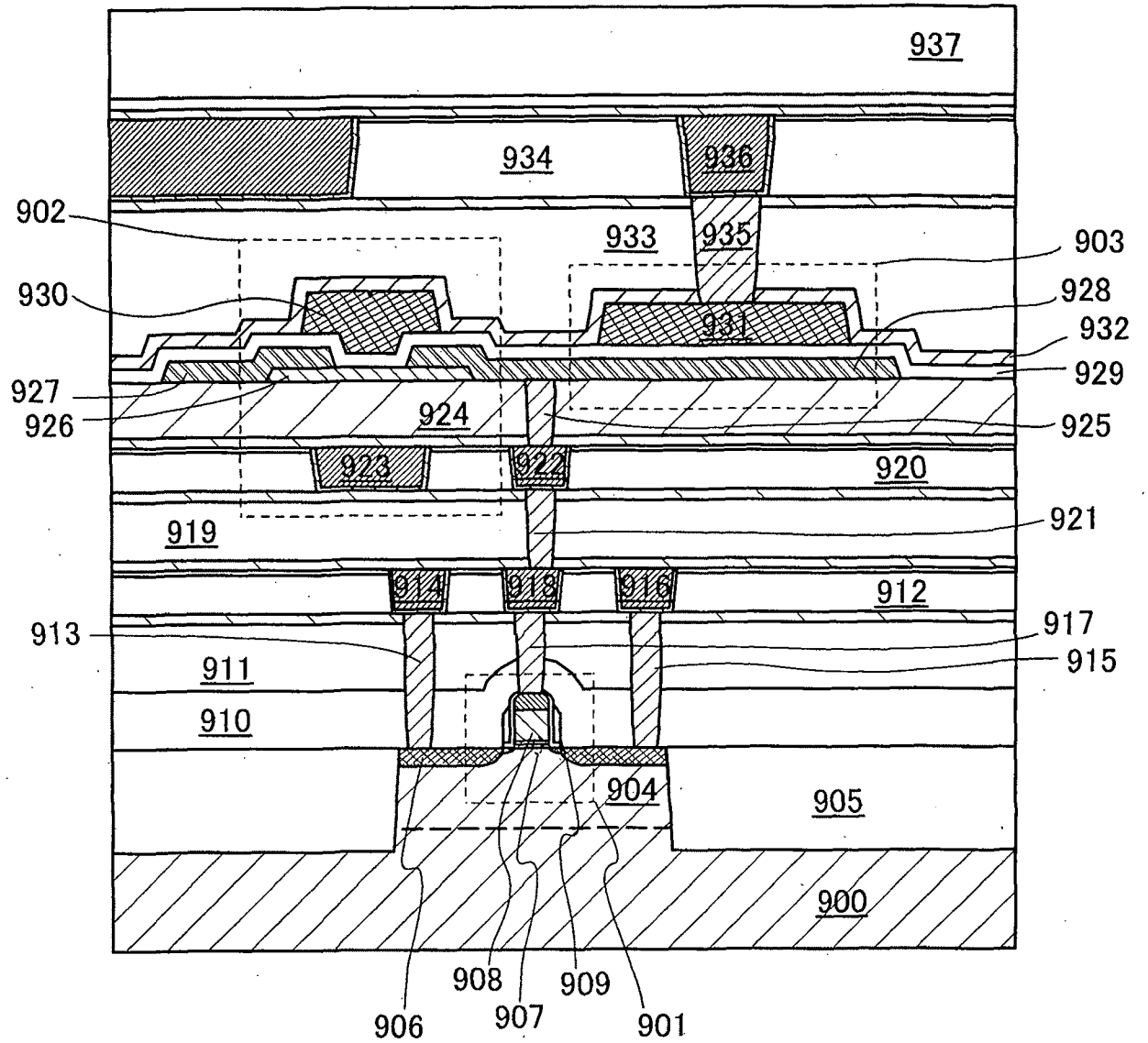


FIG. 15A

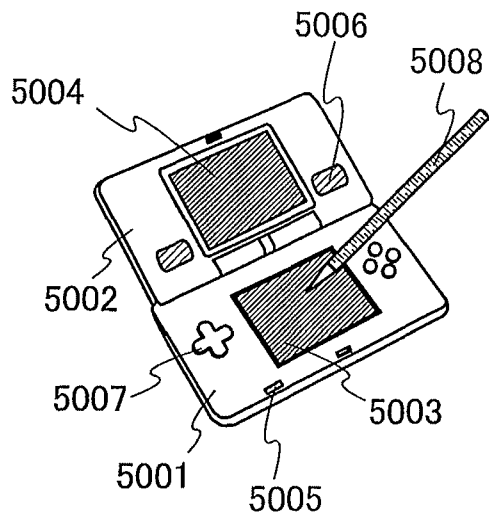


FIG. 15B

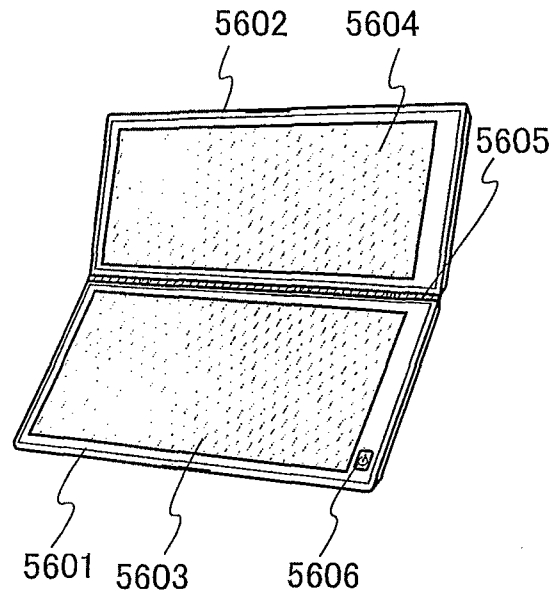


FIG. 15C

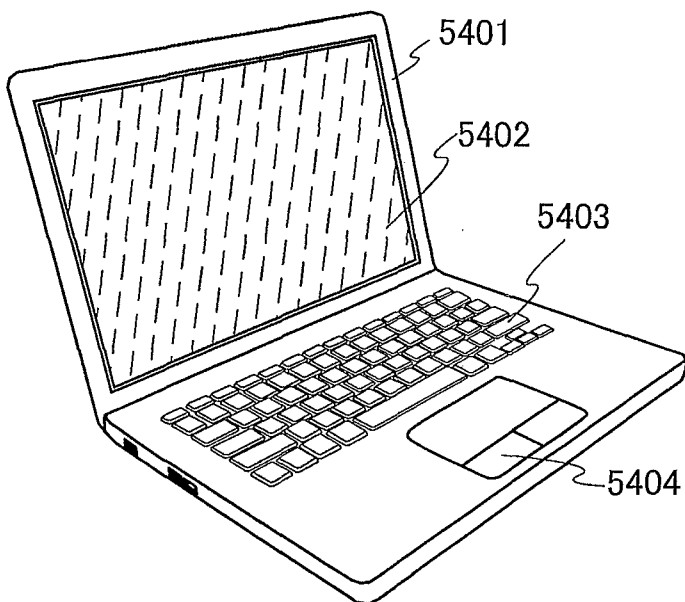


FIG. 15D

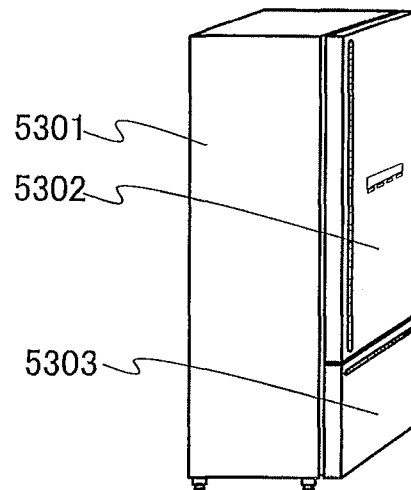


FIG. 15E

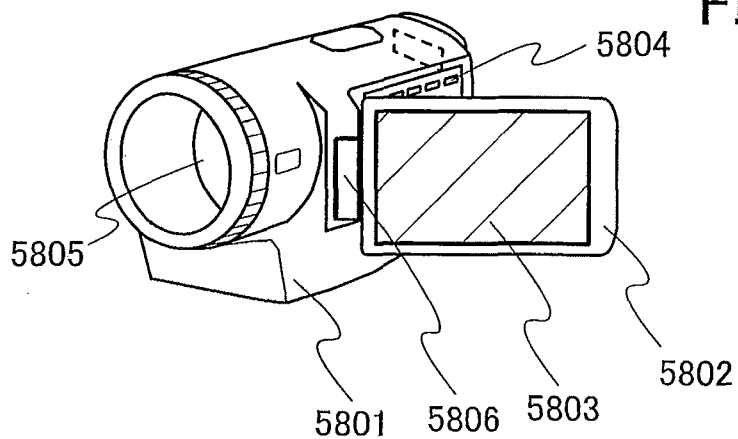


FIG. 15F

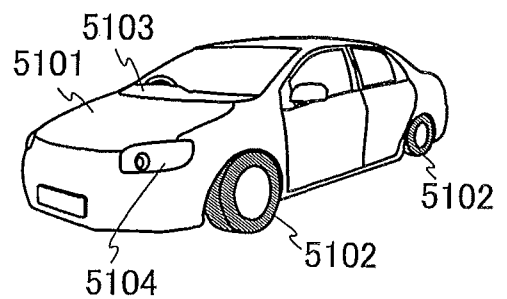


FIG. 16

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	720a	720b
Circuit diagram		
Configuration data "1"		
Configuration data "0"		

FIG. 17

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