

REDUNDANT MEMORY ARRAY

ABSTRACT

A set associative cache memory array (302,...,305) includes redundant memory portions (311) for use in the case of a defective portion of the memory. Information is stored within the defective portion of the memory array and an identical copy is stored within the redundant portion (311). Additionally, reading of the information is done from both the defective portion and the redundant portion (311). Selection of the information from either the defective portion or the redundant portion (311) is made using programmable circuitry such as a fuse.

Fig. 3

This Sheet Has Been Replaced

