

DATA SLICER WITH HOLD

Abstract of the Disclosure

- 5 A data slicer including a comparator and a clamping circuit has been provided wherein the clamping circuit functions to clamp a signal appearing at a first input of the comparator to a predetermined voltage swing. The first input of the comparator is coupled through a capacitive element to receive an input signal.
- 10 The clamping circuit includes a first diode being coupled between the first input of the comparator and a first voltage. The clamping circuit also includes a transistor having its current carrying electrodes coupled between a first supply voltage terminal and the first input of the comparator. The control electrode of the
- 15 transistor is coupled to receive a second voltage. The second input of the comparator is coupled to receive a bias voltage which is substantially equal to the midpoint of the first and second voltages. The data slicer further includes a hold circuit for disabling the clamping circuit.

FIG. 1 accompnies this abstract.