

Abstract

1. Process and circuit for regeneration of the binary bit stream from a ternary signal.
- 2.1 In the application P 40 41 717, an improvement in
5 the signal-to-noise ratio is achieved in the case of a bit-stream regenerator by a circuit which is simplified in comparison with Viterbi detection. The object of the present invention is to make use of this circuit possible in the case of a PR4 signal
10 and to increase the transmission reliability.
- 2.2 By demultiplexing the sampled values of the ternary PR4 signal, there can be formed two value sequences which correspond to signals whose pulses are alternately positively and negatively directed with
15 respect to a centre line. The two sampled-value sequences are processed in time-division multiplex in a circuit corresponding to the earlier application (Figs. 9 and 10).
- 2.3 In particular for bit regeneration in a digital
20 video recorder.
3. Fig. 9.