CLAIMS

1. A system for generating an analog signal having at least:

a set of at least two electric current generators

at least one capacitor

activation/deactivation devices for said electric current generators;

the electric current generators being connected in parallel with one another and the capacitor being connected in series with the electric current generators, the activation/deactivation devices controlling said generators by means of a digital stream allowing control of the intensity of the electric current entering said capacitor and generating a trapezoidal voltage signal at the terminals of said capacitor, said analog signal being reconstructed through interpolation of said trapezoidal signal.

2. The system of claim 1, wherein said interpolation is of Riemann type.

3. The system of claim 1, further comprising:

at least one transistor connected in parallel with the capacitor and/or

at least one other electric current generator connected in series with the capacitor

and a comparator for comparing a value of a voltage at the terminals of said capacitor with a theoretical value;

said transistor and/or said other electric current generator being adapted to modify said value of the voltage at the terminals of said capacitor when it is different than said theoretical value.

4. The system of claim 1, the system being integrated on one and the same chip.

5. The system of claim 1, wherein said capacitor is made up of a capacitor of a transistor belonging to the input stage of a power amplifier.

6. The system of claim 1, the system being integrated on an integrated circuit.

7. The system of claim 1, the system being integrated on an application-specific integrated circuit.