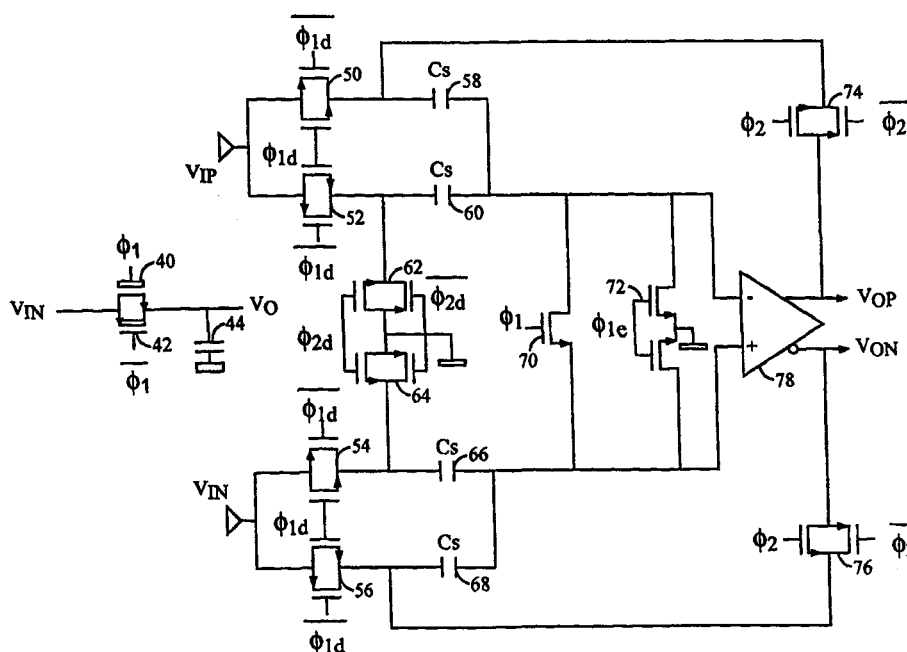




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁷ : G11C 27/02	A1	(11) International Publication Number: WO 00/42616 (43) International Publication Date: 20 July 2000 (20.07.00)
(21) International Application Number: PCT/US00/00665 (22) International Filing Date: 11 January 2000 (11.01.00) (30) Priority Data: 09/228,826 12 January 1999 (12.01.99) US (71) Applicant: QUALCOMM INCORPORATED [US/US]; 5775 Morehouse Drive, San Diego, CA 92121 (US). (72) Inventor: BAZARJANI, Seyfollah, S.; 13280 Tiverton Road, San Diego, CA 92130 (US). (74) Agents: OGROD, Gregory, D. et al.; Qualcomm Incorporated, 5775 Morehouse Drive, San Diego, CA 92121 (US).	(81) Designated States: AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG). Published <i>With international search report.</i>	

(54) Title: LINEAR SAMPLING SWITCH**(57) Abstract**

A linear switch is incorporated into an active sample and hold switch. The active sample and hold circuit is symmetric and configured to accept a balanced input. Two linear switches couple a positive input signal of the balanced input to two different sampling capacitors. After the sampling capacitors are charged, another set of switches configures the sampling capacitors such that one of the sampling capacitor is in the feed back of an op amp and the other is connected from the input of the op amp to ground. In this configuration, the circuit has a gain of two and the output of the op amp is twice the voltage sampled by the sampling capacitors.

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