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(71) Applicant: INTEL CORPORATION [US/US]; 2200 Mission College Boulevard, Santa Clara, CA 95052 (US).

(72) Inventors: CHRISTOPHERSON, Mark; 1200 Creekside Drive #2733, Folsom, CA 95630 (US). WELLS, Steven; 7744 Sun Terrace Way, Citrus Heights, CA (US). ATWOOD, Greg; 2495 Marsha Way, San Jose, CA 95125 (US). BAUER, Mark; 2841 Hillcrest Drive, Cameron Park, CA 95682 (US). BAZIO, Albert; 363 Blackwell Drive, Los Gatos, CA 95032 (US). HASBUN, Robert; 4544 Fawn Street, Shingle Springs, CA 95682 (US).

(74) Agents: TAYLOR, Edwin, H. et al.; Blakely, Sokoloff, Taylor & Zafman, 7th floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025-1026 (US).

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(54) Title: ERROR MANAGEMENT PROCESSES FOR FLASH EEPROM MEMORY ARRAYS

(57) Abstract

A method of utilizing error detecting and correcting circuitry (56) to detect and correct errors which can occur in data stored in multi-bit cell format in a flash EEPROM memory array (23) before those errors can affect the accuracy of data provided by a flash EEPROM memory array.