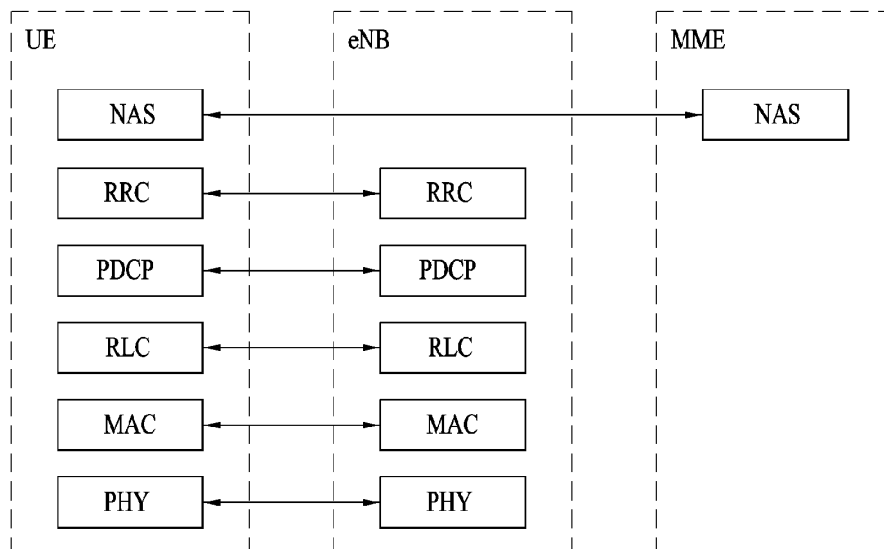
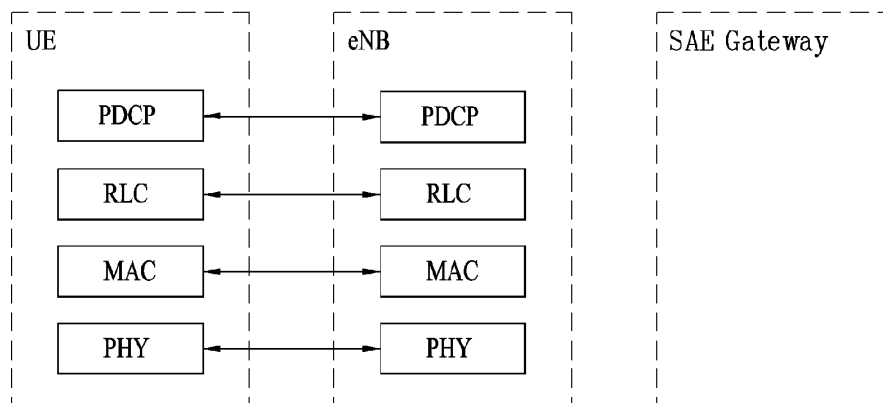


[도 1]

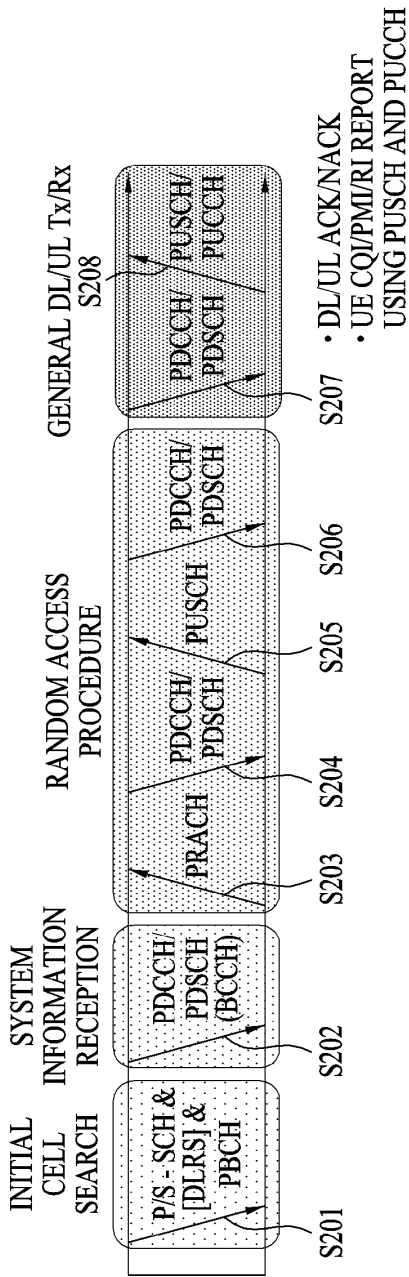


(A) CONTROL-PLANE PROTOCOL STACK

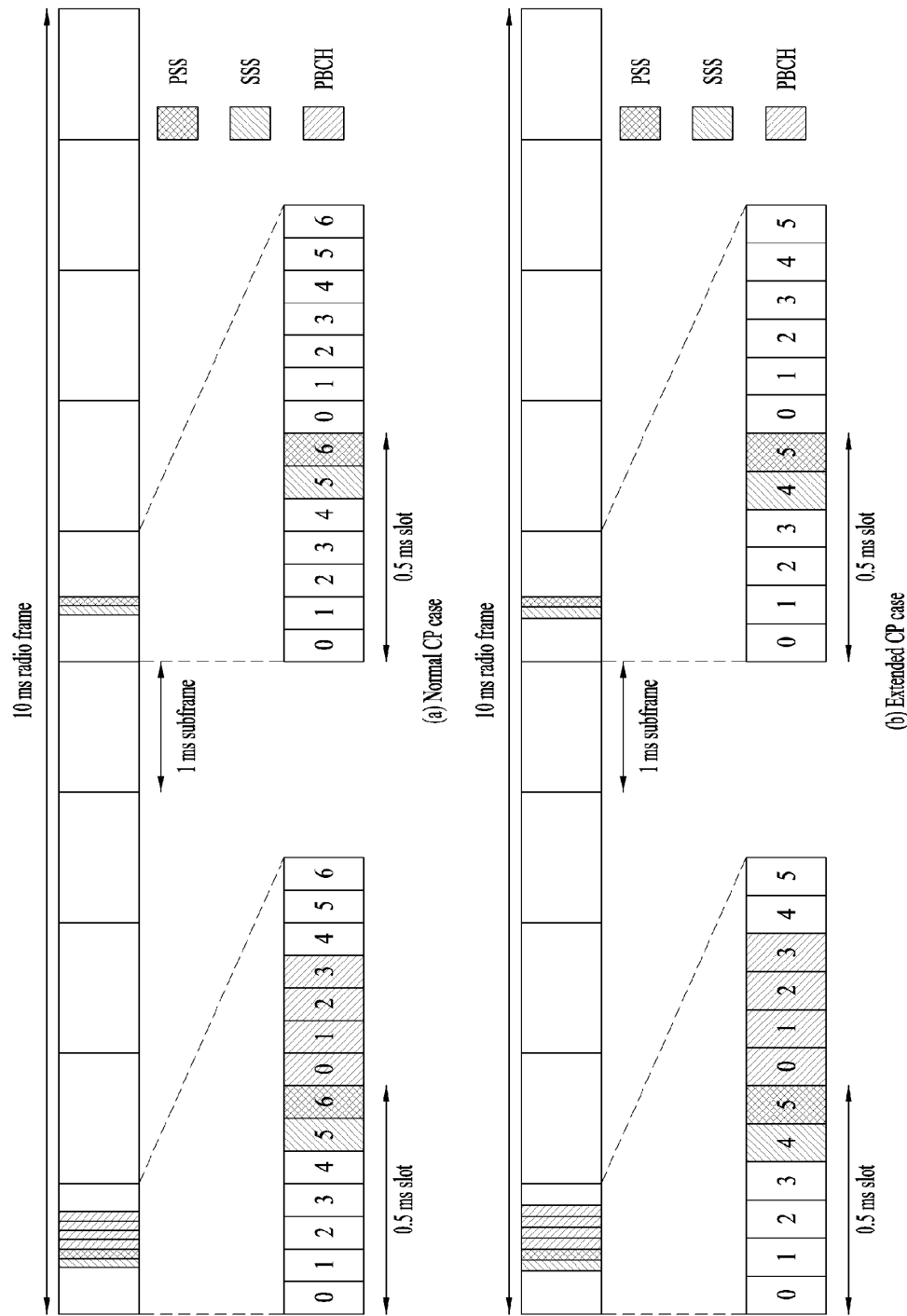


(B) USER-PLANE PROTOCOL STACK

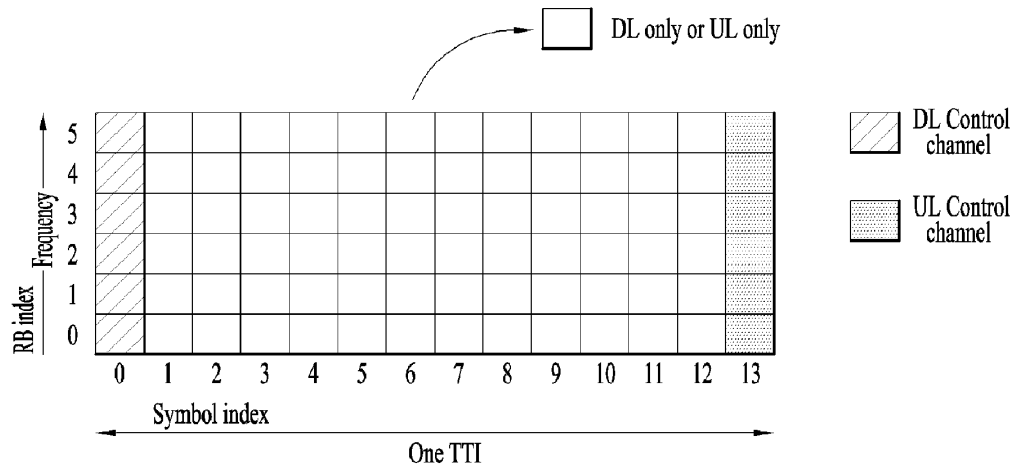
[도2]



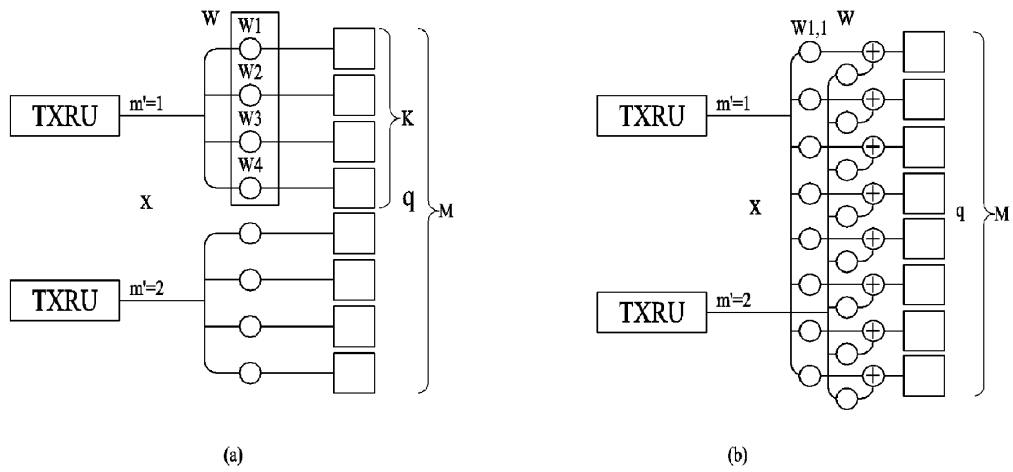
[圖3]



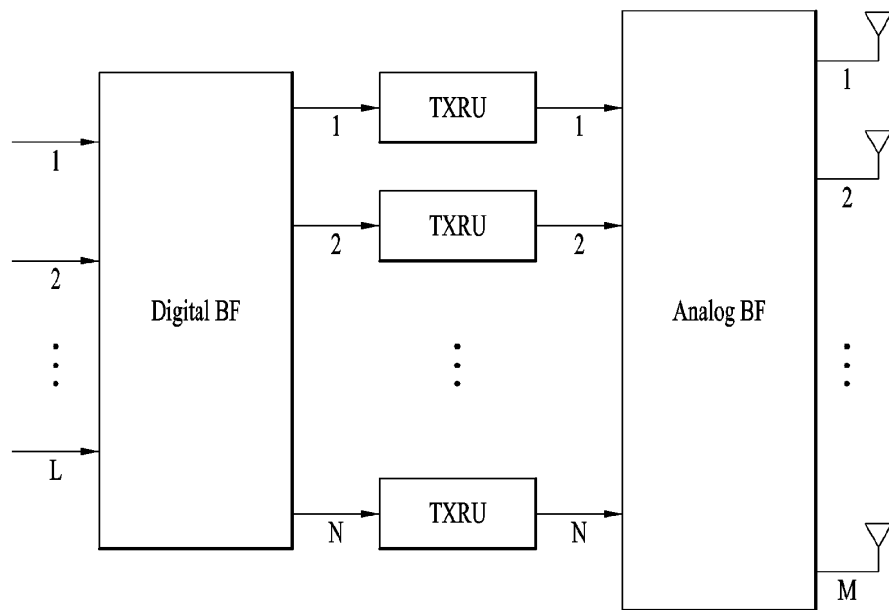
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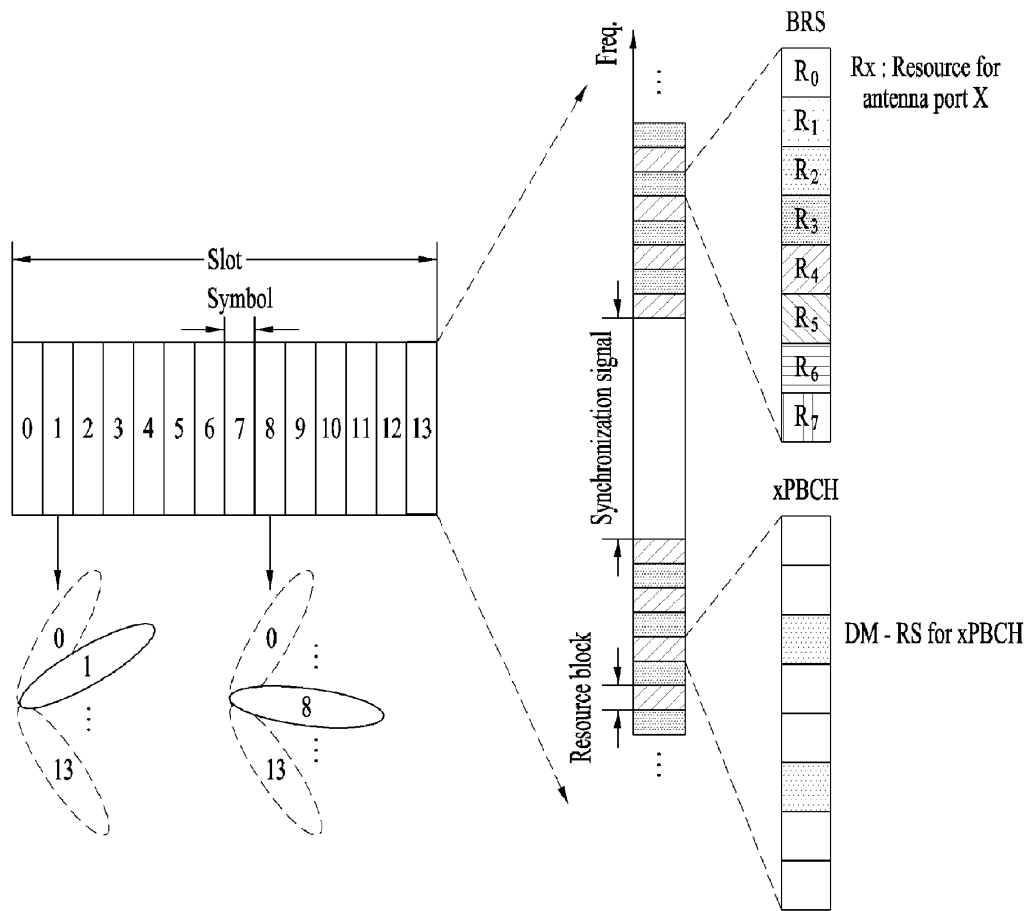
[도5]



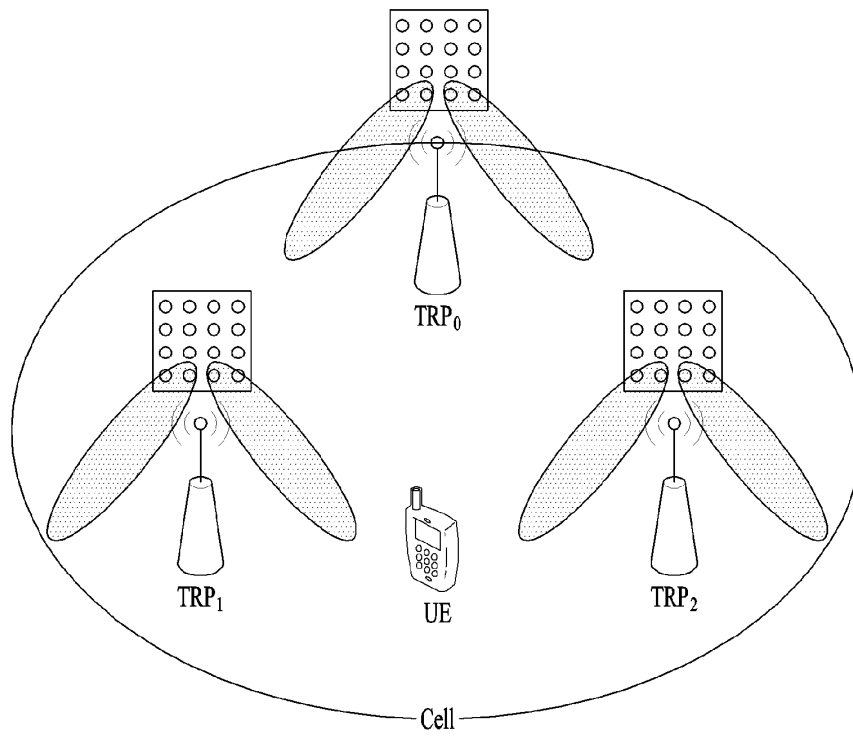
[도6]



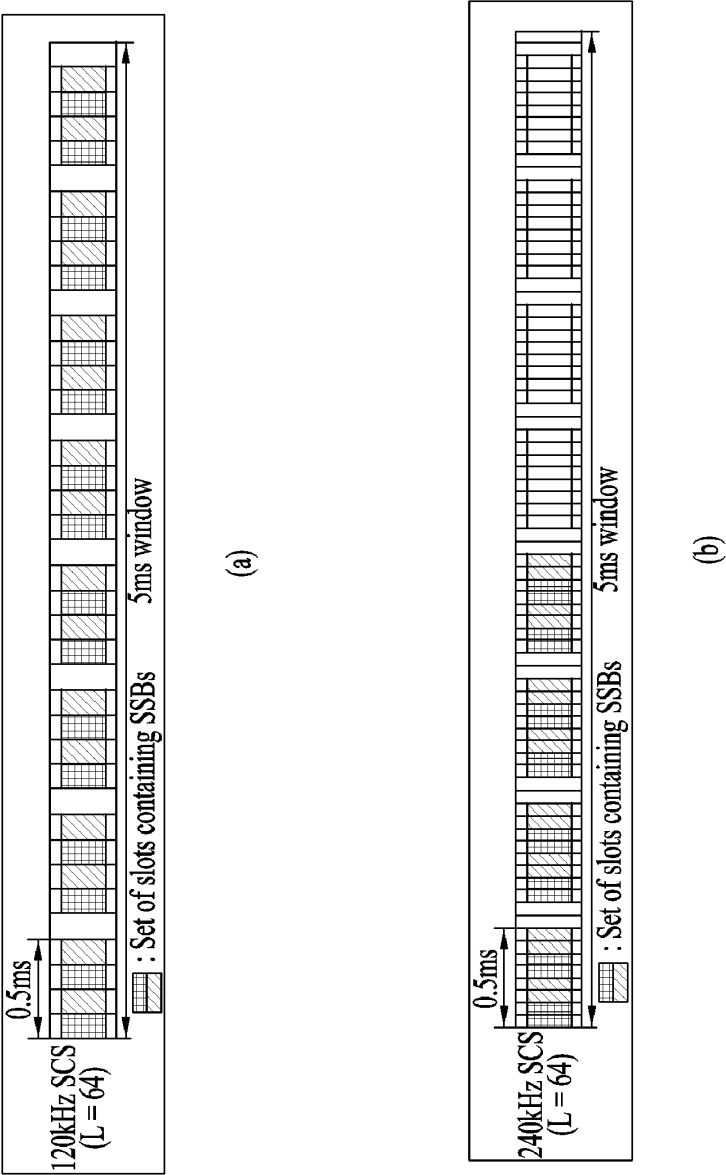
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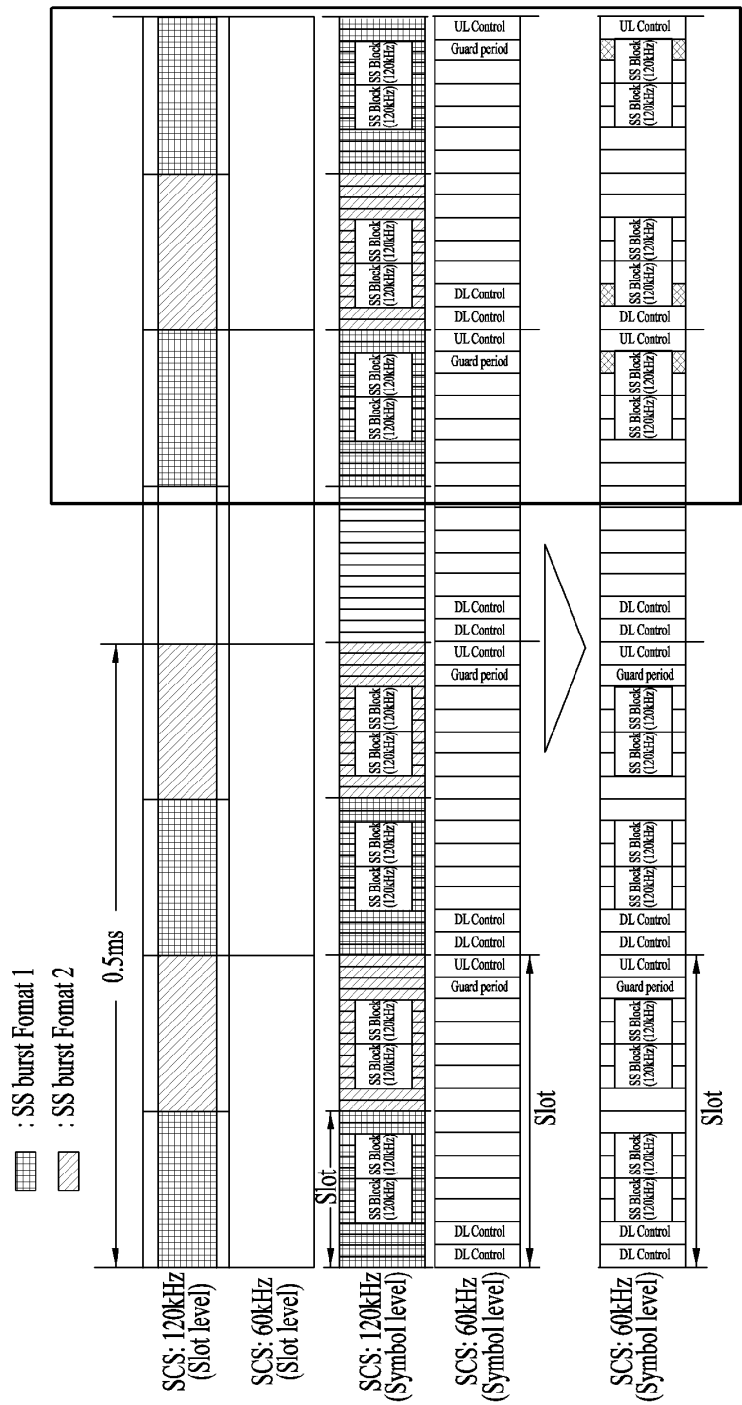
[도8]



[도9]

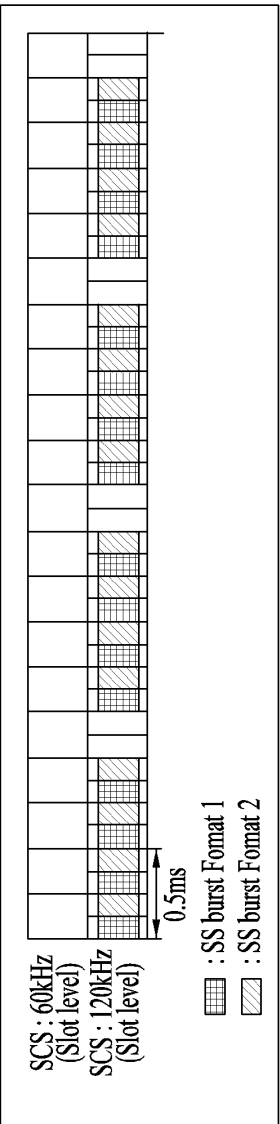


[도 10]

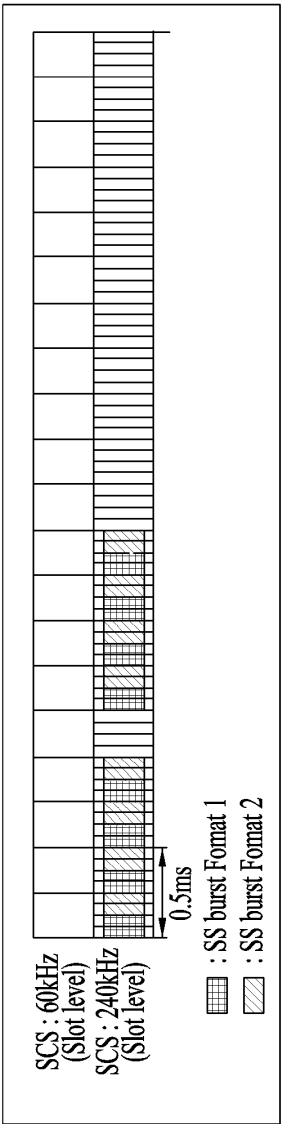




[도 12]

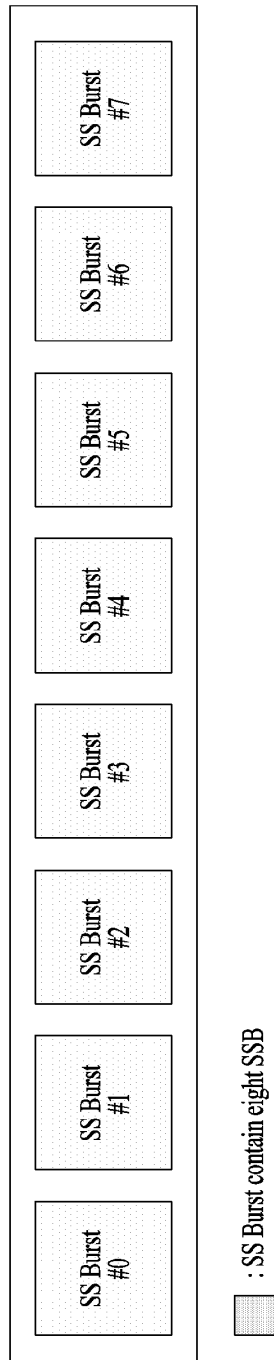


(a)

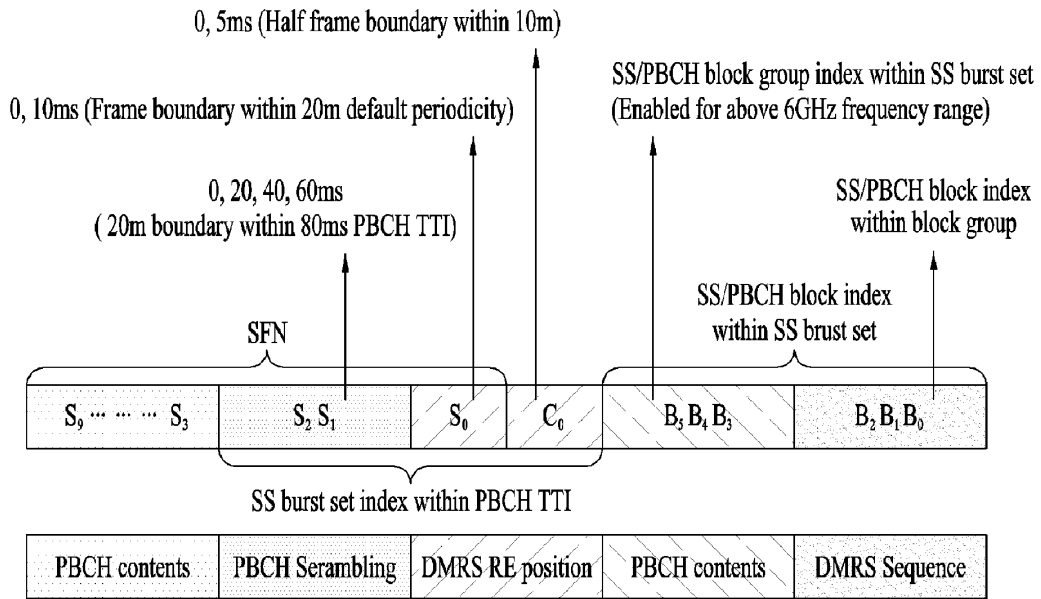


(b)

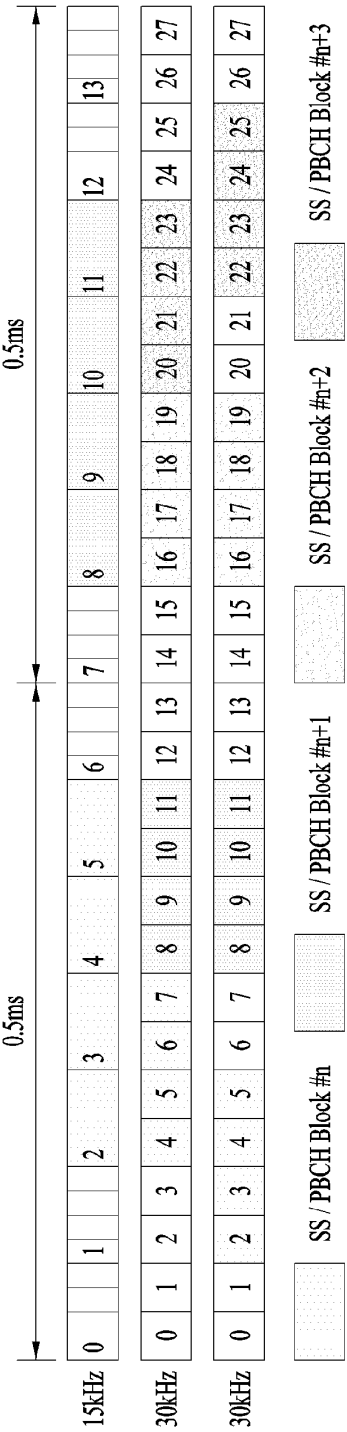
[도 13]



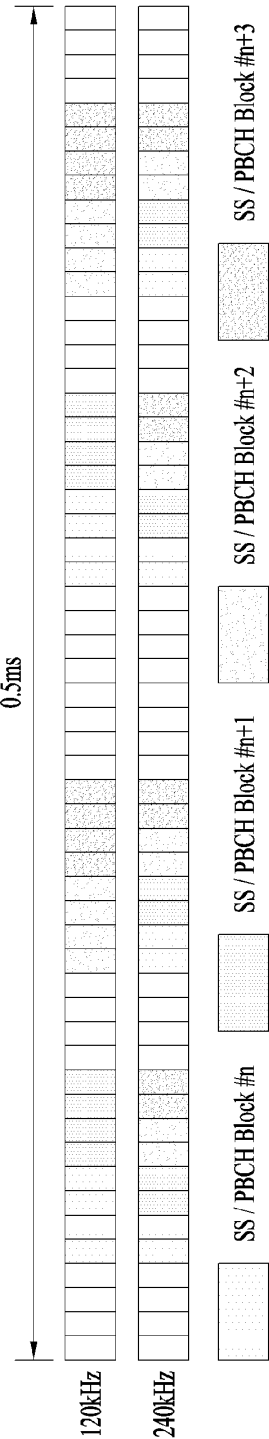
[圖 14]



[도15]

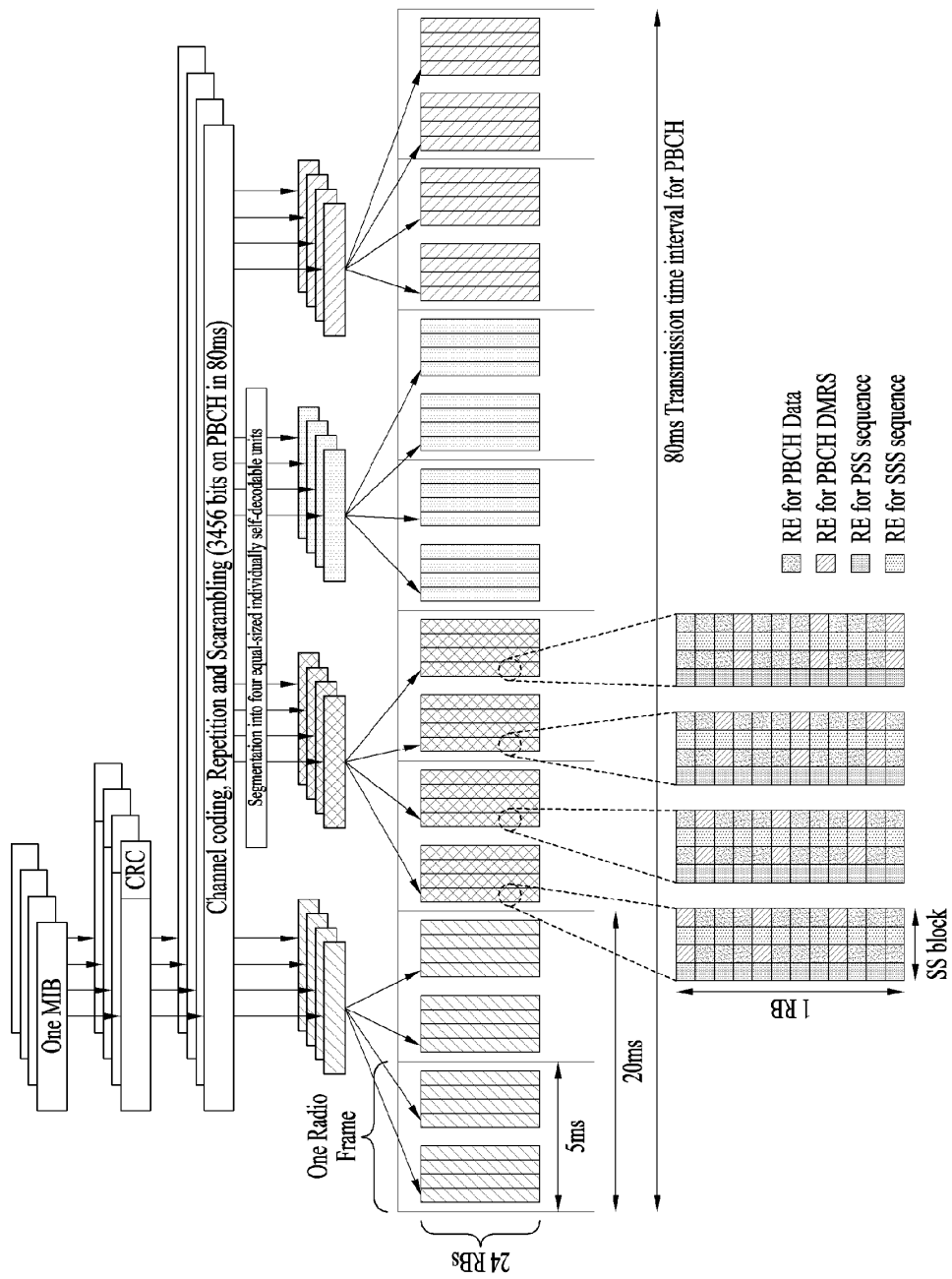


(a)

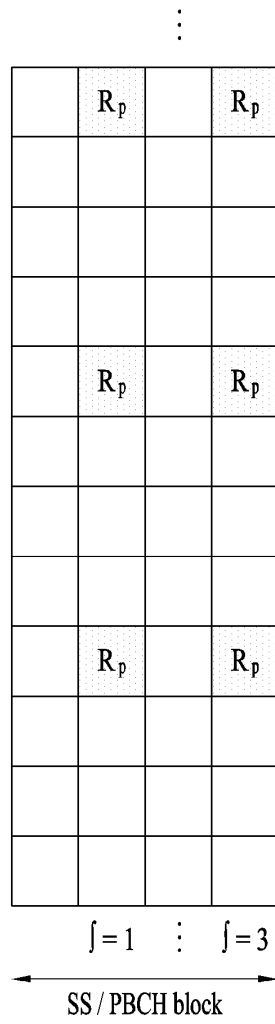


(b)

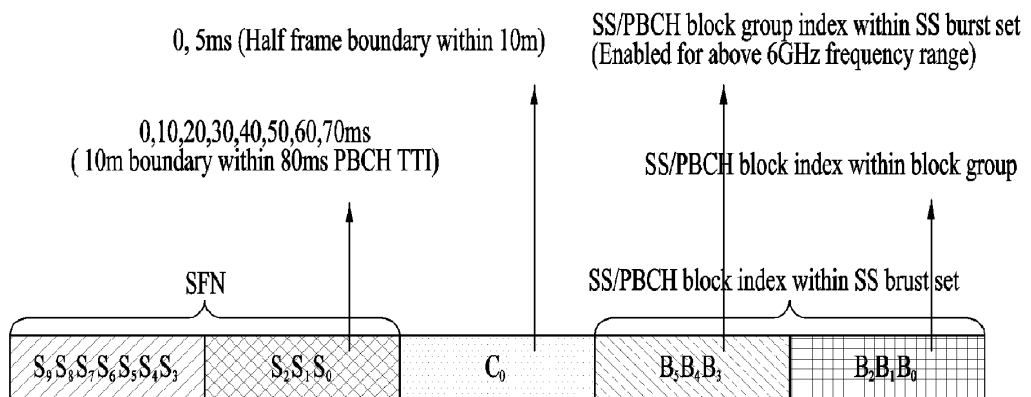
[도16]



[圖17]

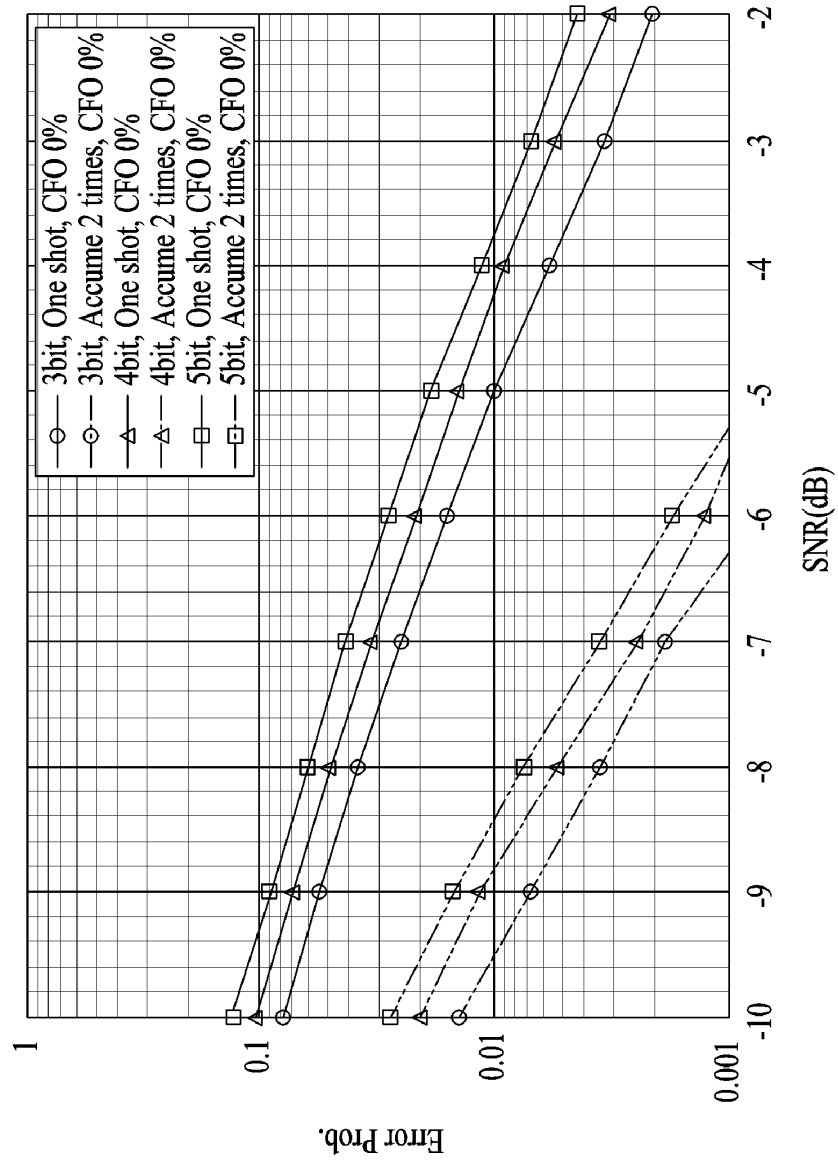


[圖18]

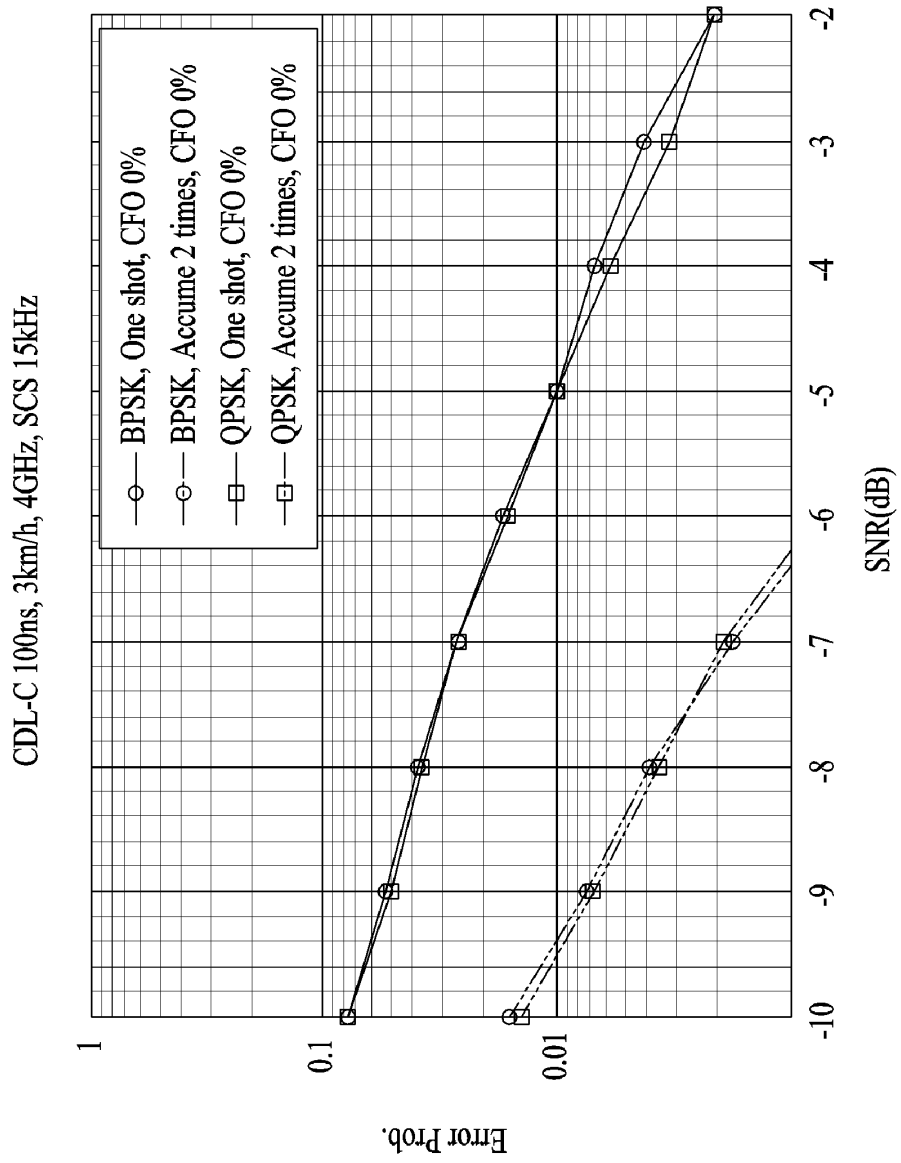


[도19]

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

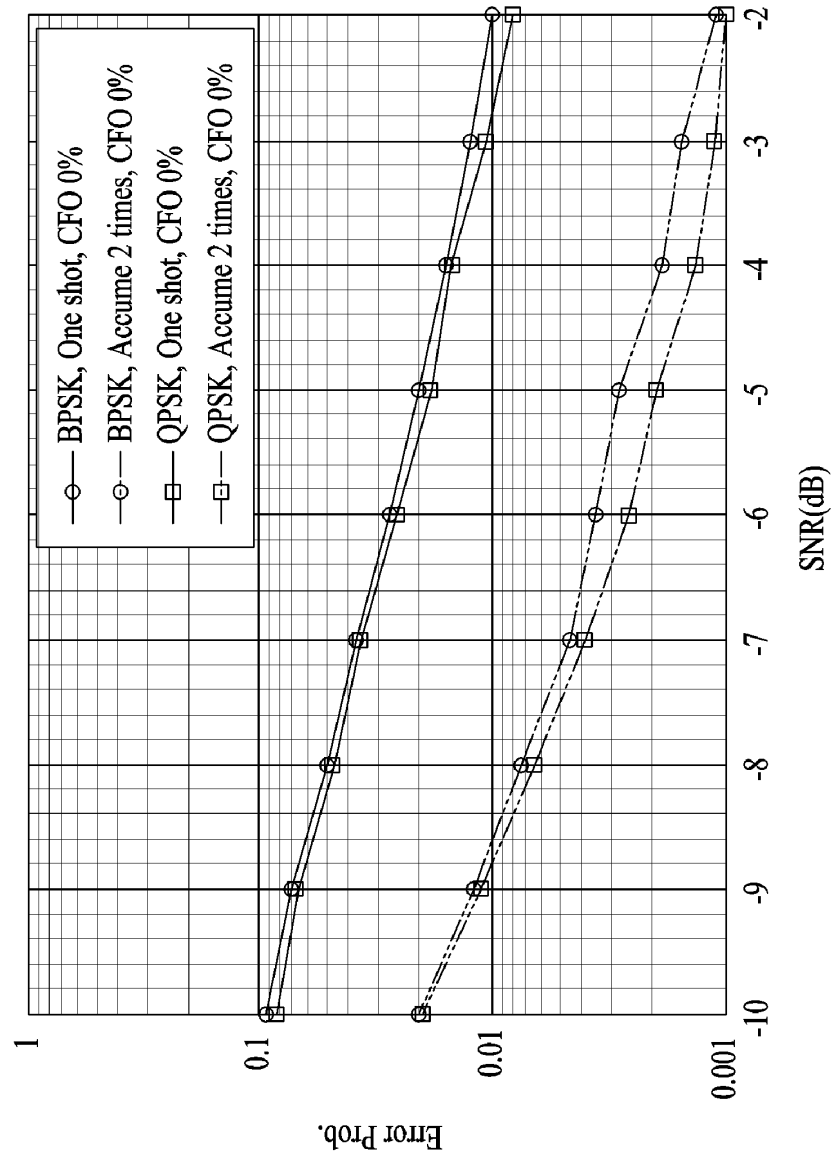


[도 20]

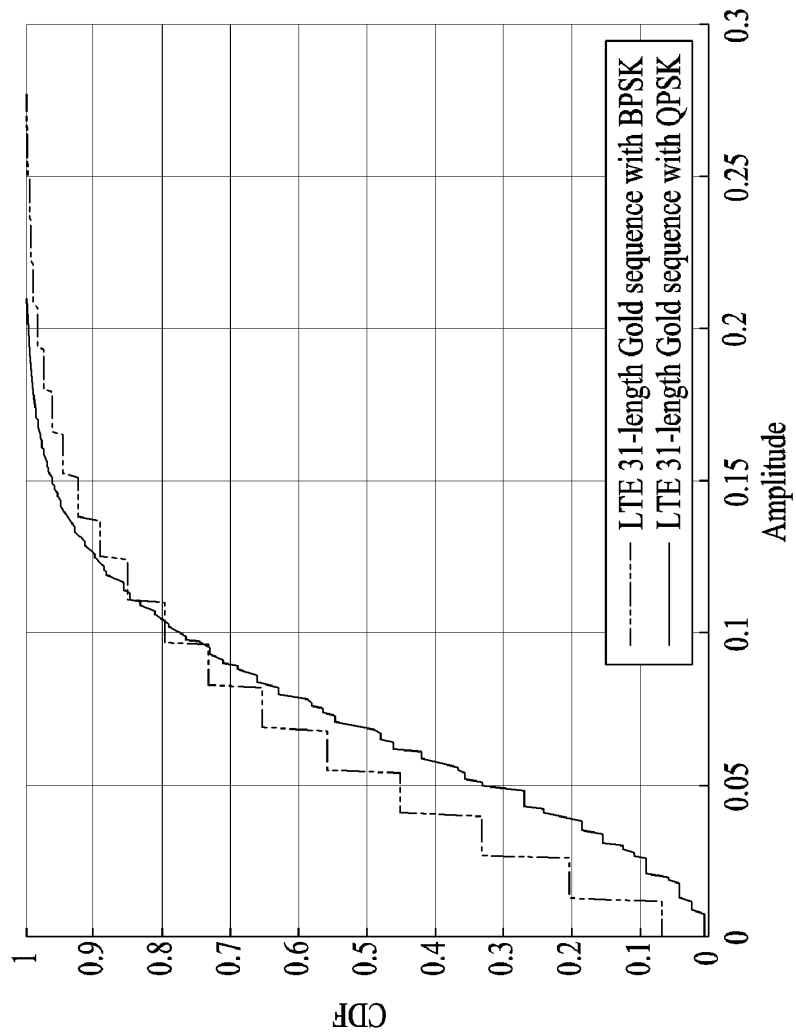


[도 21]

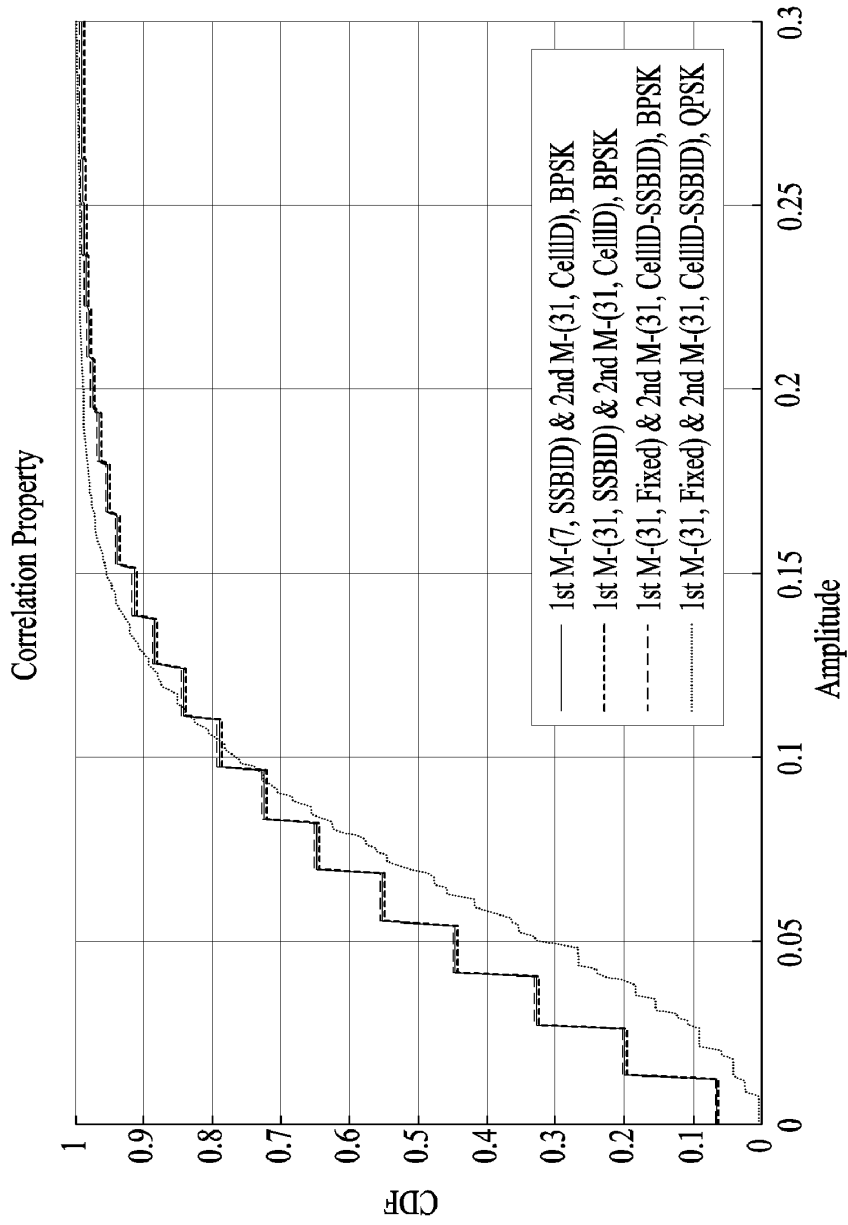
CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz



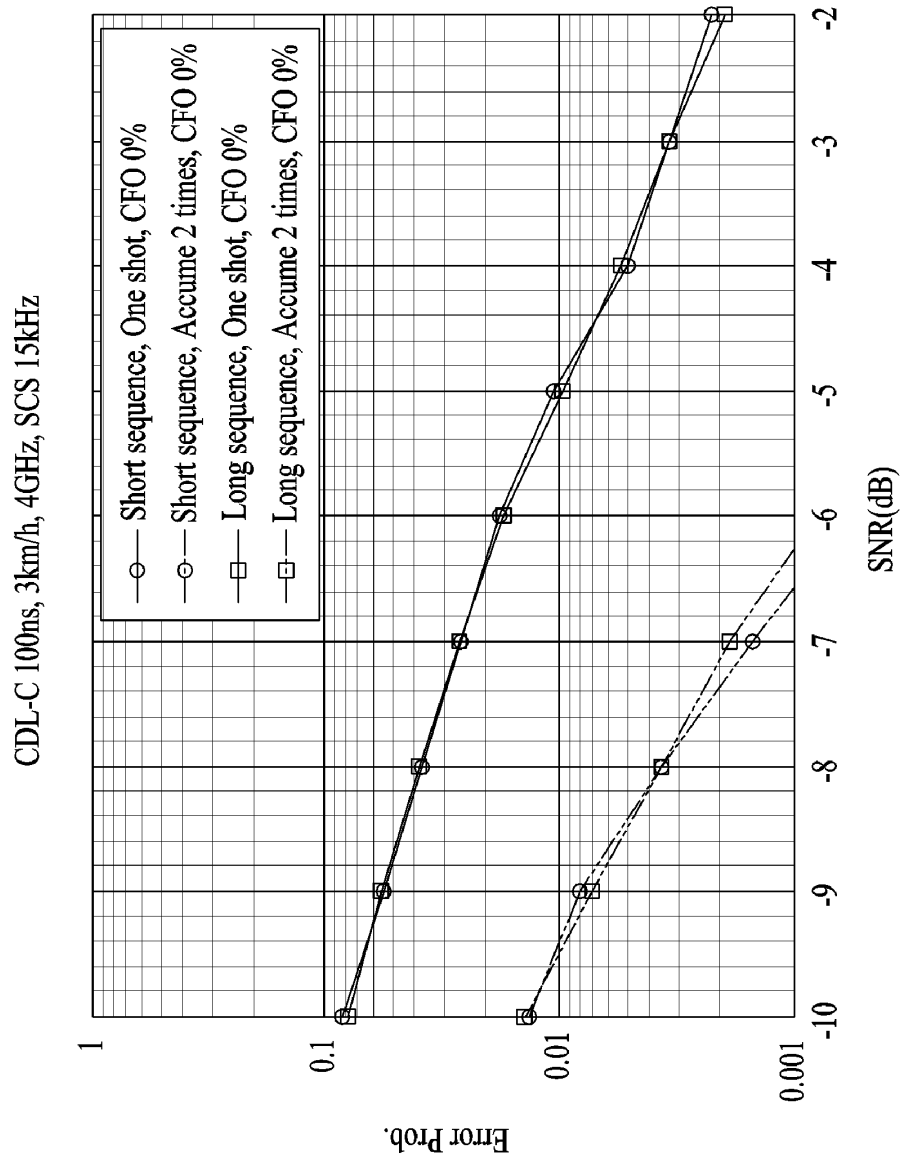
[도 22]



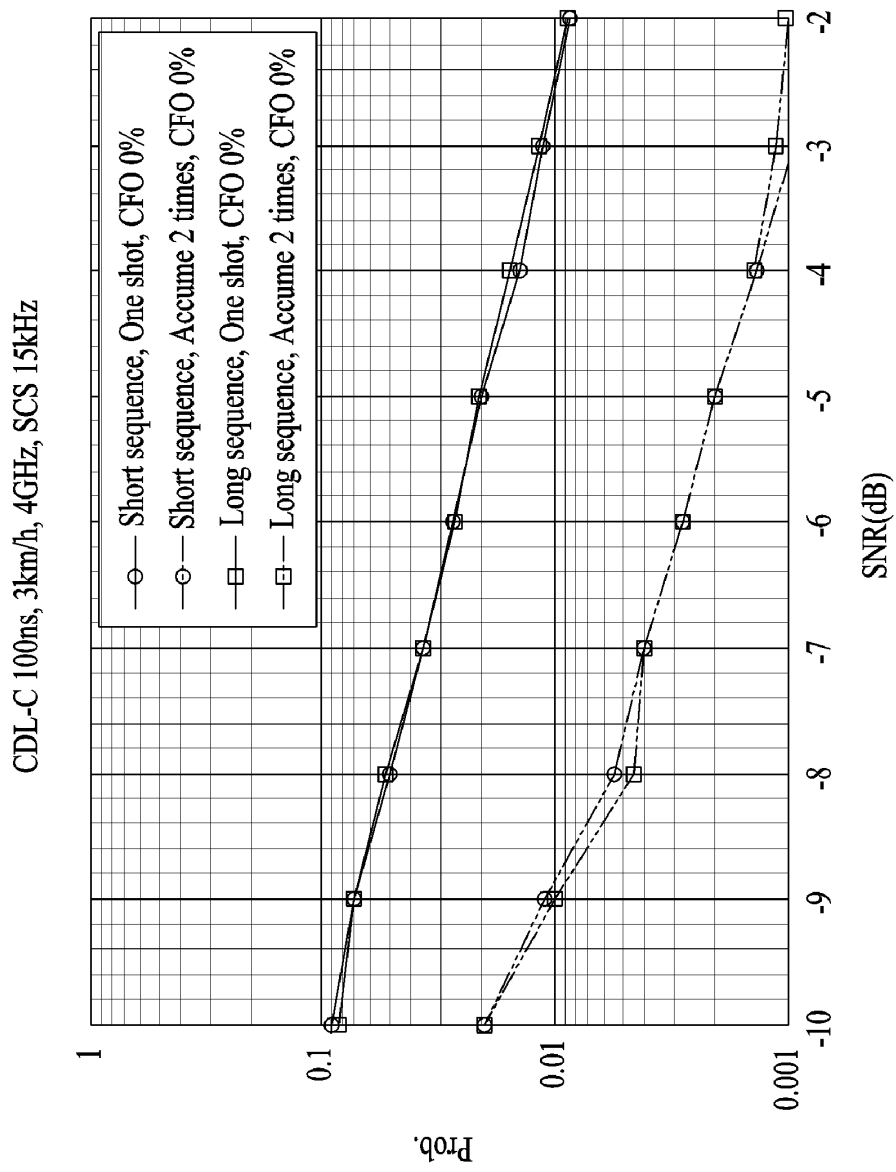
[도23]



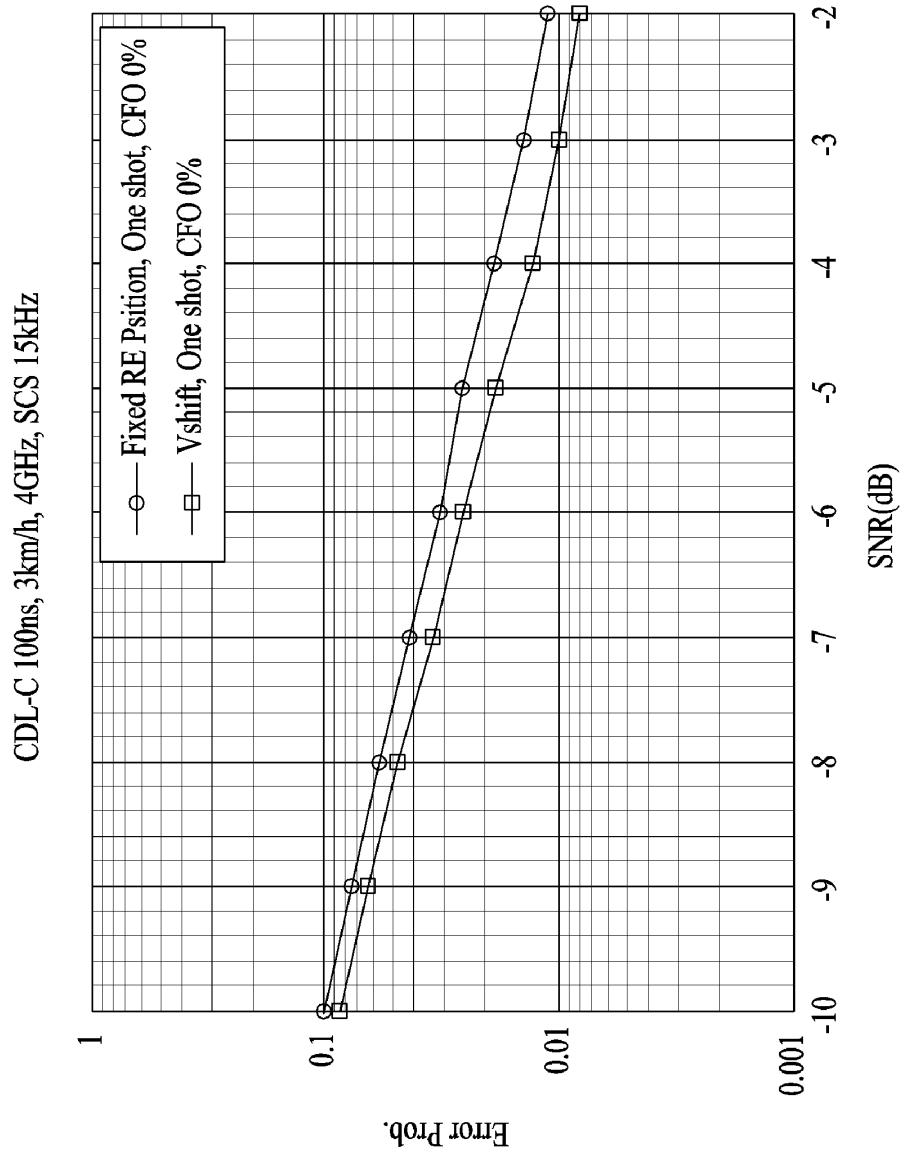
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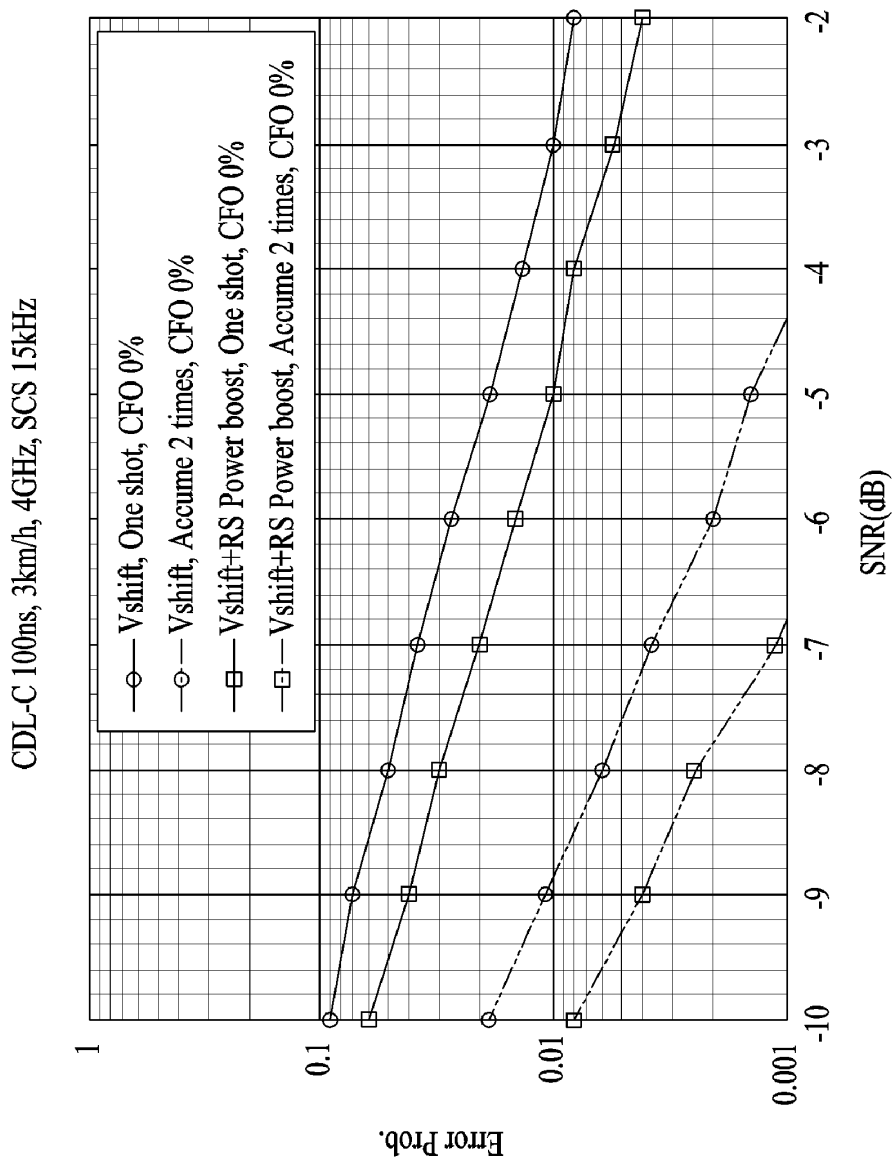
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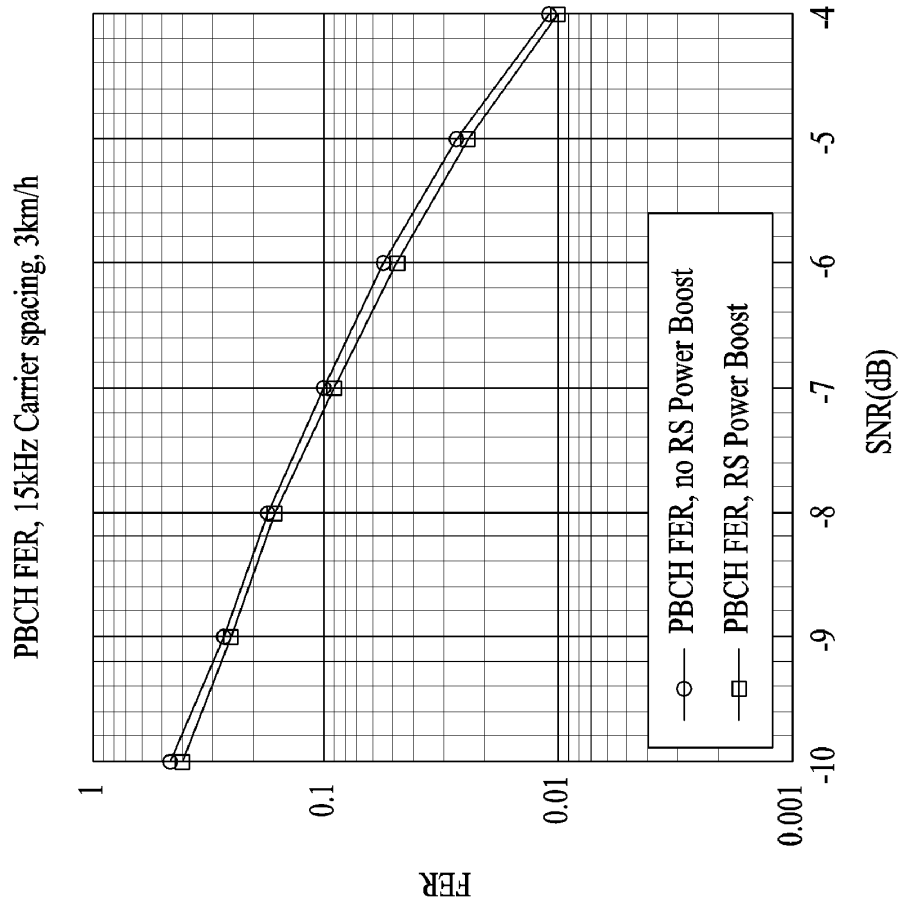
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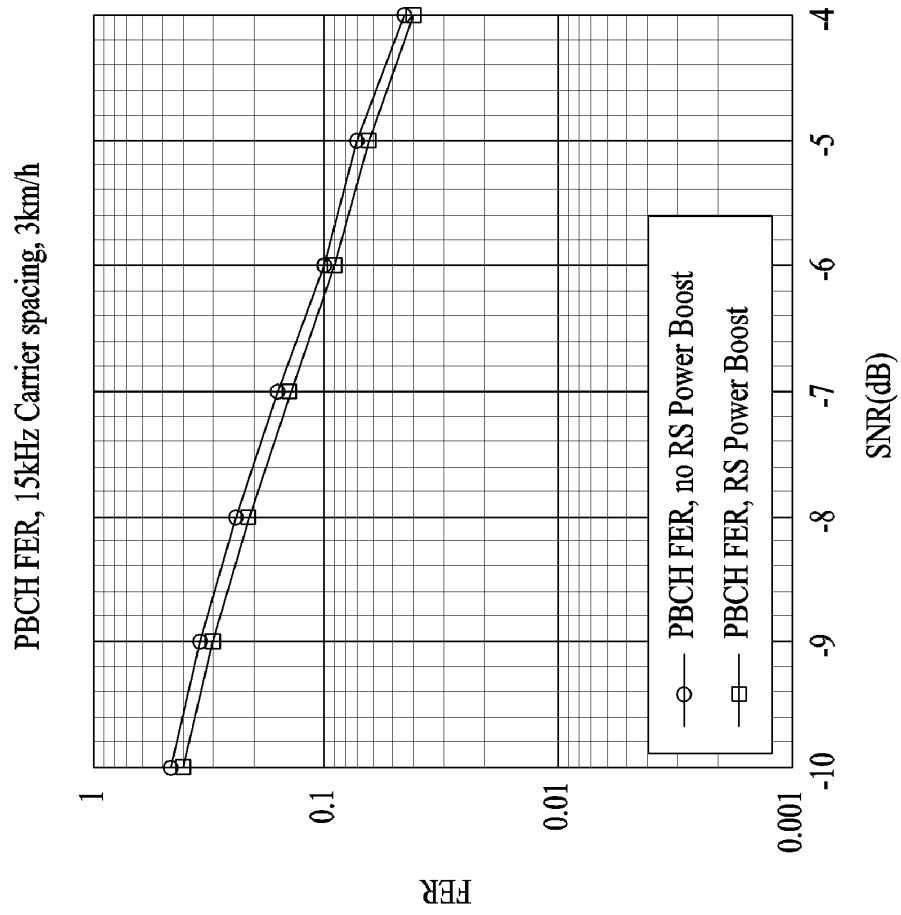
[도 27]



[도28]

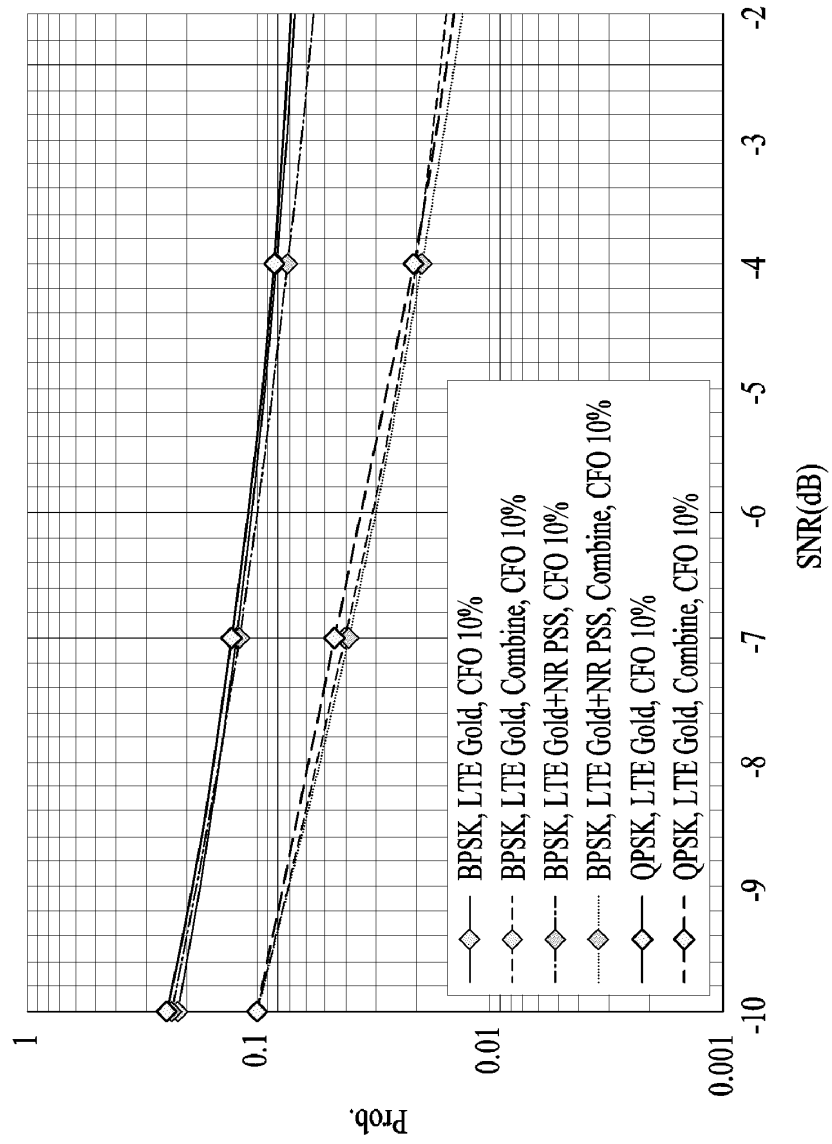


[도29]

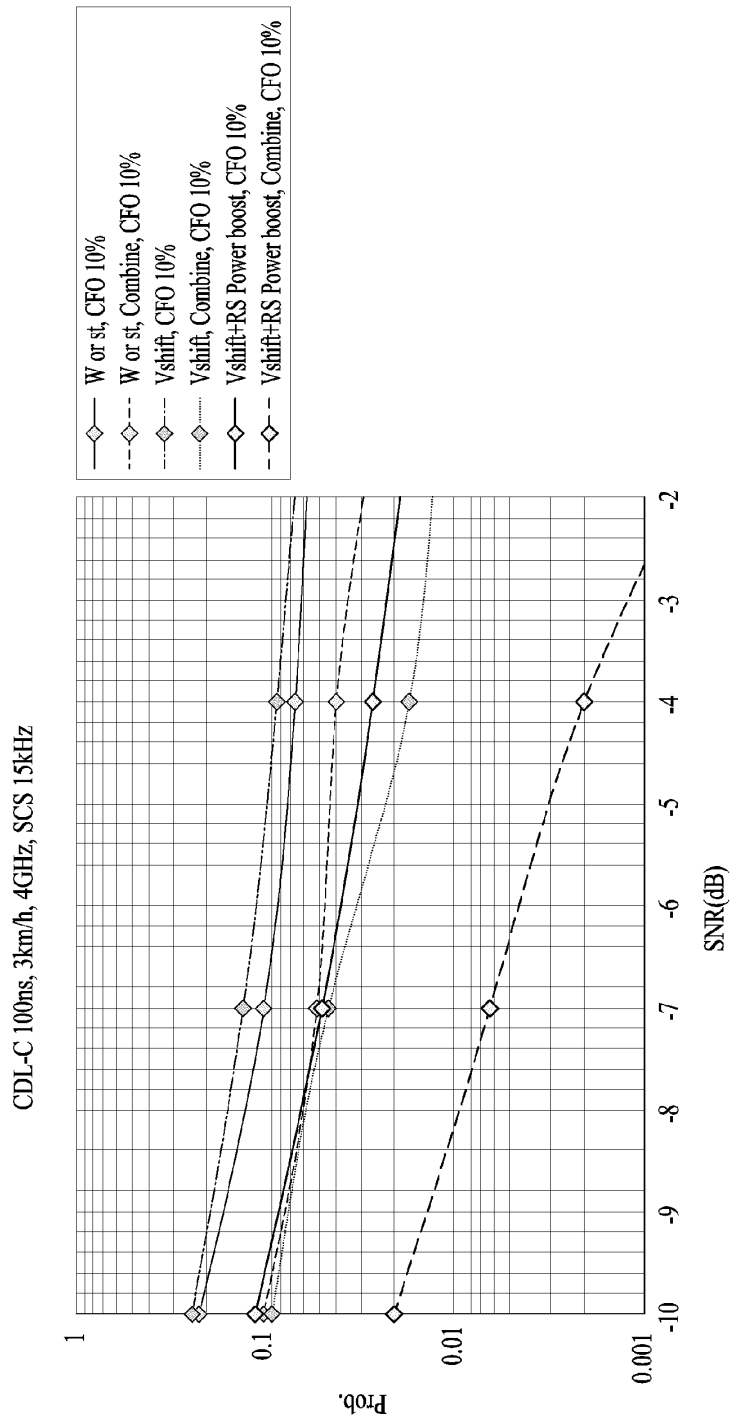


[도30]

CDL-C 100ns, 3km/h, 4GHz, SCS 15kHz

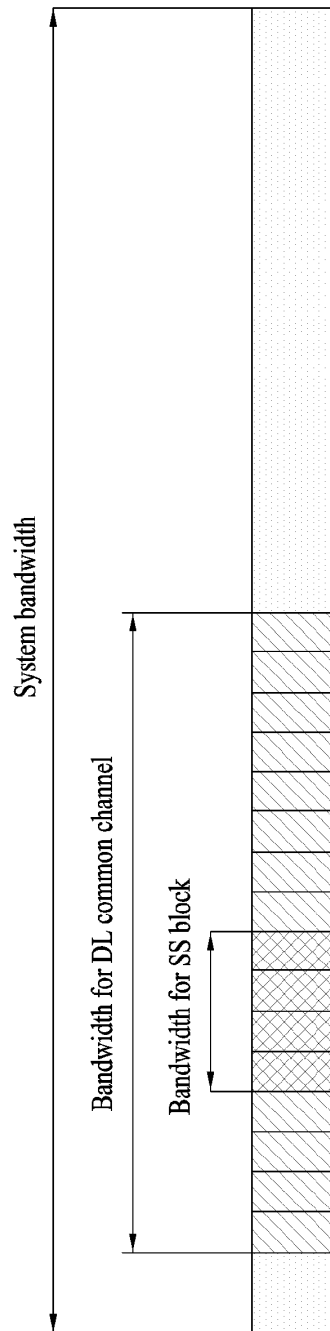


[도31]

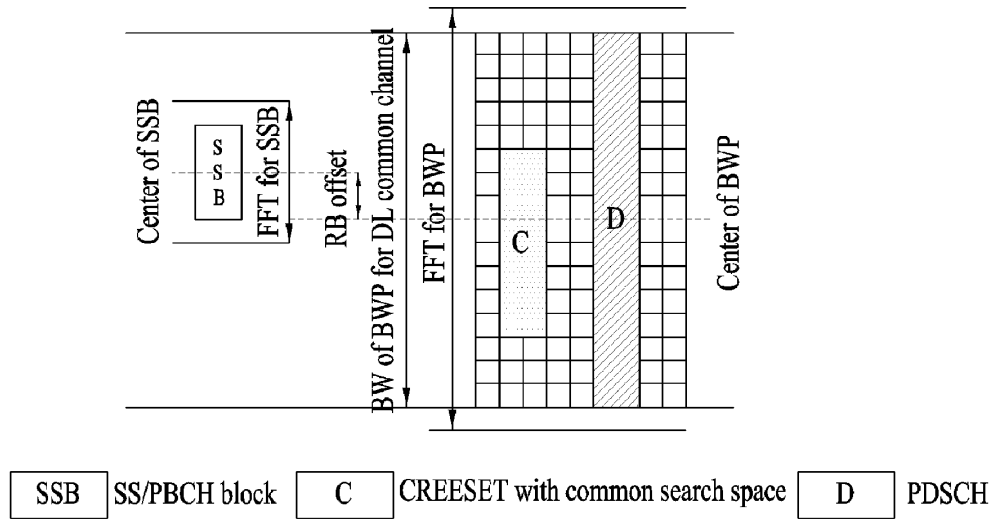




[도33]



[도34]



[도35]

