

ABSTRACT

The present disclosure relates to a memory circuit, a data transmission circuit, and a memory. The memory circuit includes: at least one memory structure arranged parallel to a data transmission region, wherein the memory structure includes a first
5 memory array and a second memory array arranged adjacent to each other in a first direction, a distance between the first memory array and the data transmission region is less than a distance between the second memory array and the data transmission region, and the first direction is a direction of approaching the data transmission region; the first memory array includes a read/write module and a forwarding module;
10 the second memory array includes a read/write module; the first memory array performs a data interaction with the data transmission region based on the read/write module in the first memory array; and the second memory array performs a data transmission with the data transmission region based on the read/write module in the second memory array and the forwarding module in the first memory array.

15 **FIG. 1**