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(71) Applicant: DOLBY INTERNATIONAL AB [SE/NL];
Apollo Building, 3E, Herikerbergweg 1-35, NL-1101 CN
Amsterdam (NL).(72) Inventors: VILLEMOES, Lars; Gavlegatan 12A, S-113
30 Stockholm (SE). MUNDT, Harald; Deutschherrn-
strasse 15-19, 90429 Nuremberg (DE).(74) Agent: DOLBY INTERNATIONAL AB PATENT
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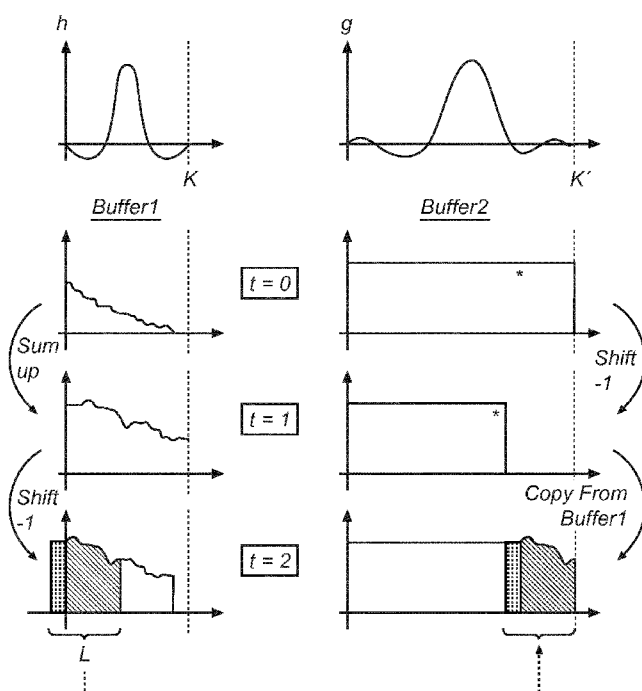
(54) Title: LOW DELAY REAL-TO-COMPLEX CONVERSION IN OVERLAPPING FILTER BANKS FOR PARTIALLY COM-
PLEX PROCESSING

Fig. 3a

(57) Abstract: An arrangement of overlapping filter banks
comprises a synthesis stage and an analysis stage. The syn-
thesis stage receives a first signal segmented into time blocks
and outputs, based thereon, an intermediate signal to be re-
ceived by the analysis stage forming the basis for the compu-
tation of a second signal segmented into time frames. In an
embodiment, the synthesis stage is operable to release an ap-
proximate value of the intermediate signal in a time block
located $L - 1$ time blocks ahead of its output block, which
approximate value is computed on the basis of any available
time blocks of the first signal, so that the approximate value
contributes, in the analysis stage, to the second signal. The
delay is typically reduced by $L - 1$ blocks. Applications in-
clude audio signal processing in general and real -to-com-
plex conversion in particular.



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