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Abstract of the Disclosure

A digital operation unit includes an arithmetic logic unit for executing arithmetic and logical operations by receiving two data via first and second input ports, a plurality of registers for storing operation results of the arithmetic logic unit, a plurality of first switching means connected between the arithmetic logic unit and the plurality of registers for controlling the transmission of data, a plurality of second switching means connected between the plurality of registers and the first input port of the arithmetic logic unit for controlling the transmission of data, and a plurality of third switching means connected between the plurality of registers and the second input port of the arithmetic logic unit for controlling the transmission of data. The digital operation unit can execute variety of complex arithmetic and logical operations easily without the assistance of an external memory and can reduce the data shifting, thereby improving the overall operation speed.

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