(A) receiving a set of input signals;

(B) buffering selected signals from said input signals when said selected signals are required at a subsequent logic level, said buffering step generating buffered signals;

(C) executing a primary set of logic on said input signals to generate a set of internal signals;

(D) selecting a set of intermediate signals from said buffered signals and said internal signals;

(E) buffering designated signals from said intermediate signals when said designated signals are required at a subsequent logic level;

(E) performing a preselected set of logic on said intermediate signals to generate a new set of internal signals; and

(F) repeating steps (D) through (E) until a predetermined set of logic is executed, said predetermined set of logic corresponding to a plurality of logic levels in a circuit.

12. The method of claim 11 wherein said preselected set of logic of said performing set is designated by defining a logic level within said plurality of logic levels.

13. The method of claim 12 wherein said logic level of said defining step is incremented after each performing step.

This Sheet Has Been Replaced

