

ABSTRACT

The present disclosure provides an in-memory processing chip, which may include at least an interface circuit, a memory circuit, and a computing unit. A first transmission path exists between the interface circuit and the memory circuit; a second transmission path exists
5 between the memory circuit and the computing unit; and a third transmission path exists between the interface circuit and the computing unit, and the third transmission path and the first transmission path are independent of each other.

[FIG. 1]