MEMORY ACCESS CONTROL METHOD, APPARATUS, SYSTEM AND ELECTRONIC DEVICE

ABSTRACT

The present disclosure relates to the technical field of computer technology, and provides a memory access control method and apparatus, a system, and an electronic device. The method includes: generating, based on a service requirement, a memory access request of a master device, wherein the memory access request comprises a target stream identifier, the target stream identifier being one of at least one stream identifier pre-configured for the master device; determining, based on the service requirement and the target stream identifier, a pre-created target virtual device node receiving the memory access request, wherein a target virtual stream identifier pre-configured on the target virtual device node is the same as the target stream identifier; forwarding the memory access request to a system memory management unit via the target virtual device node; and processing the memory access request through the system memory management unit.

(FIG. 2)