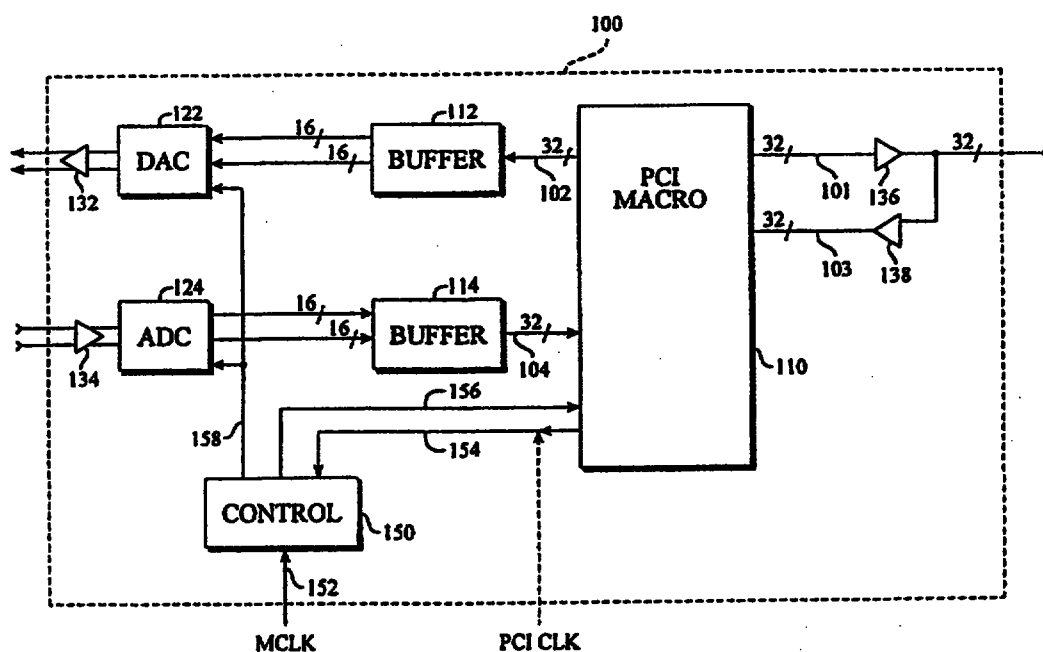




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 7 : H03M 1/08	A1	(11) International Publication Number: WO 00/52829 (43) International Publication Date: 8 September 2000 (08.09.00)
(21) International Application Number: PCT/US00/05345 (22) International Filing Date: 29 February 2000 (29.02.00) (30) Priority Data: 09/262,717 4 March 1999 (04.03.99) US (71) Applicant: ATMEL CORPORATION [US/US]; 2325 Orchard Parkway, San Jose, CA 95131 (US). (72) Inventor: TANN, Christopher, P., A.; 377 Manila Drive, San Jose, CA 95119 (US). (74) Agent: SCHNECK, Thomas; Law Offices of Thomas Schneck, P.O. Box 2-E, San Jose, CA 95109-0005 (US).		(81) Designated States: CA, CN, JP, KR, NO, SG, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i> <i>With amended claims and statement.</i>

(54) Title: REDUCING DIGITAL SWITCHING NOISE IN MIXED SIGNAL IC'S



(57) Abstract

A mixed signal IC 100 comprising analog circuitry for converting between analog and digital signals and digital circuitry includes circuitry to produce a conversion enable signal (158) which causes the analog circuit to perform a conversion between an analog signal and a digital signal. The circuitry also asserts a warning signal (156) prior to production of the enable signal to momentarily disable the digital circuitry during operation of the analog circuitry. By so doing, noise artifacts from operation of the digital circuitry is eliminated when the analog circuitry is making a conversion.