

CLAIMS

1. A method for wireless communications, comprising:
performing a rate matching procedure that comprises joint rate matching or separate rate matching of a transport block for a first transmission configuration indicator state of a plurality of transmission configuration indicator states and a second transmission configuration indicator state of the plurality of transmission configuration indicator states;
generating a set of bits based at least in part on the rate matching procedure, wherein the set of bits indicates a joint redundancy version for the first transmission configuration indicator state and the second transmission configuration indicator state if the rate matching procedure comprises joint rate matching and the set of bits indicates a first redundancy version for the first transmission configuration indicator state and a second redundancy version for the second transmission configuration indicator state if the rate matching procedure comprises separate rate matching; and
transmitting, to a user equipment (UE), a downlink control information message comprising an indication of the set of bits.
2. The method of claim 1, wherein the rate matching procedure comprises separate rate matching, the first redundancy version corresponds to a first codeword, and the second redundancy version corresponds to a second codeword different from the first codeword.
3. The method of claim 1, wherein the set of bits comprises a first set of bits, the method further comprising:
generating a second set of bits indicating whether the rate matching procedure comprises joint rate matching or separate rate matching, wherein the downlink control information message further comprises an indication of the second set of bits.
4. The method of claim 1, wherein the set of bits further indicates whether the rate matching procedure comprises joint rate matching or separate rate matching.
5. The method of claim 1, further comprising:
selecting between a first lookup table mapping the joint redundancy version to the set of bits and a second lookup table mapping a combination of the first redundancy version and

the second redundancy version to the set of bits, wherein the generating is based at least in part on the selecting.

6. The method of claim 5, wherein the first lookup table, the second lookup table, or a combination thereof is pre-configured in memory.

7. The method of claim 5, further comprising:
configuring the first lookup table, the second lookup table, or a combination thereof; and
transmitting, to the UE, an indication of the first lookup table, the second lookup table, or a combination thereof.

8. The method of claim 1, wherein generating the set of bits comprises:
identifying the set of bits based at least in part on a lookup table mapping one or more redundancy versions to the set of bits.

9. The method of claim 8, wherein the lookup table is pre-configured in memory.

10. The method of claim 8, further comprising:
configuring the lookup table; and
transmitting, to the UE, an indication of the lookup table.

11. The method of claim 1, wherein the rate matching procedure comprises separate rate matching, the method further comprising:
performing a slot aggregation procedure for the transport block over a plurality of slots, wherein the set of bits further indicates the first redundancy version for the first transmission configuration indicator state and the second redundancy version for the second transmission configuration indicator state for each slot of the plurality of slots.

12. A method for wireless communications at a user equipment (UE), comprising:
receiving, from a base station, a downlink control information message comprising an indication of a set of bits;

determining a rate matching procedure performed by the base station, wherein the rate matching procedure comprises joint rate matching or separate rate matching of a transport block for a first transmission configuration indicator state of a plurality of transmission configuration indicator states and a second transmission configuration indicator state of the plurality of transmission configuration indicator states; and

identifying, using the set of bits and based at least in part on the rate matching procedure, a joint redundancy version for the first transmission configuration indicator state and the second transmission configuration indicator state if the rate matching procedure comprises joint rate matching or a first redundancy version for the first transmission configuration indicator state and a second redundancy version for the second transmission configuration indicator state if the rate matching procedure comprises separate rate matching.

13. The method of claim 12, wherein the rate matching procedure comprises separate rate matching, the first redundancy version corresponds to a first codeword, and the second redundancy version corresponds to a second codeword different from the first codeword.

14. The method of claim 12, wherein:
the set of bits comprises a first set of bits;
the downlink control information message further comprises an indication of a second set of bits; and
the rate matching procedure performed by the base station is determined using the second set of bits.

15. The method of claim 12, wherein the rate matching procedure performed by the base station is determined using the set of bits.

16. The method of claim 12, further comprising:
selecting between a first lookup table mapping the set of bits to the joint redundancy version and a second lookup table mapping the set of bits to a combination of the first redundancy version and the second redundancy version, wherein the identifying is based at least in part on the selecting.

17. The method of claim 16, wherein the first lookup table, the second lookup table, or a combination thereof is pre-configured in memory.

18. The method of claim 16, further comprising:
receiving, from the base station, a configuration of the first lookup table, the second lookup table, or a combination thereof.

19. The method of claim 12, wherein the joint redundancy version or the first redundancy version and the second redundancy version are identified based at least in part on a lookup table mapping the set of bits to one or more redundancy versions.

20. The method of claim 19, wherein the lookup table is pre-configured in memory.

21. The method of claim 19, further comprising:
receiving, from the base station, a configuration of the lookup table.

22. The method of claim 12, wherein the rate matching procedure comprises separate rate matching, the method further comprising:
identifying, using the set of bits and based at least in part on the rate matching procedure, the first redundancy version for the first transmission configuration indicator state and the second redundancy version for the second transmission configuration indicator state for each slot of a plurality of slots in a slot aggregation procedure.

23. An apparatus for wireless communications, comprising:
a processor;
memory coupled with the processor; and
instructions stored in the memory and executable by the processor to cause the apparatus to:
perform a rate matching procedure that comprises joint rate matching or separate rate matching of a transport block for a first transmission configuration indicator state of a plurality of transmission configuration indicator states and a second transmission configuration indicator state of the plurality of transmission configuration indicator states;

generate a set of bits based at least in part on the rate matching procedure, wherein the set of bits indicates a joint redundancy version for the first transmission configuration indicator state and the second transmission configuration indicator state if the rate matching procedure comprises joint rate matching and the set of bits indicates a first redundancy version for the first transmission configuration indicator state and a second redundancy version for the second transmission configuration indicator state if the rate matching procedure comprises separate rate matching; and

transmit, to a user equipment (UE), a downlink control information message comprising an indication of the set of bits.

24. The apparatus of claim 23, wherein the rate matching procedure comprises separate rate matching, the first redundancy version corresponds to a first codeword, and the second redundancy version corresponds to a second codeword different from the first codeword.

25. The apparatus of claim 23, wherein the set of bits comprises a first set of bits, and the instructions are further executable by the processor to cause the apparatus to:

generate a second set of bits indicating whether the rate matching procedure comprises joint rate matching or separate rate matching, wherein the downlink control information message further comprises an indication of the second set of bits.

26. The apparatus of claim 23, wherein the set of bits further indicates whether the rate matching procedure comprises joint rate matching or separate rate matching.

27. The apparatus of claim 23, wherein the instructions are further executable by the processor to cause the apparatus to:

select between a first lookup table mapping the joint redundancy version to the set of bits and a second lookup table mapping a combination of the first redundancy version and the second redundancy version to the set of bits, wherein the generating is based at least in part on the selecting.

28. The apparatus of claim 27, wherein the first lookup table, the second lookup table, or a combination thereof is pre-configured in the memory.

29. The apparatus of claim 27, wherein the instructions are further executable by the processor to cause the apparatus to:
configure the first lookup table, the second lookup table, or a combination thereof;
and
transmit, to the UE, an indication of the first lookup table, the second lookup table, or a combination thereof.

30. The apparatus of claim 23, wherein the instructions to generate the set of bits are executable by the processor to cause the apparatus to:
identify the set of bits based at least in part on a lookup table mapping one or more redundancy versions to the set of bits.

31. The apparatus of claim 30, wherein the lookup table is pre-configured in the memory.

32. The apparatus of claim 30, wherein the instructions are further executable by the processor to cause the apparatus to:
configure the lookup table; and
transmit, to the UE, an indication of the lookup table.

33. The apparatus of claim 23, wherein the rate matching procedure comprises separate rate matching, and the instructions are further executable by the processor to cause the apparatus to:
perform a slot aggregation procedure for the transport block over a plurality of slots, wherein the set of bits further indicates the first redundancy version for the first transmission configuration indicator state and the second redundancy version for the second transmission configuration indicator state for each slot of the plurality of slots.

34. An apparatus for wireless communications at a user equipment (UE), comprising:
a processor;
memory coupled with the processor; and

instructions stored in the memory and executable by the processor to cause the apparatus to:

receive, from a base station, a downlink control information message comprising an indication of a set of bits;

determine a rate matching procedure performed by the base station, wherein the rate matching procedure comprises joint rate matching or separate rate matching of a transport block for a first transmission configuration indicator state of a plurality of transmission configuration indicator states and a second transmission configuration indicator state of the plurality of transmission configuration indicator states; and

identify, using the set of bits and based at least in part on the rate matching procedure, a joint redundancy version for the first transmission configuration indicator state and the second transmission configuration indicator state if the rate matching procedure comprises joint rate matching or a first redundancy version for the first transmission configuration indicator state and a second redundancy version for the second transmission configuration indicator state if the rate matching procedure comprises separate rate matching.

35. The apparatus of claim 34, wherein the rate matching procedure comprises separate rate matching, the first redundancy version corresponds to a first codeword, and the second redundancy version corresponds to a second codeword different from the first codeword.

36. The apparatus of claim 34, wherein:
the set of bits comprises a first set of bits;
the downlink control information message further comprises an indication of a second set of bits; and
the rate matching procedure performed by the base station is determined using the second set of bits.

37. The apparatus of claim 34, wherein the rate matching procedure performed by the base station is determined using the set of bits.

38. The apparatus of claim 34, wherein the instructions are further executable by the processor to cause the apparatus to:

select between a first lookup table mapping the set of bits to the joint redundancy version and a second lookup table mapping the set of bits to a combination of the first redundancy version and the second redundancy version, wherein the identifying is based at least in part on the selecting.

39. The apparatus of claim 38, wherein the first lookup table, the second lookup table, or a combination thereof is pre-configured in the memory.

40. The apparatus of claim 38, wherein the instructions are further executable by the processor to cause the apparatus to:

receive, from the base station, a configuration of the first lookup table, the second lookup table, or a combination thereof.

41. The apparatus of claim 34, wherein the joint redundancy version or the first redundancy version and the second redundancy version are identified based at least in part on a lookup table mapping the set of bits to one or more redundancy versions.

42. The apparatus of claim 41, wherein the lookup table is pre-configured in the memory.

43. The apparatus of claim 41, wherein the instructions are further executable by the processor to cause the apparatus to:

receive, from the base station, a configuration of the lookup table.

44. The apparatus of claim 34, wherein the rate matching procedure comprises separate rate matching, and the instructions are further executable by the processor to cause the apparatus to:

identify, using the set of bits and based at least in part on the rate matching procedure, the first redundancy version for the first transmission configuration indicator state and the second redundancy version for the second transmission configuration indicator state for each slot of a plurality of slots in a slot aggregation procedure.