

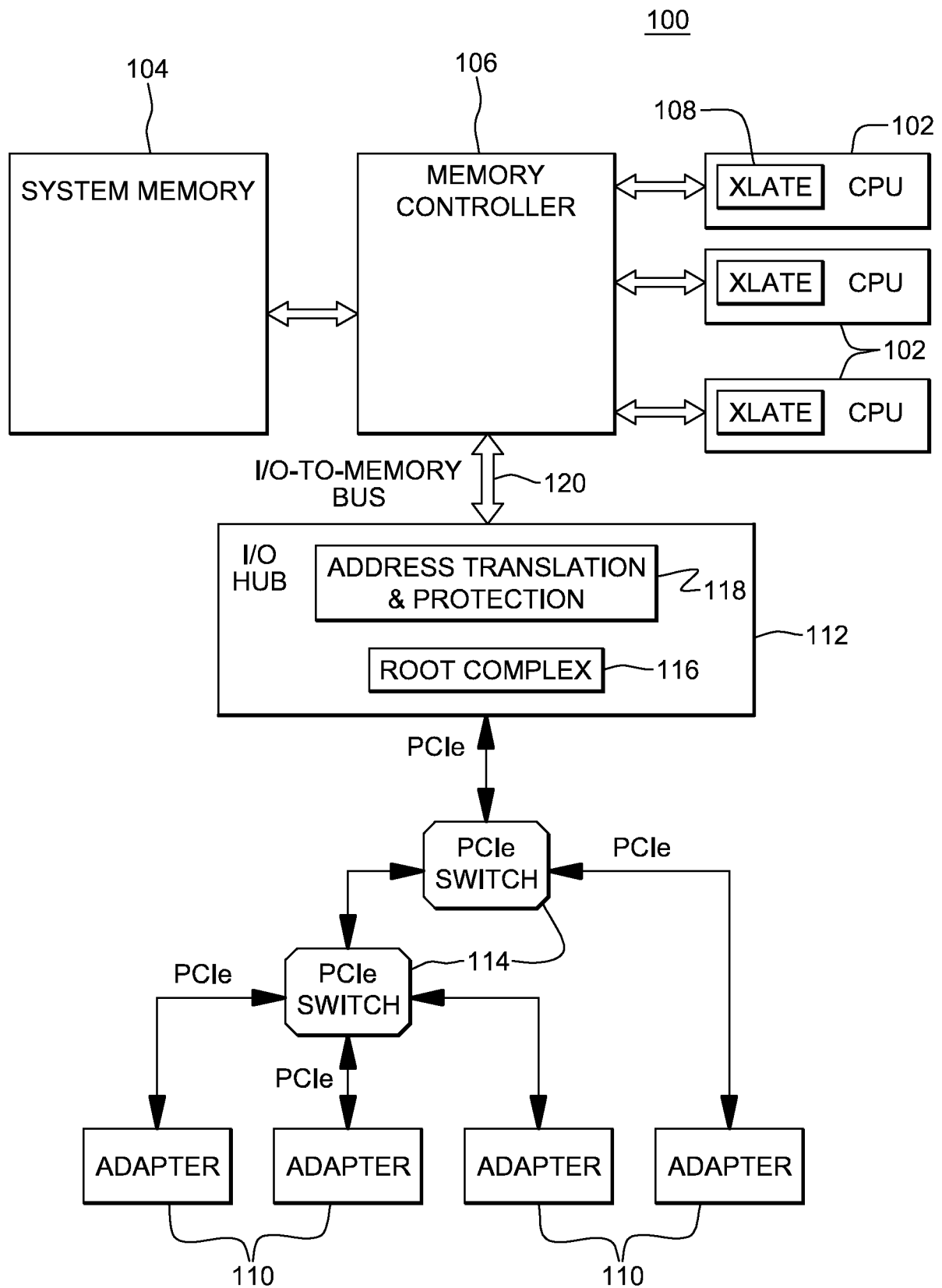


FIG. 1

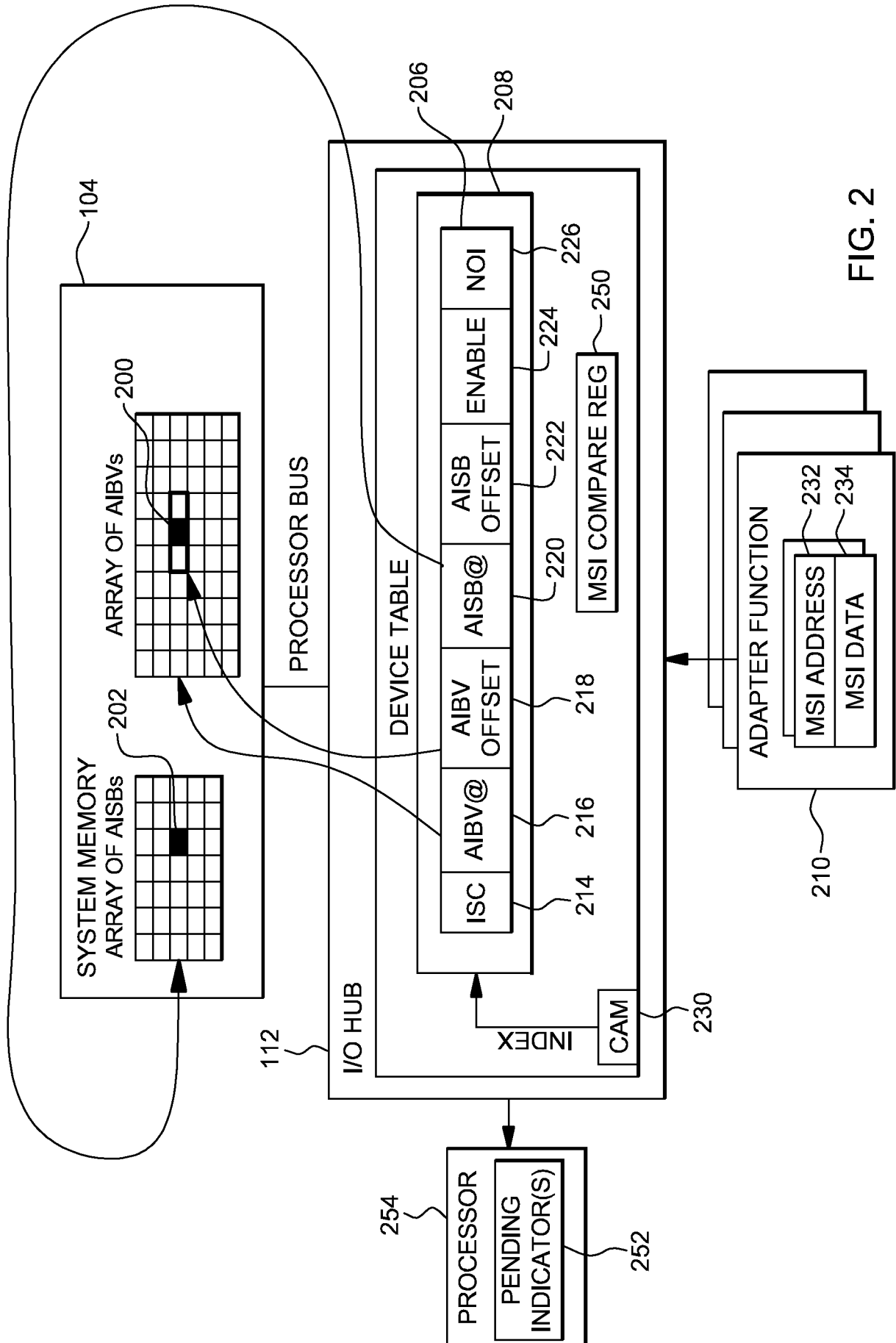


FIG. 2

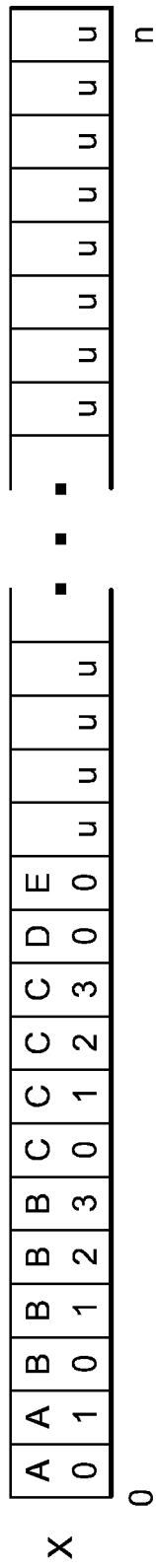


FIG. 3A

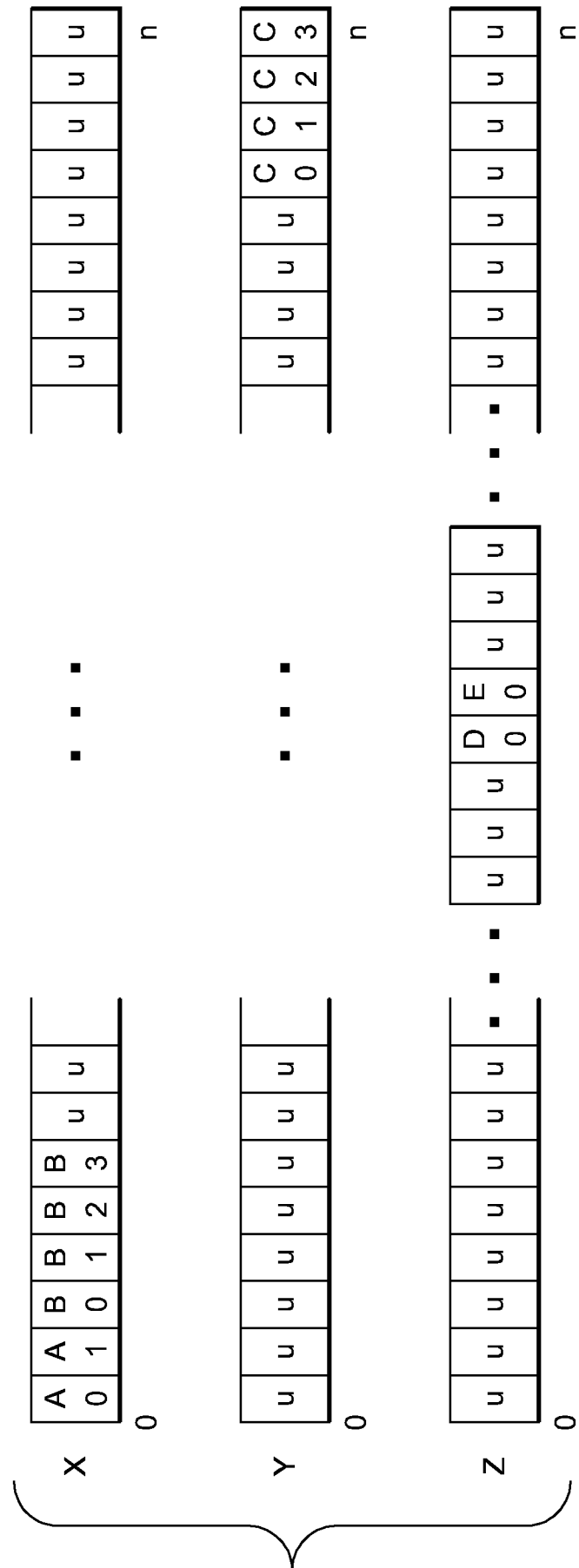


FIG. 3B

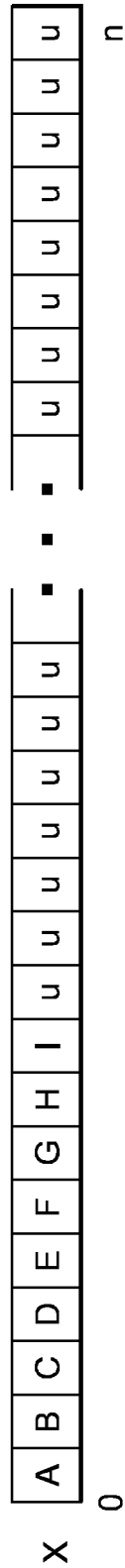


FIG. 3C

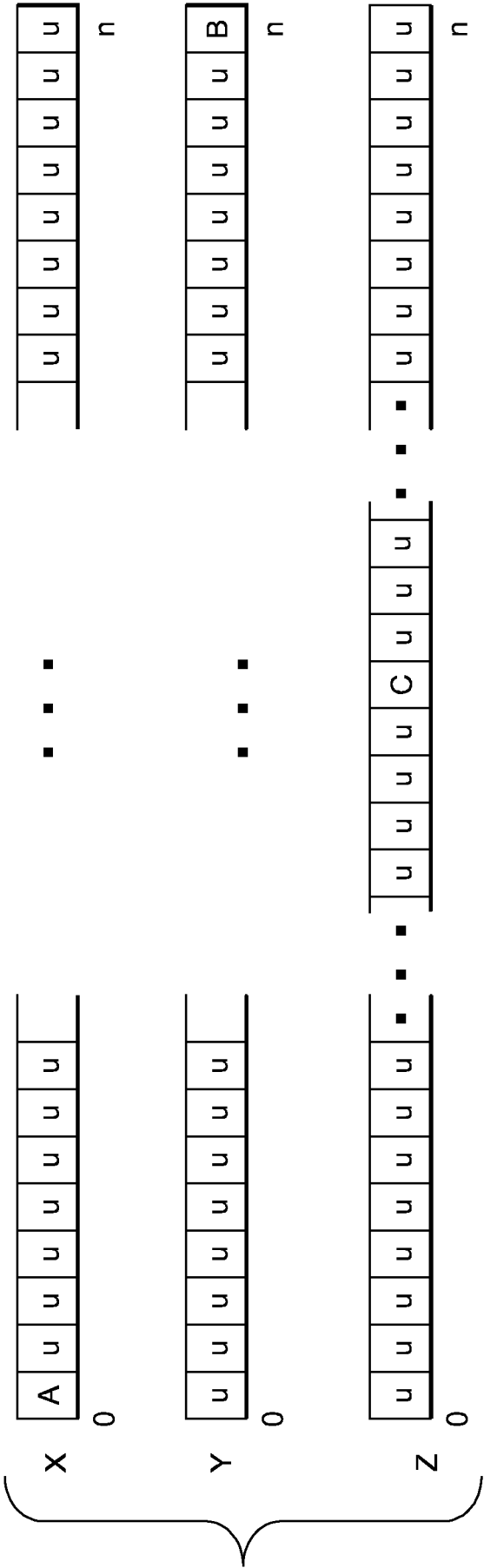


FIG. 3D

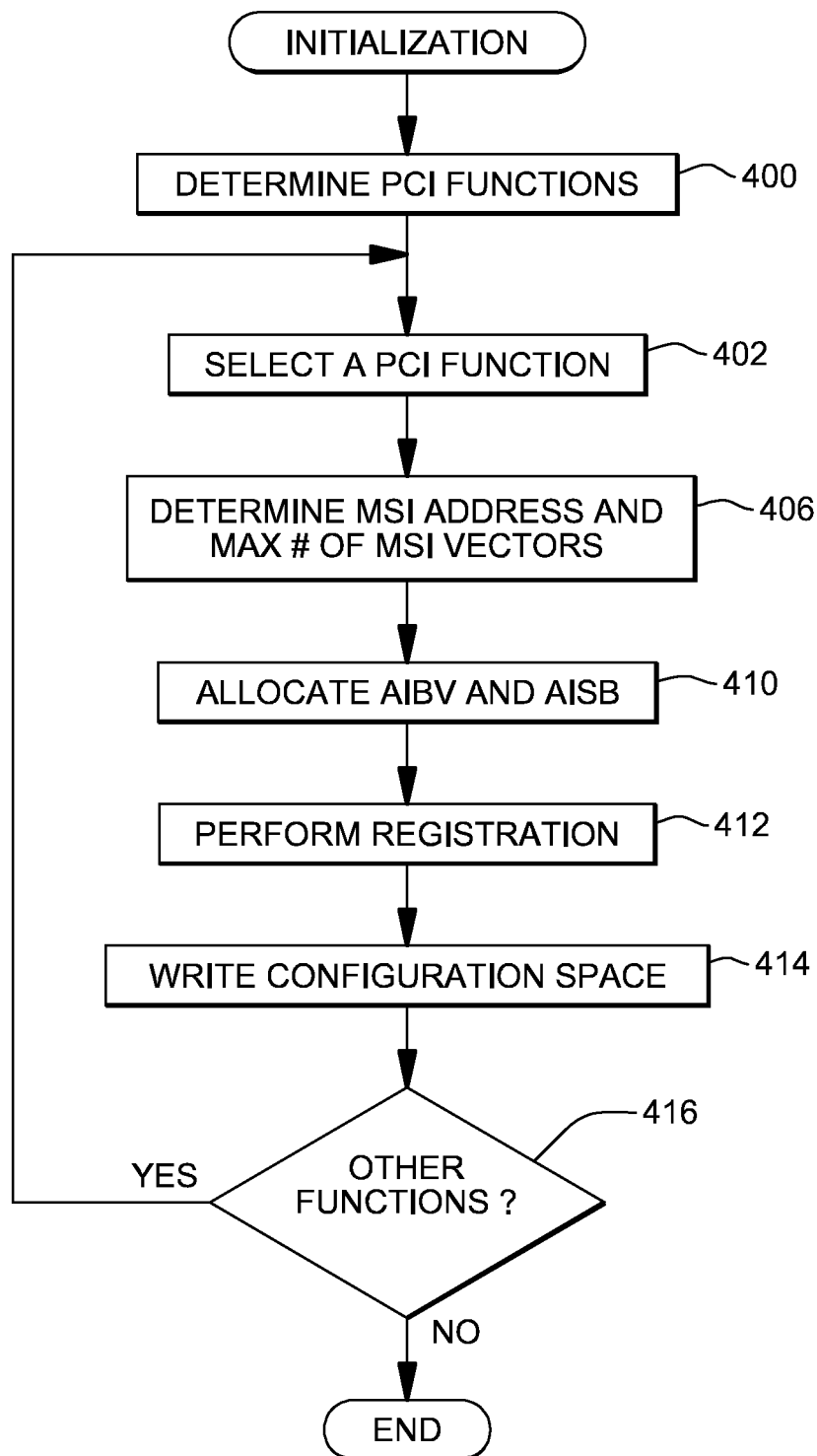


FIG. 4

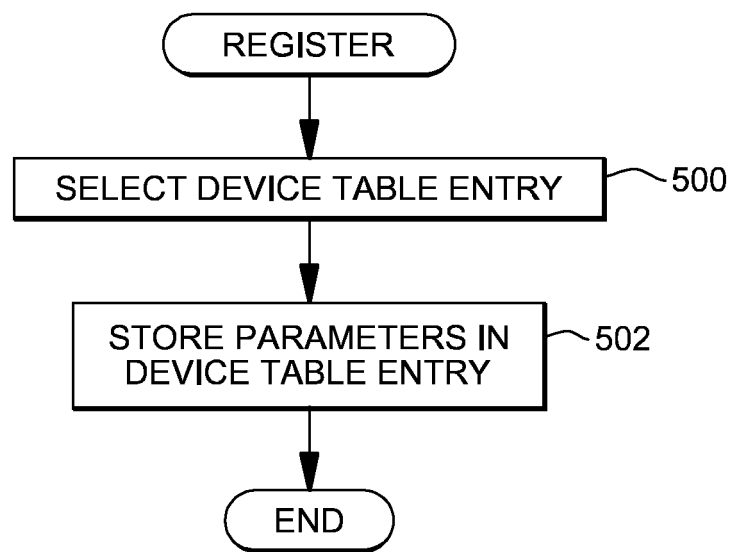


FIG. 5

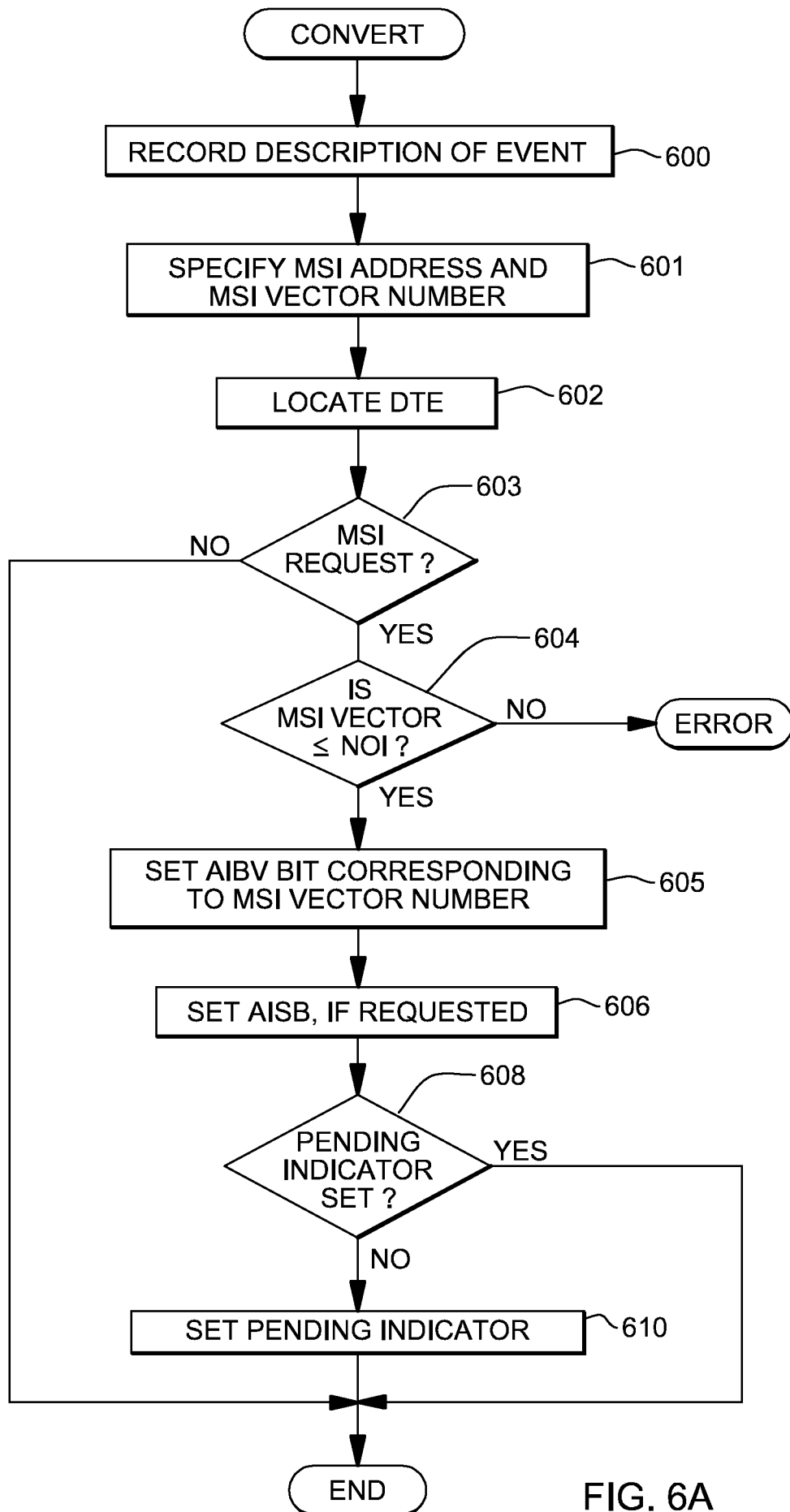


FIG. 6A

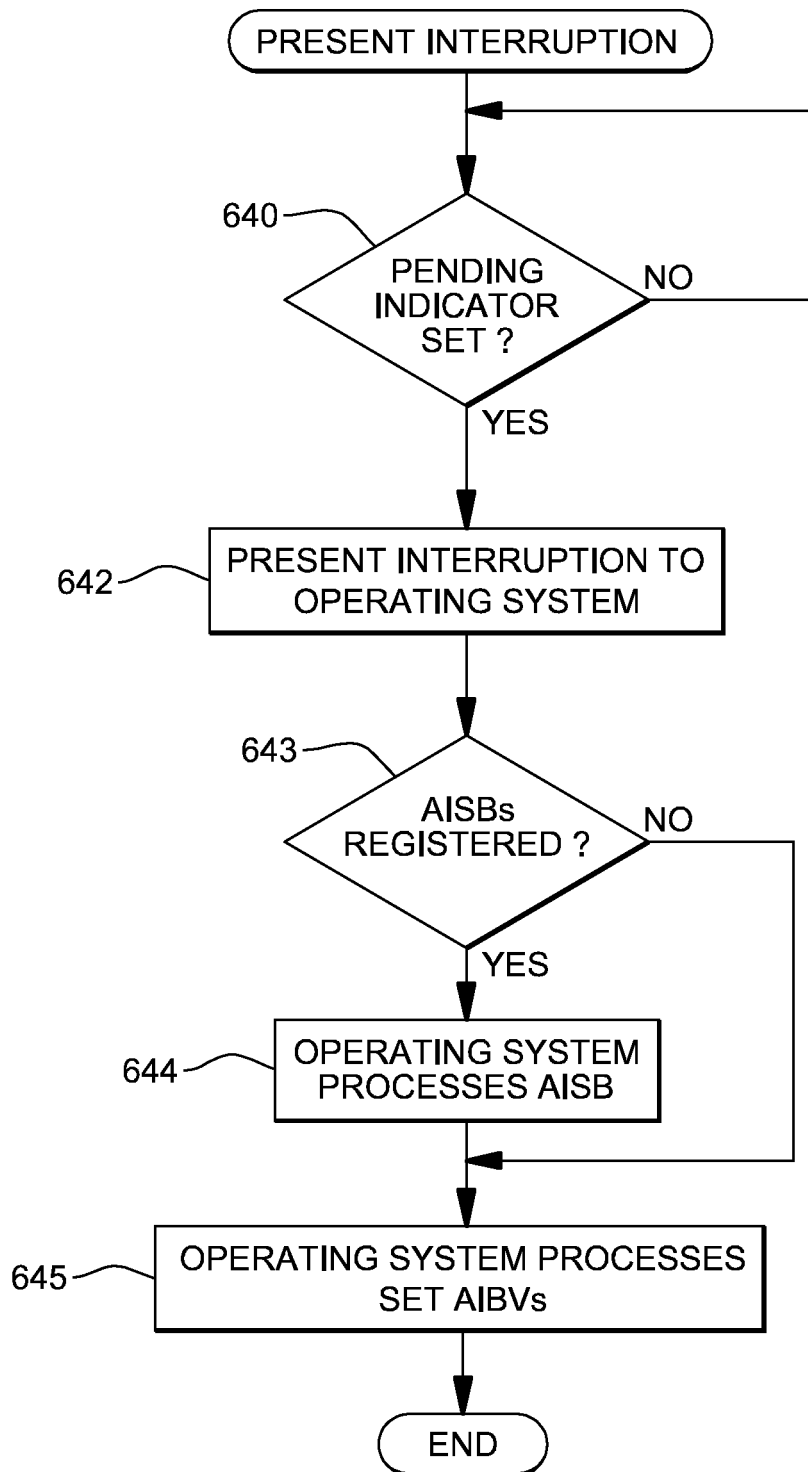


FIG. 6B

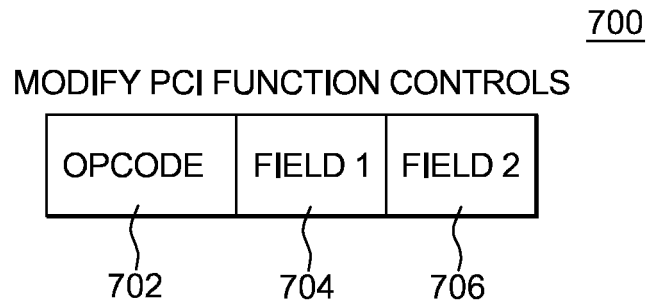


FIG. 7A



FIG. 7B



FIG. 7C

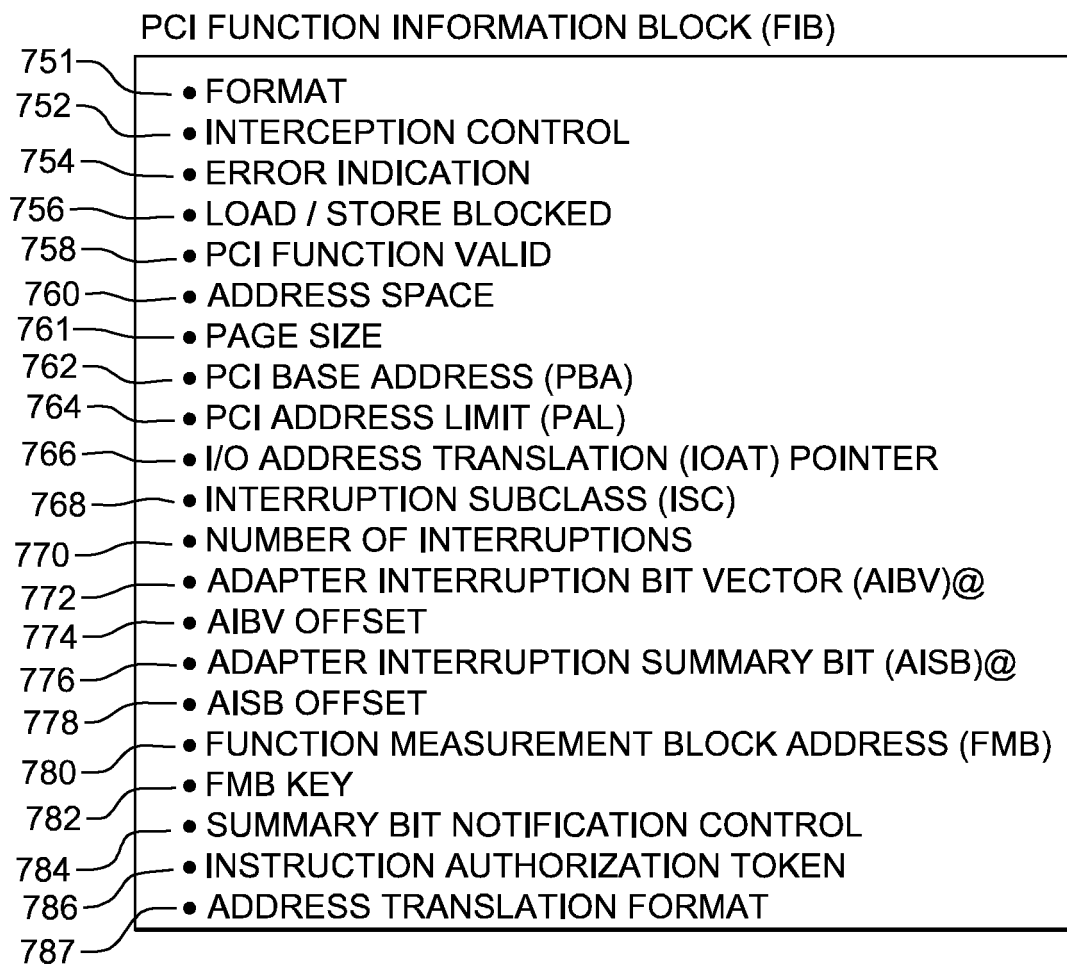
750

FIG. 7D

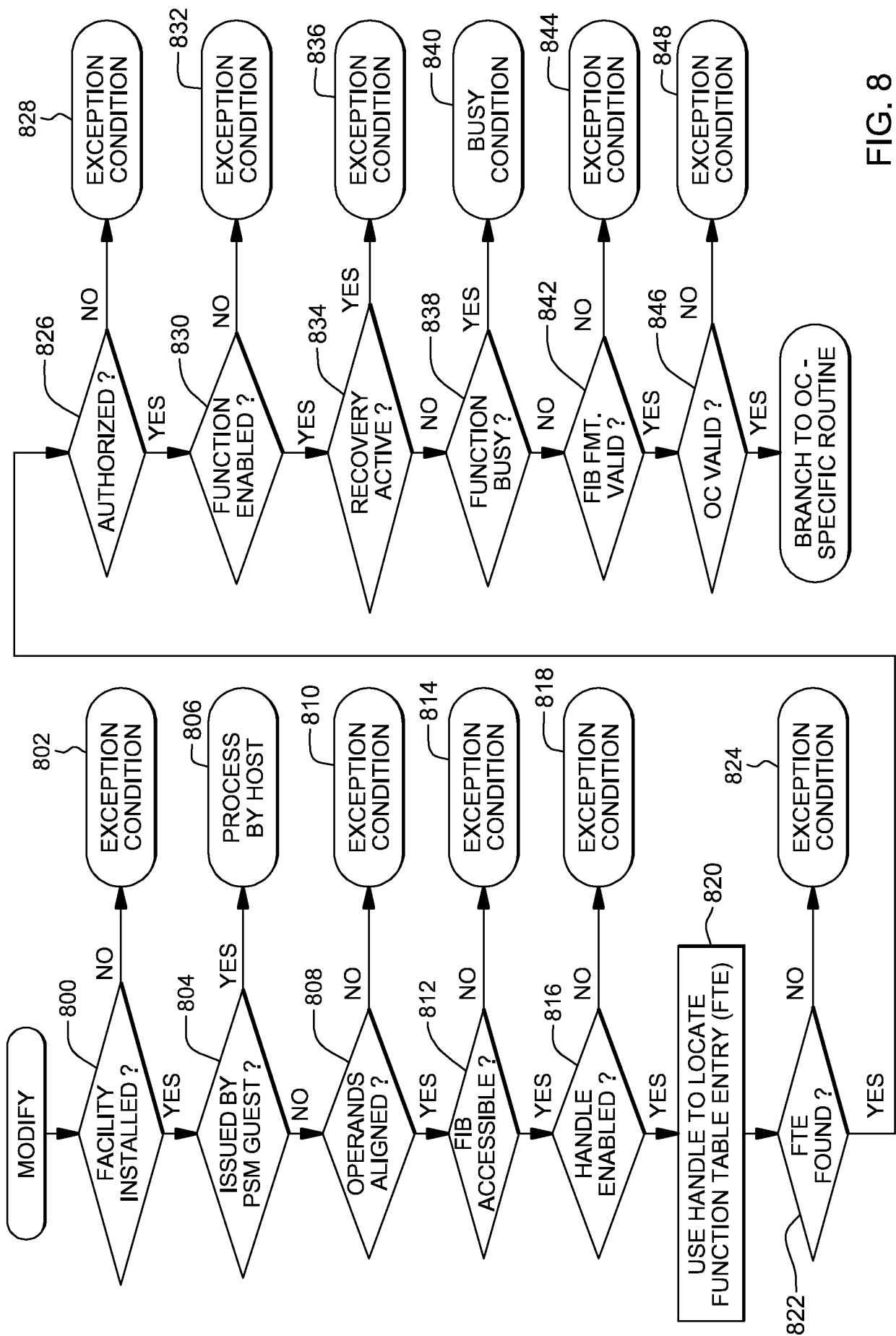


FIG. 8

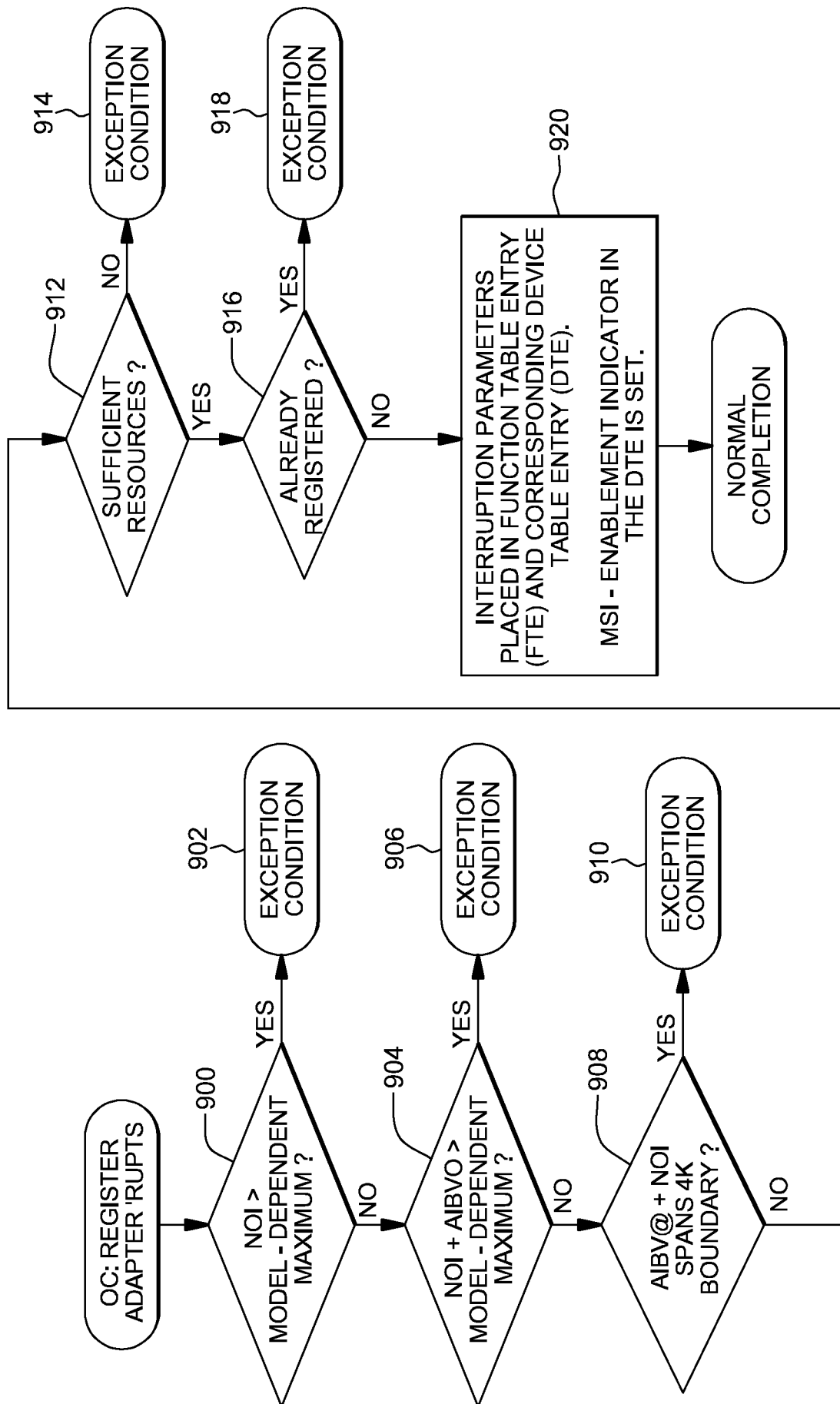


FIG. 9

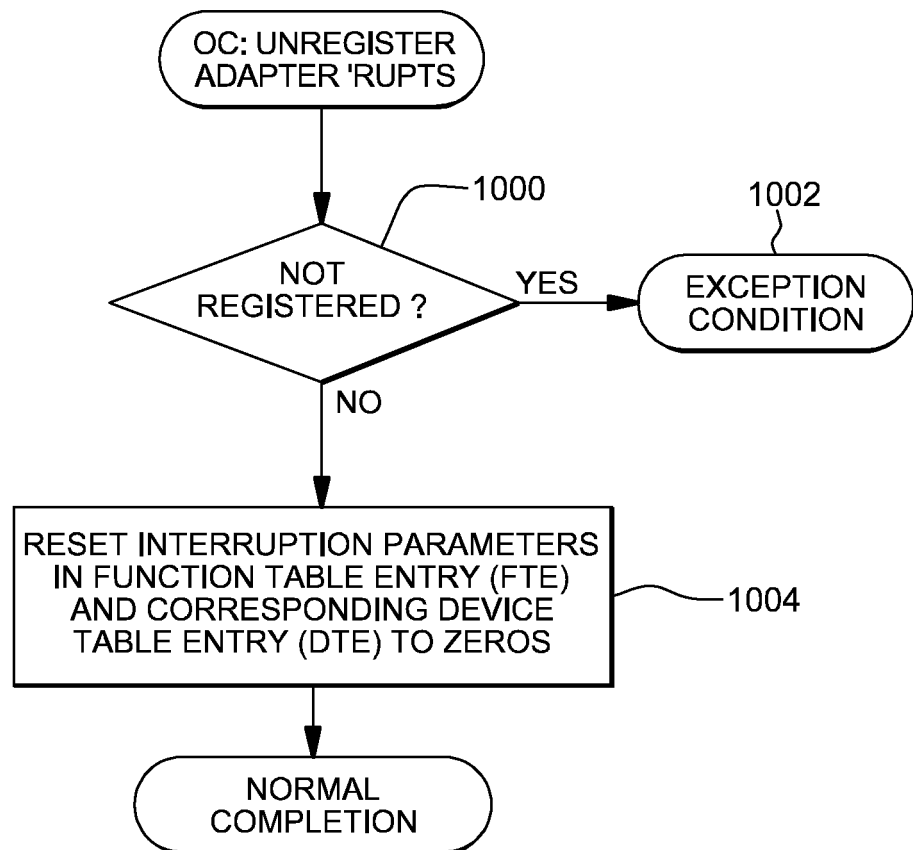


FIG. 10

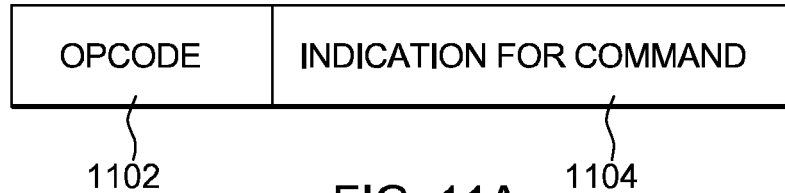
CALL LOGICAL PROCESSOR (CLP) INSTRUCTION 1100

FIG. 11A

REQUEST BLOCK FOR LIST 1120

FIG. 11B

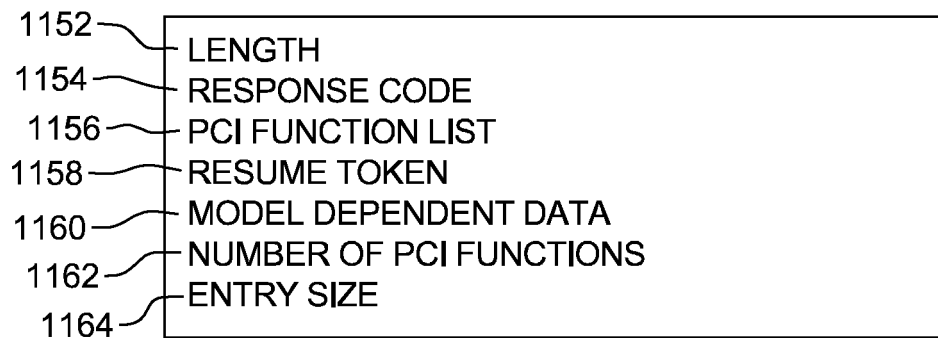
RESPONSE BLOCK FOR LIST 1150

FIG. 11C

PCI FUNCTION LIST ENTRY 1156

FIG. 11D

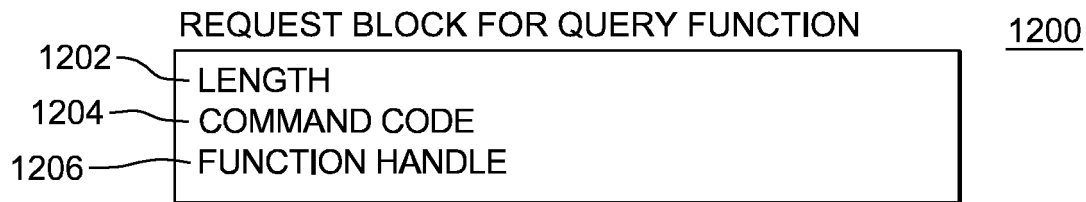


FIG. 12A

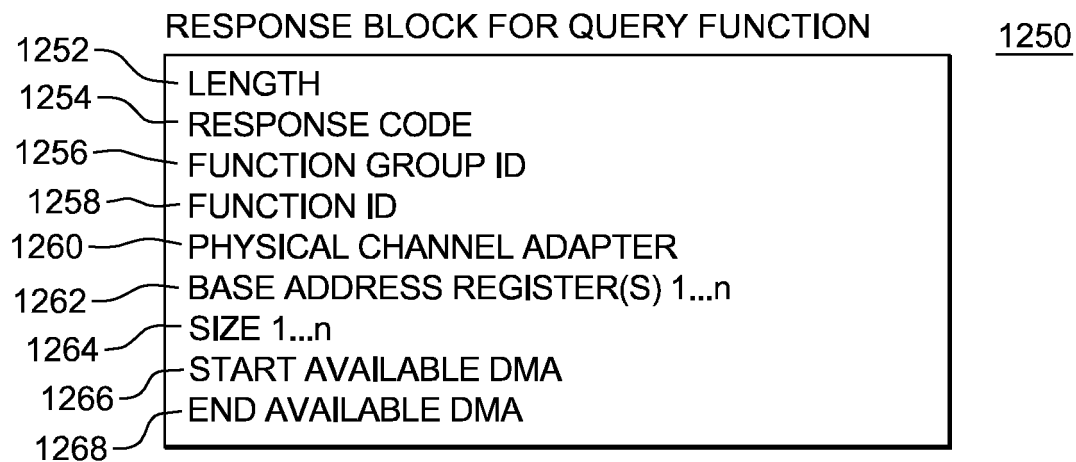


FIG. 12B

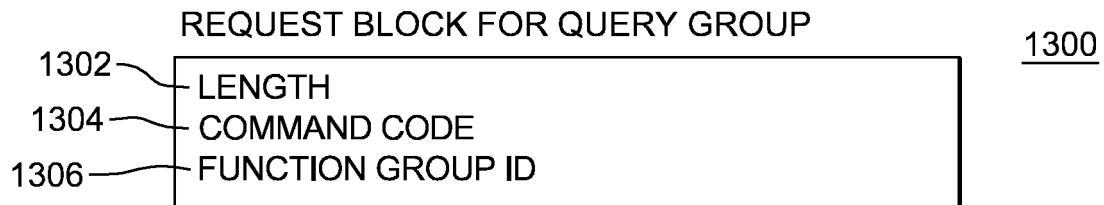


FIG. 13A

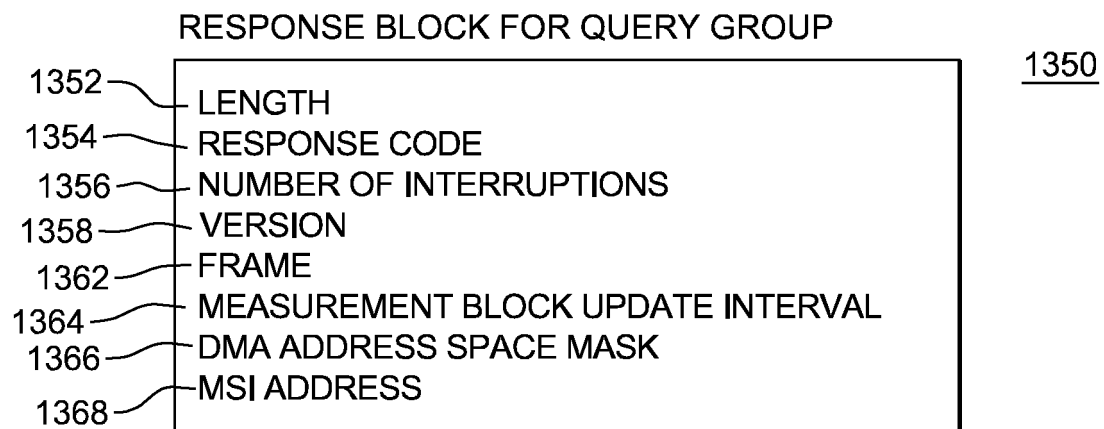


FIG. 13B

COMPUTER
PROGRAM
PRODUCT
1400

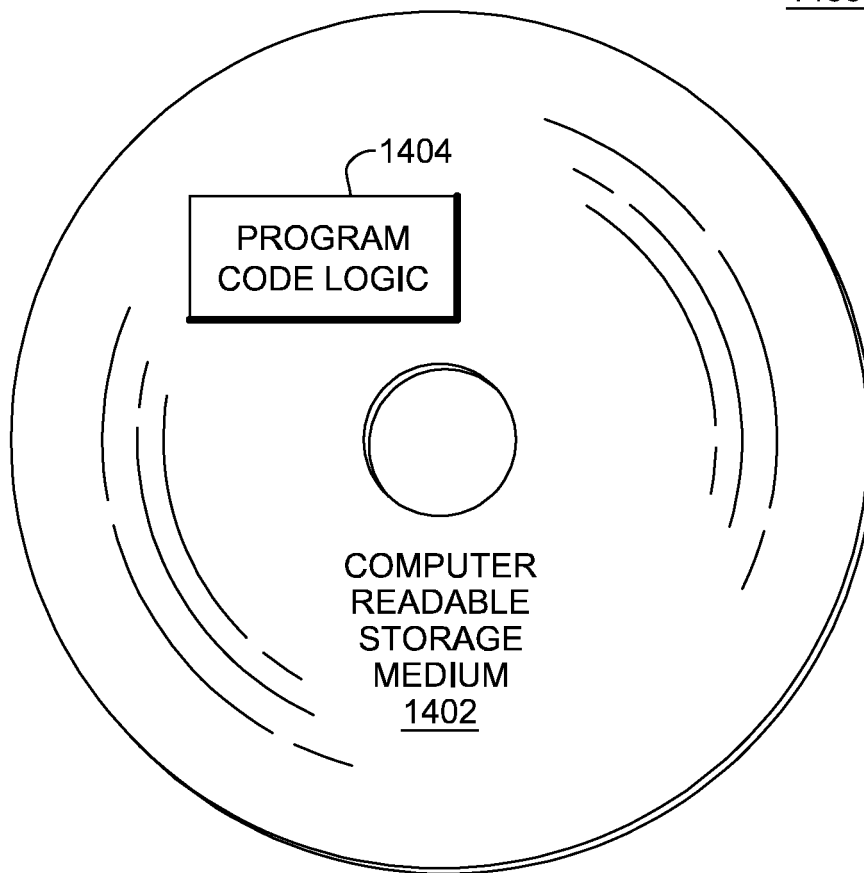


FIG. 14

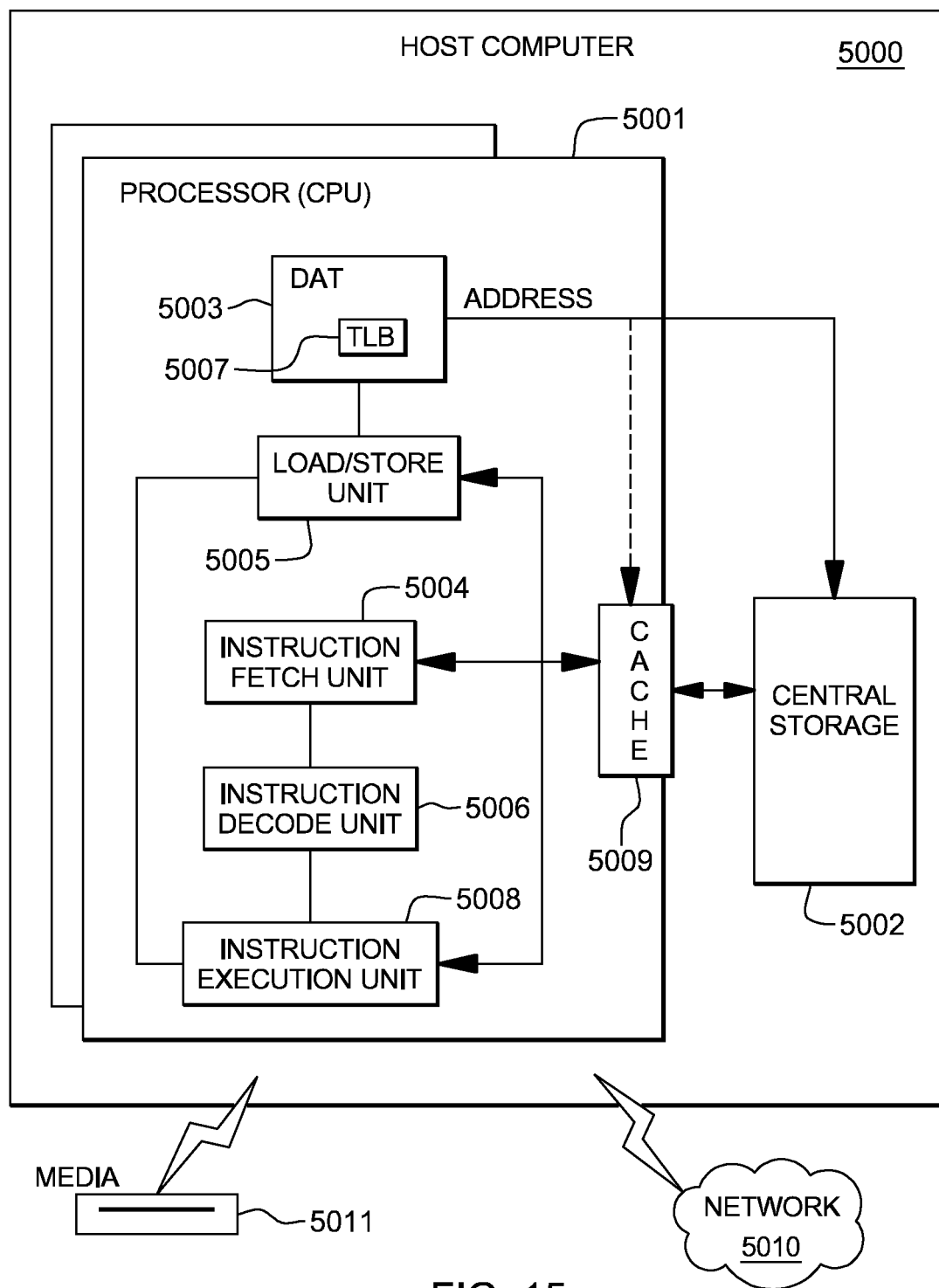


FIG. 15

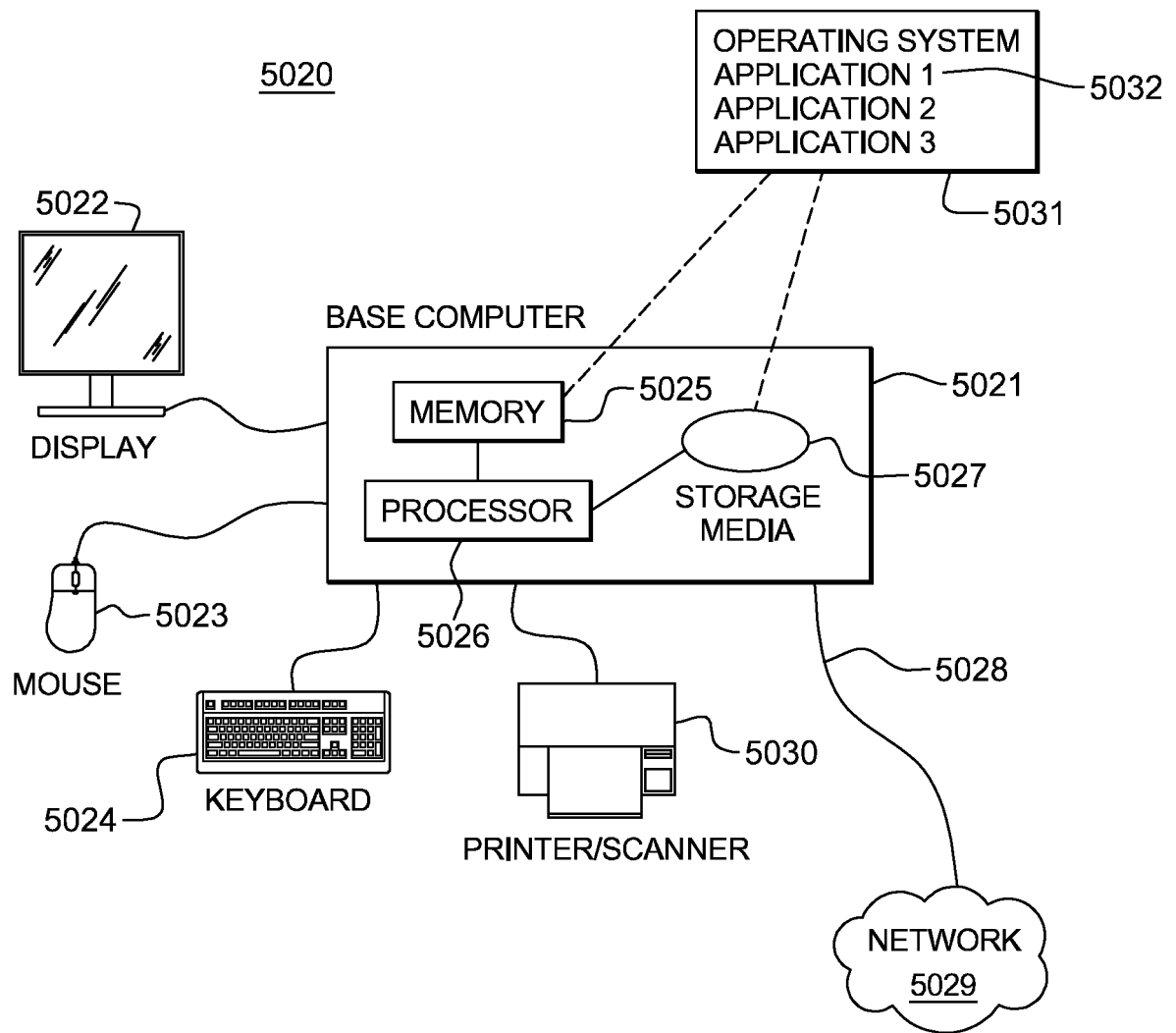


FIG. 16

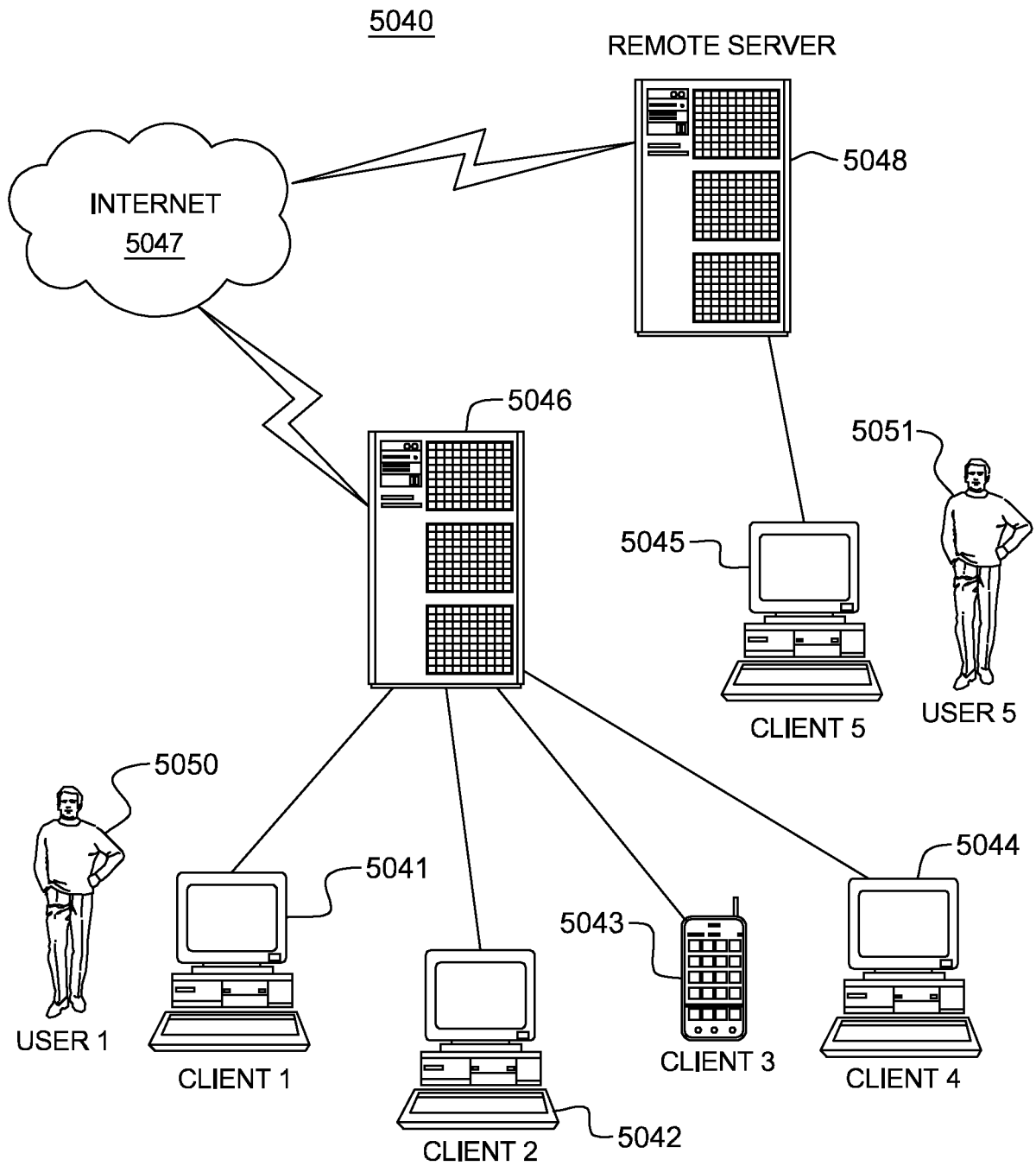


FIG. 17

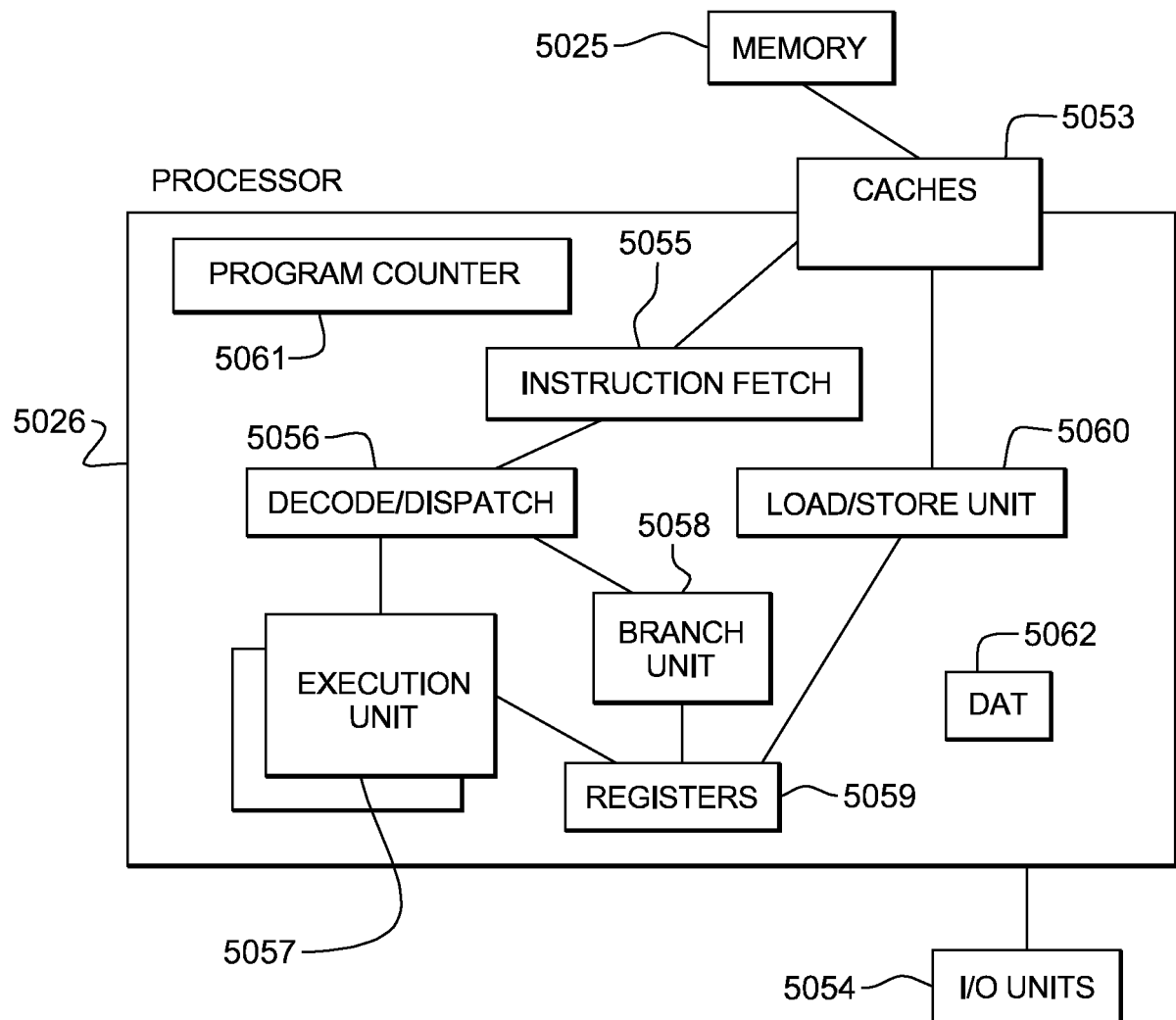


FIG. 18

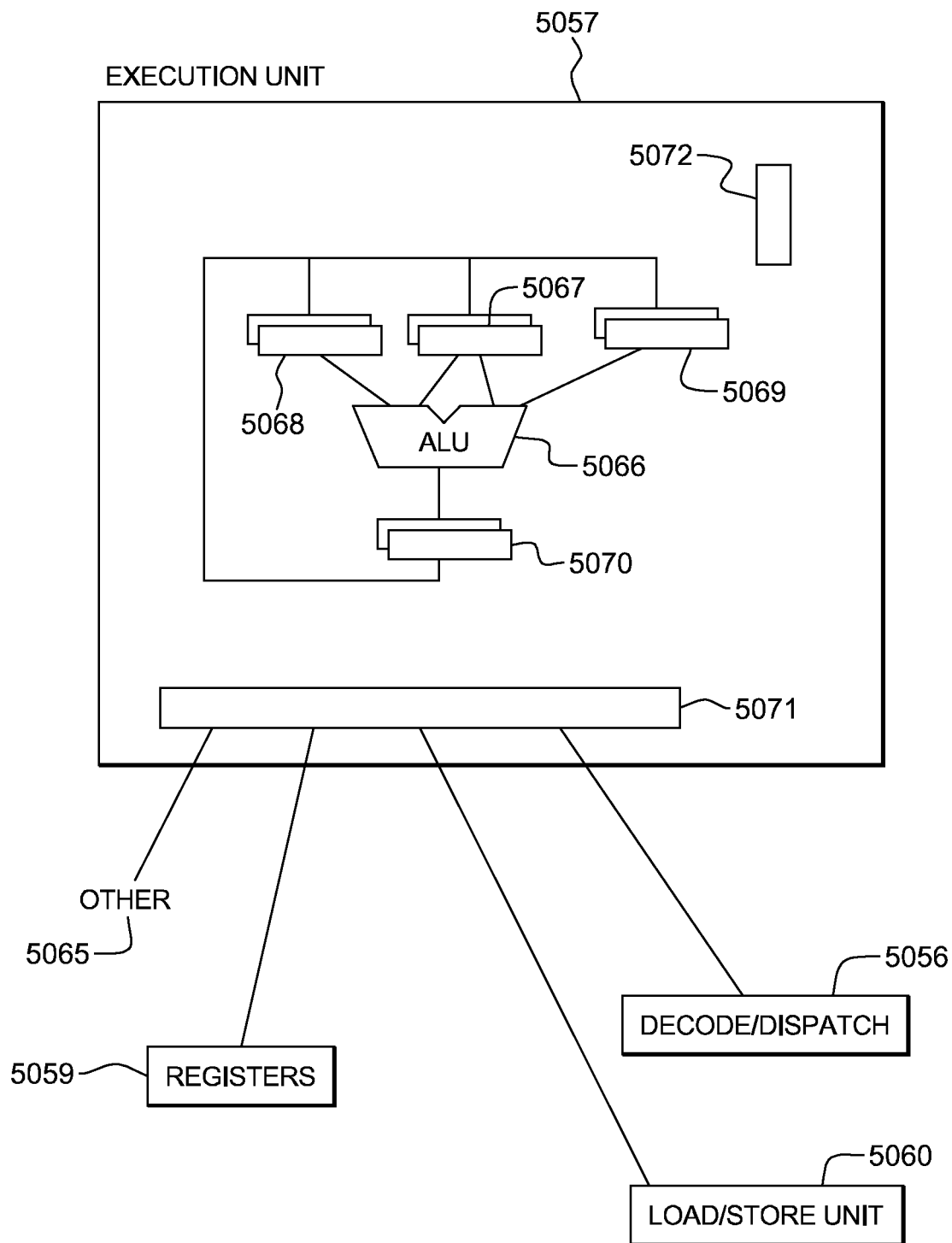


FIG. 19A

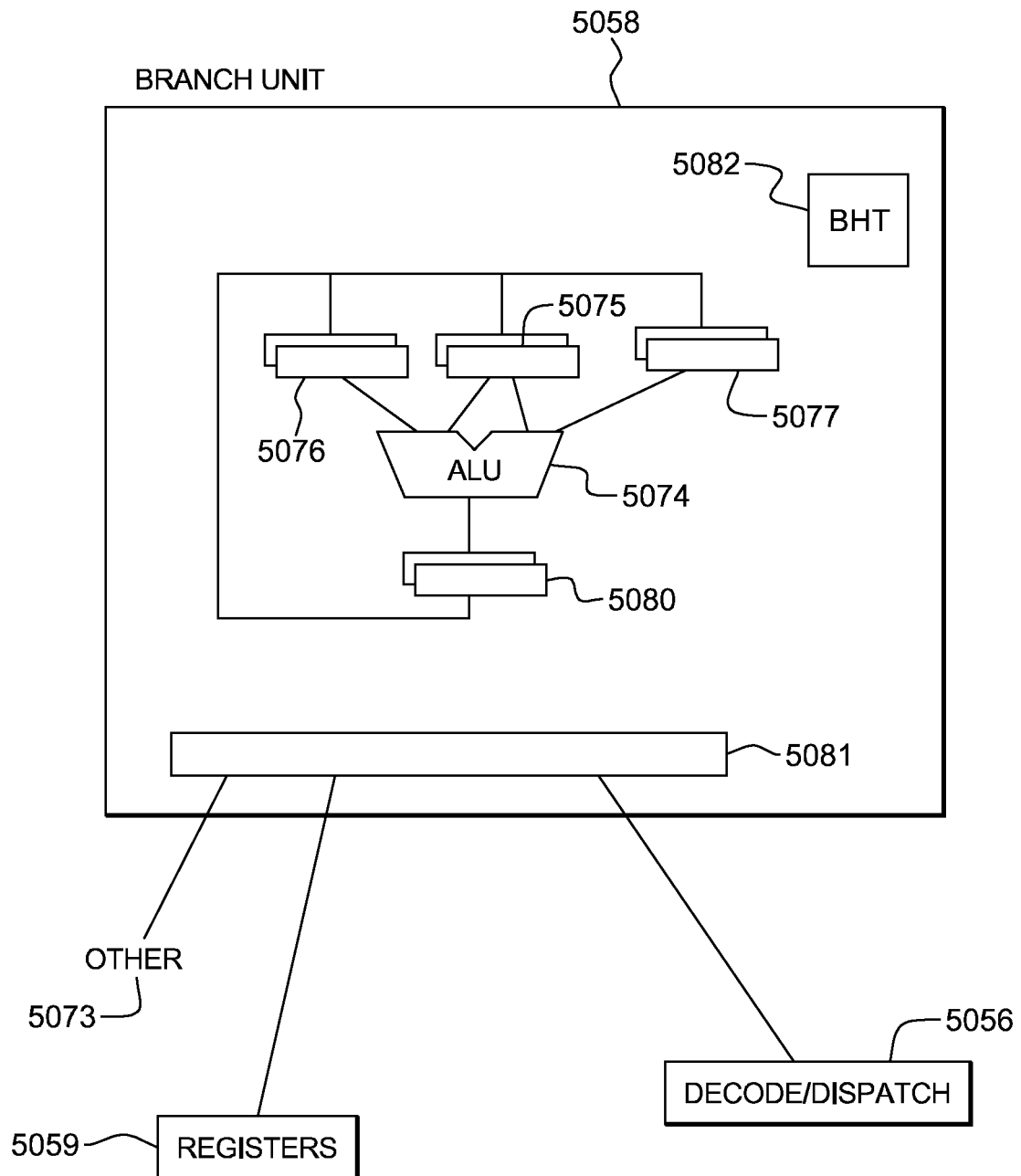


FIG. 19B

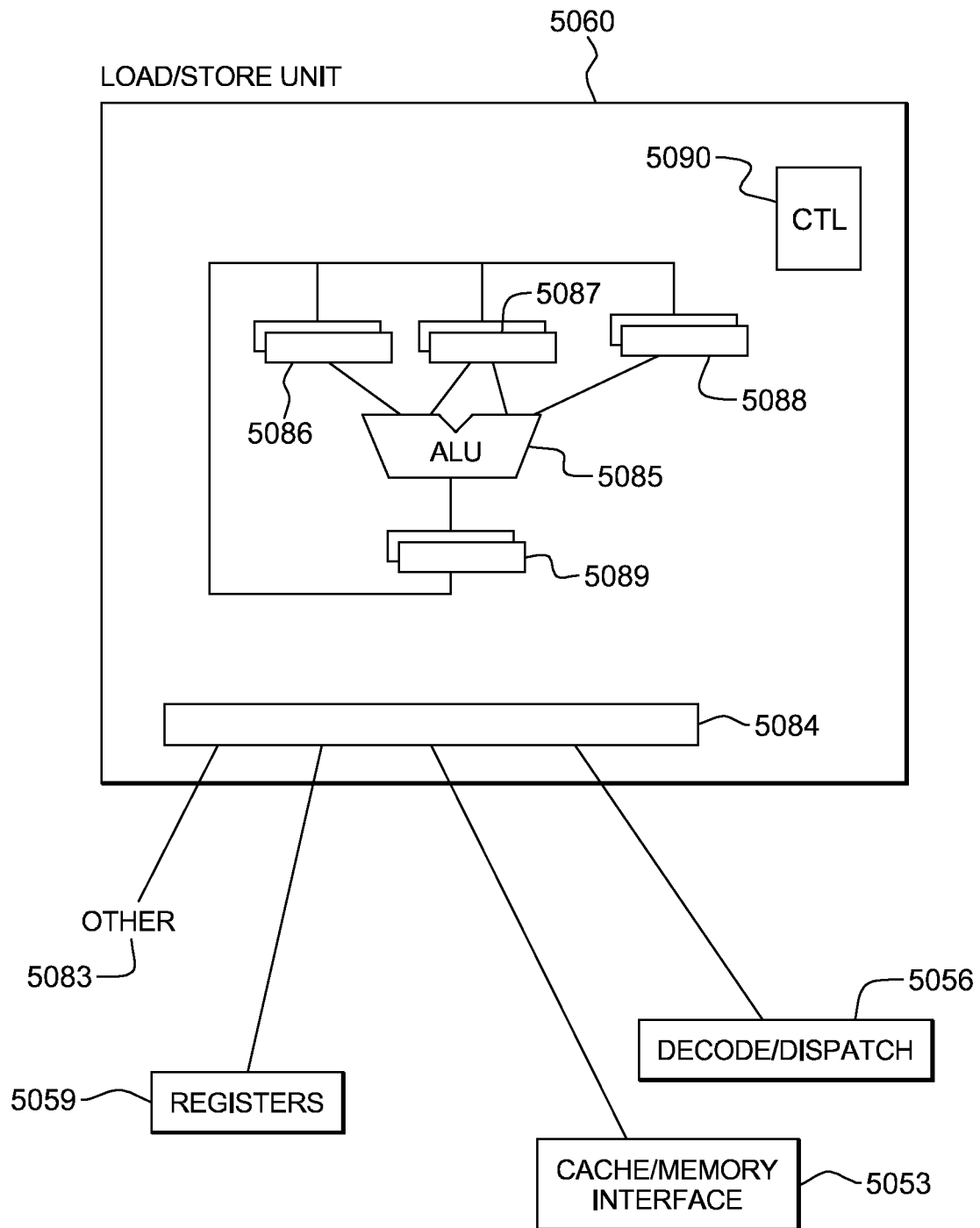


FIG. 19C

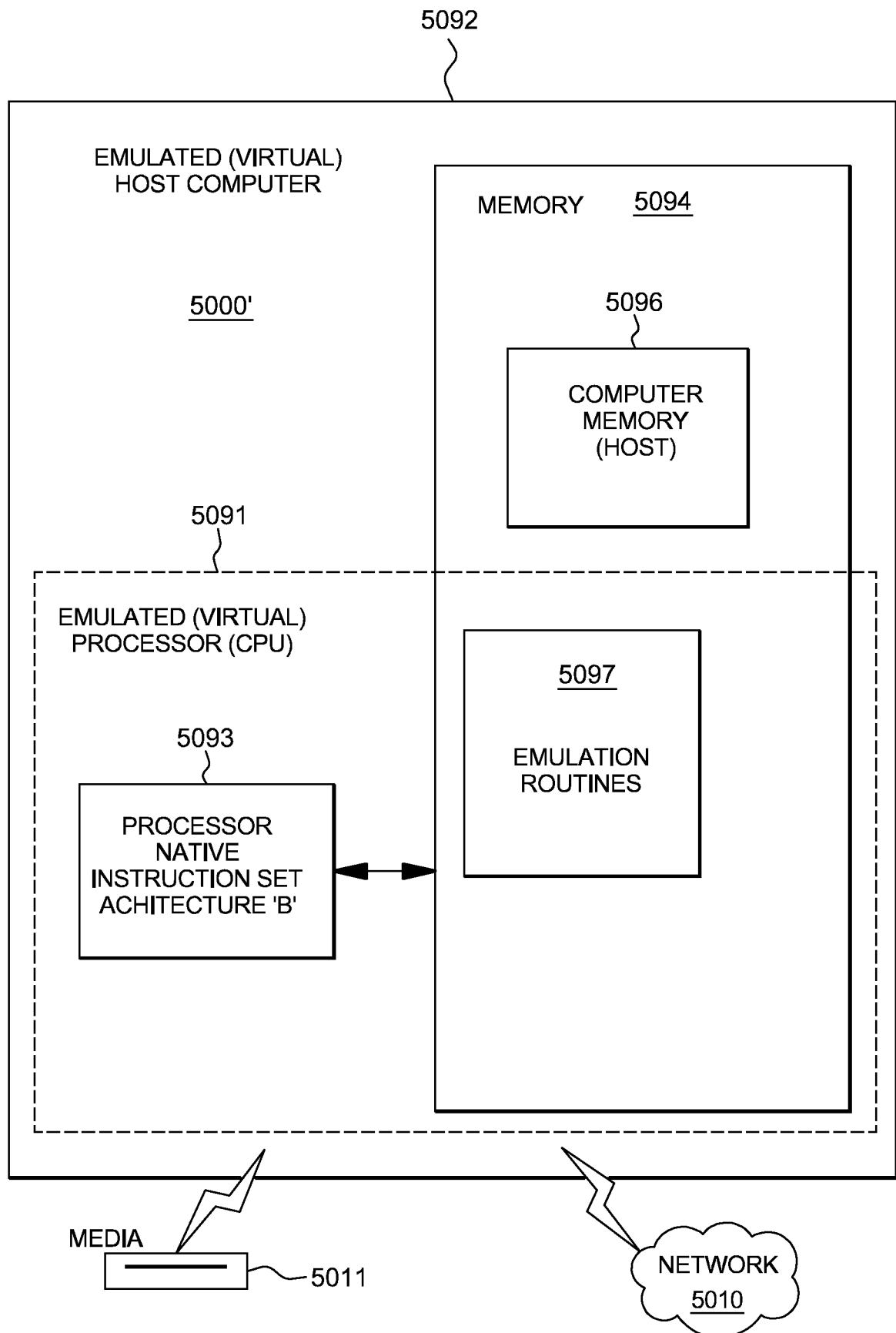


FIG. 20