

Abstract

Monolithic High Voltage Driver Circuit

5

A monolithic high voltage driver circuit (16) includes a main amplifier (10) coupled to receive an input signal via an input buffer stage (15). The output from the main amplifier (10) is coupled to an active pull-up stage (11). The input to the main
10 amplifier (10) is also coupled to an active pull-down stage (12), whose output is coupled to the output of the active pull-up stage (11) to provide an output signal V_{OUT} , and to the input of a Black Level clamping stage (13), whose output is coupled to the input signal supplied to the input buffer stage (15). The pull-down
15 output stage (12) is formed of a current mirror having an input stage having a static input coupled to a reference voltage source and an output stage coupled to the output of the input stage via an impedance and having a dynamic input coupled to receive the input to the main amplifier and an output to provide the output
20 signal from the circuit.

FIG. 3.