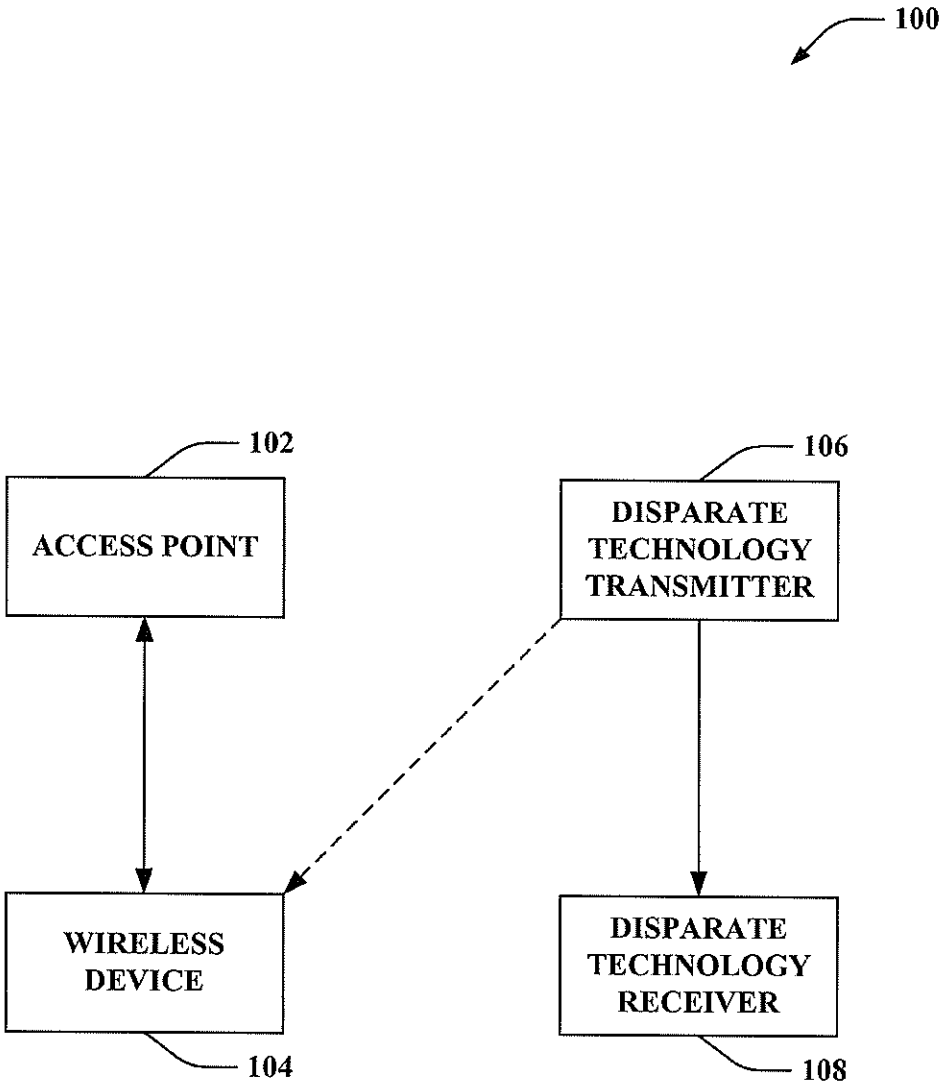

100
102
ACCESS POINT
106
DISPARATE TECHNOLOGY TRANSMITTER
WIRELESS DEVICE
104
DISPARATE TECHNOLOGY RECEIVER
108

FIG. 1

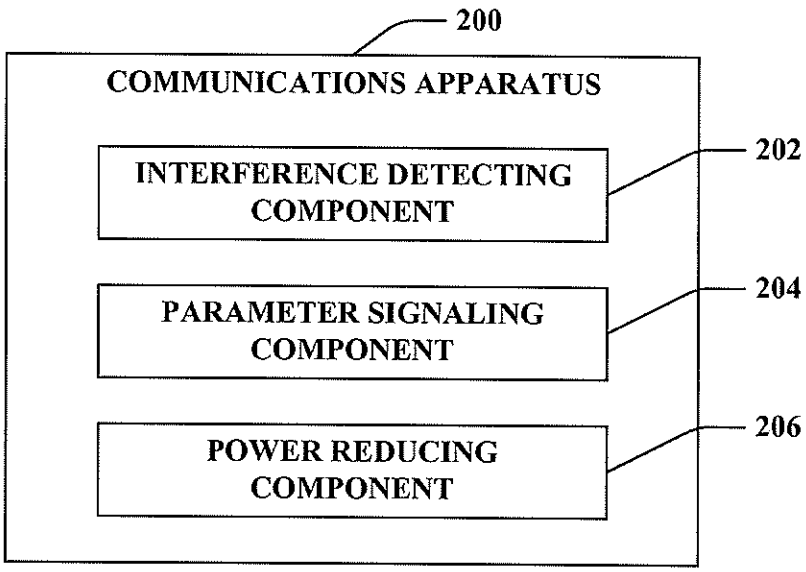

200
COMMUNICATIONS APPARATUS
INTERFERENCE DETECTING COMPONENT
202
PARAMETER SIGNALING COMPONENT
204
POWER REDUCING COMPONENT
206

FIG. 2

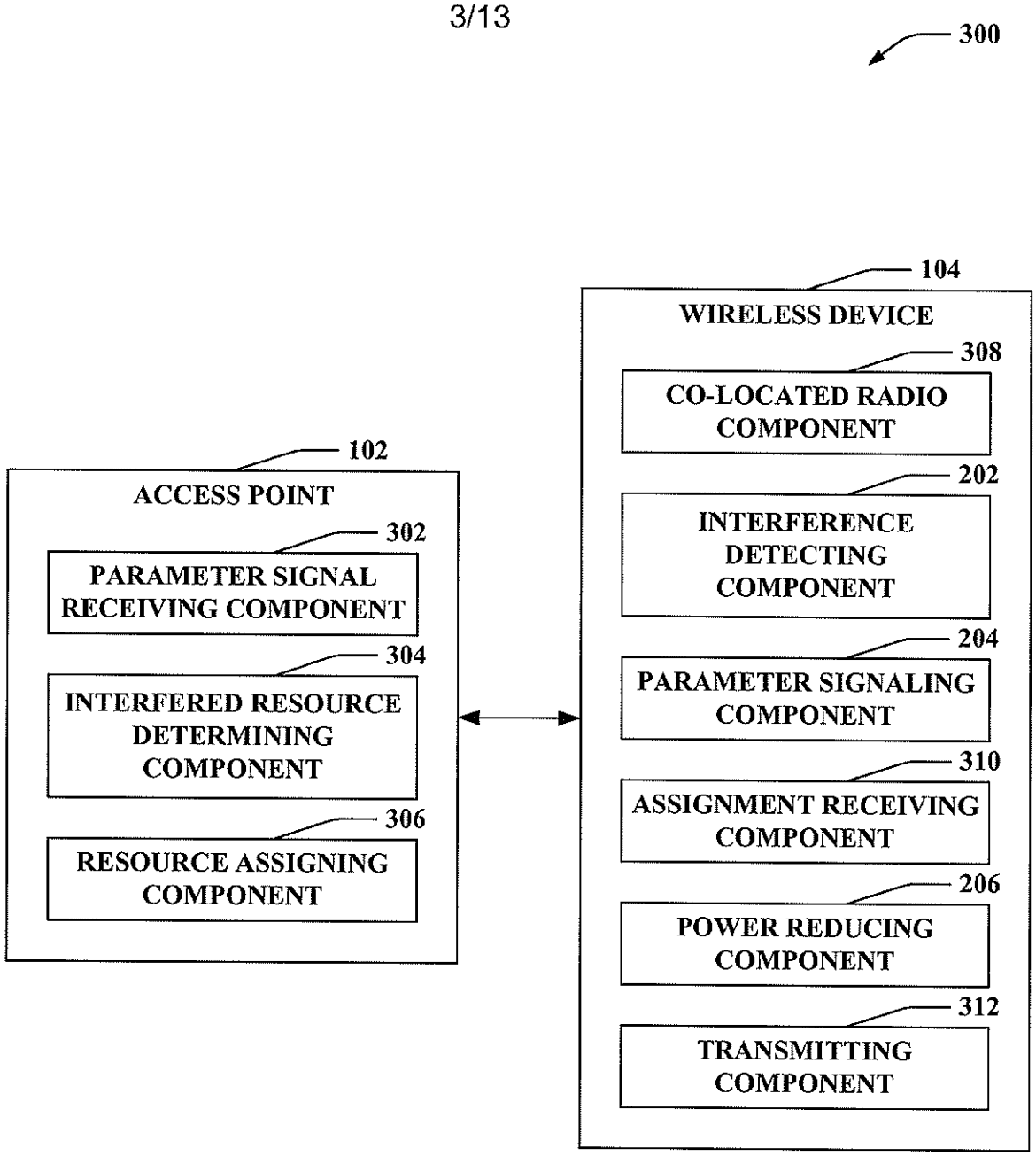

300
104
WIRELESS DEVICE
308
CO-LOCATED RADIO COMPONENT
202
INTERFERENCE DETECTING COMPONENT
204
PARAMETER SIGNALING COMPONENT
310
ASSIGNMENT RECEIVING COMPONENT
206
POWER REDUCING COMPONENT
312
TRANSMITTING COMPONENT
102
ACCESS POINT
302
PARAMETER SIGNAL RECEIVING COMPONENT
304
INTERFERED RESOURCE DETERMINING COMPONENT
306
RESOURCE ASSIGNING COMPONENT

FIG. 3

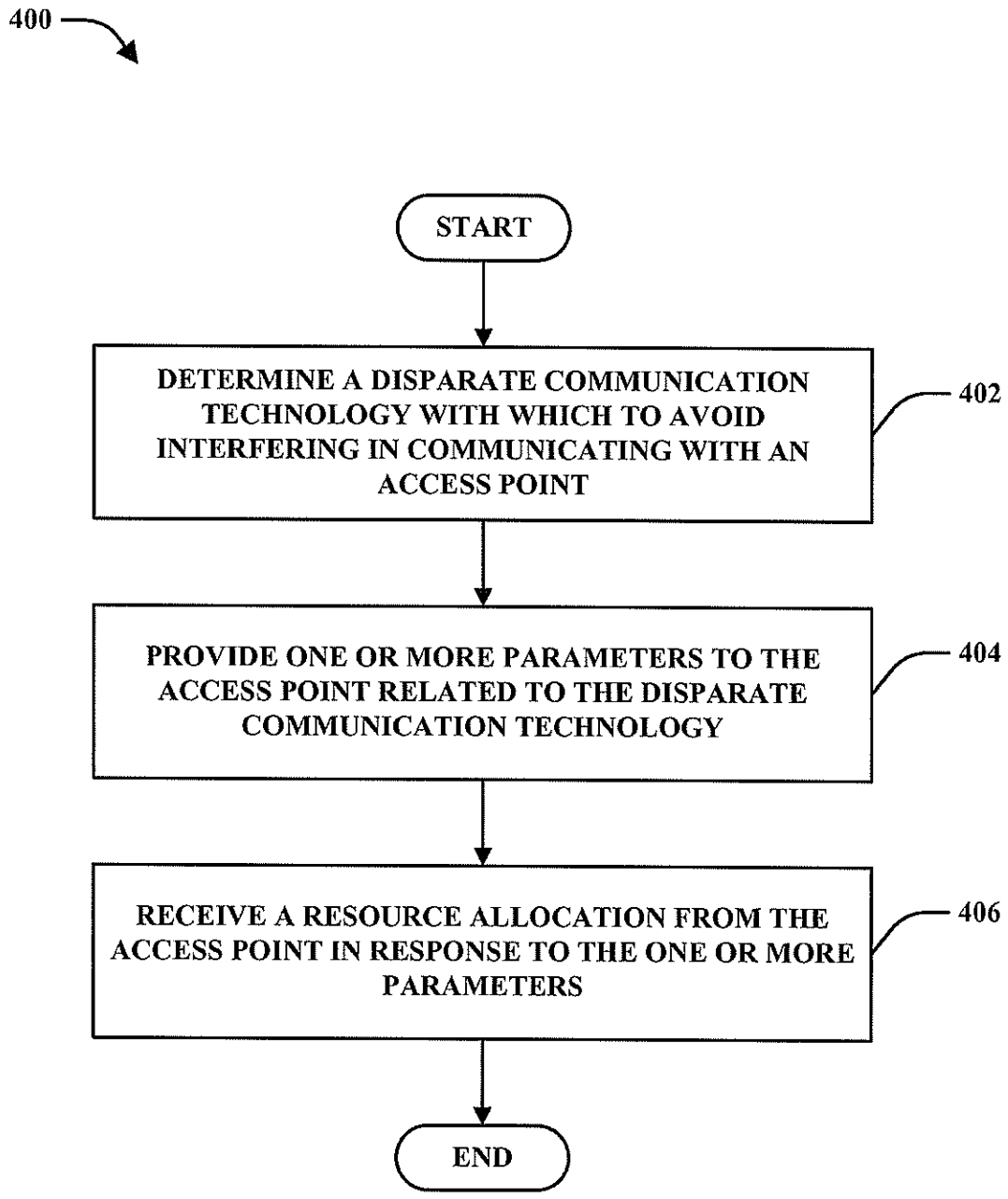

400
START
DETERMINE A DISPARATE COMMUNICATION TECHNOLOGY WITH WHICH TO AVOID INTERFERING IN COMMUNICATING WITH AN ACCESS POINT
402
PROVIDE ONE OR MORE PARAMETERS TO THE ACCESS POINT RELATED TO THE DISPARATE COMMUNICATION TECHNOLOGY
404
RECEIVE A RESOURCE ALLOCATION FROM THE ACCESS POINT IN RESPONSE TO THE ONE OR MORE PARAMETERS
406
END

FIG. 4

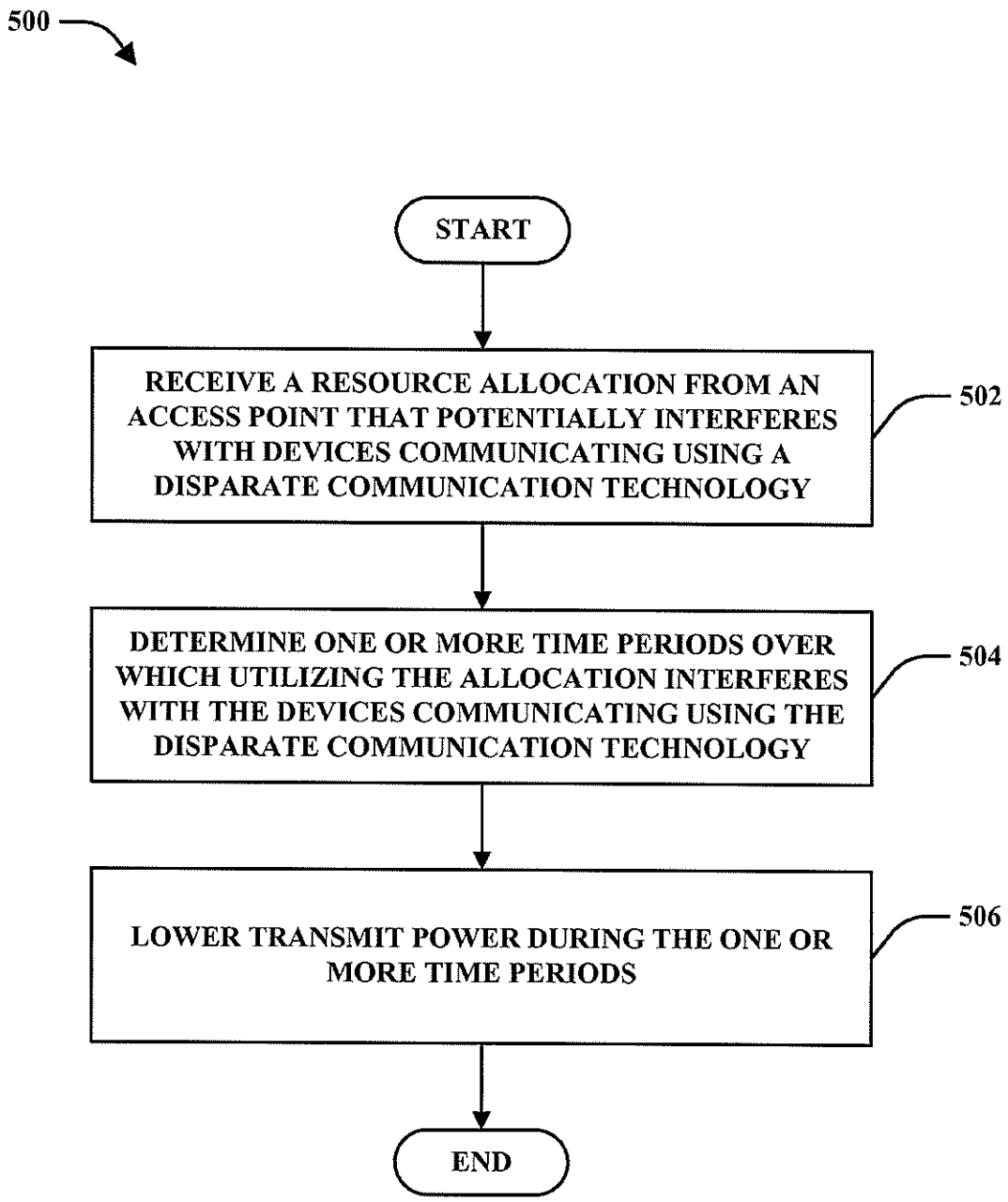

500
START
RECEIVE A RESOURCE ALLOCATION FROM AN ACCESS POINT THAT POTENTIALLY INTERFERES WITH DEVICES COMMUNICATING USING A DISPARATE COMMUNICATION TECHNOLOGY
502
DETERMINE ONE OR MORE TIME PERIODS OVER WHICH UTILIZING THE ALLOCATION INTERFERES WITH THE DEVICES COMMUNICATING USING THE DISPARATE COMMUNICATION TECHNOLOGY
504
LOWER TRANSMIT POWER DURING THE ONE OR MORE TIME PERIODS
506
END

FIG. 5

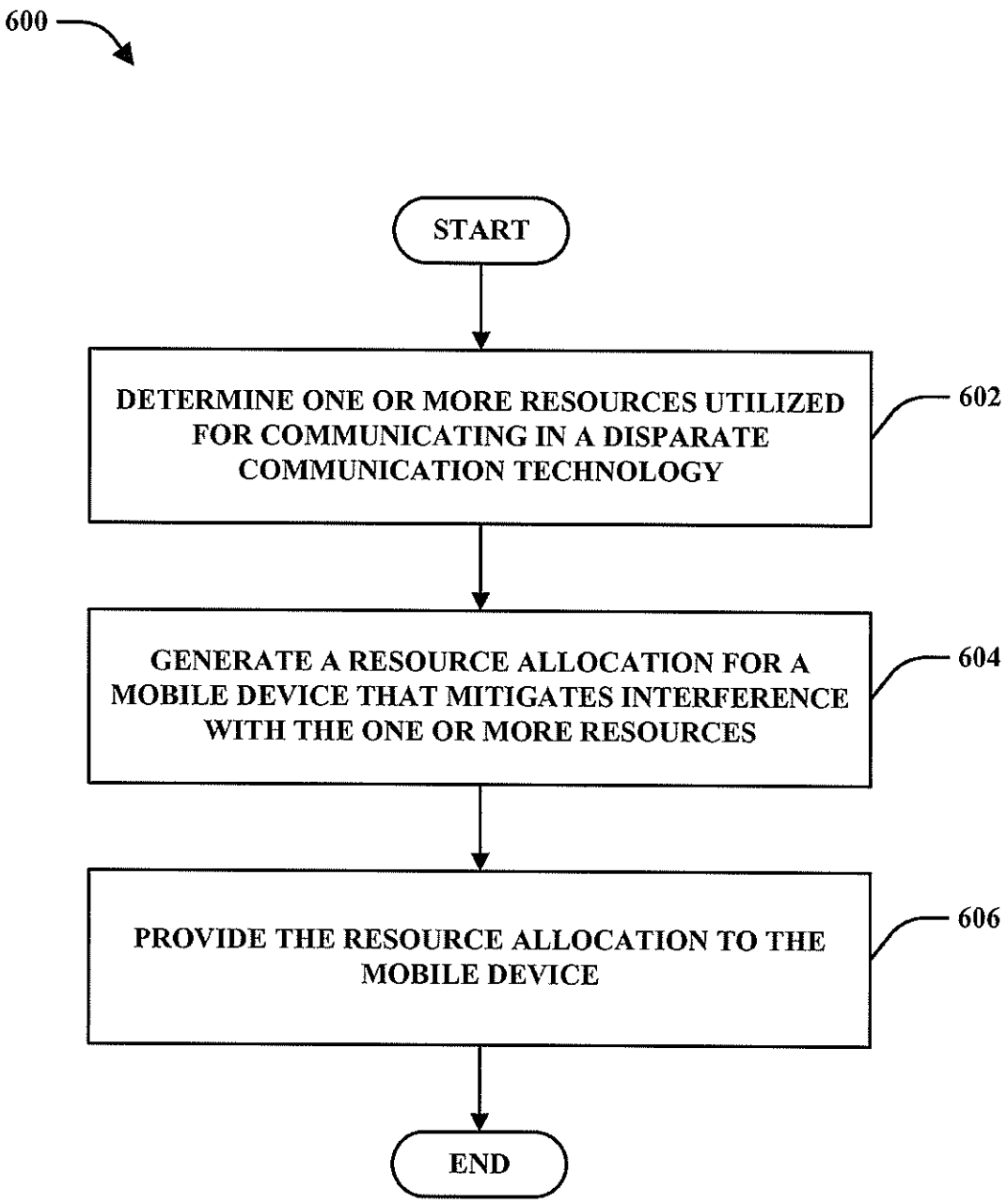

600
START
DETERMINE ONE OR MORE RESOURCES UTILIZED FOR COMMUNICATING IN A DISPARATE COMMUNICATION TECHNOLOGY
602
GENERATE A RESOURCE ALLOCATION FOR A MOBILE DEVICE THAT MITIGATES INTERFERENCE WITH THE ONE OR MORE RESOURCES
604
PROVIDE THE RESOURCE ALLOCATION TO THE MOBILE DEVICE
606
END

FIG. 6

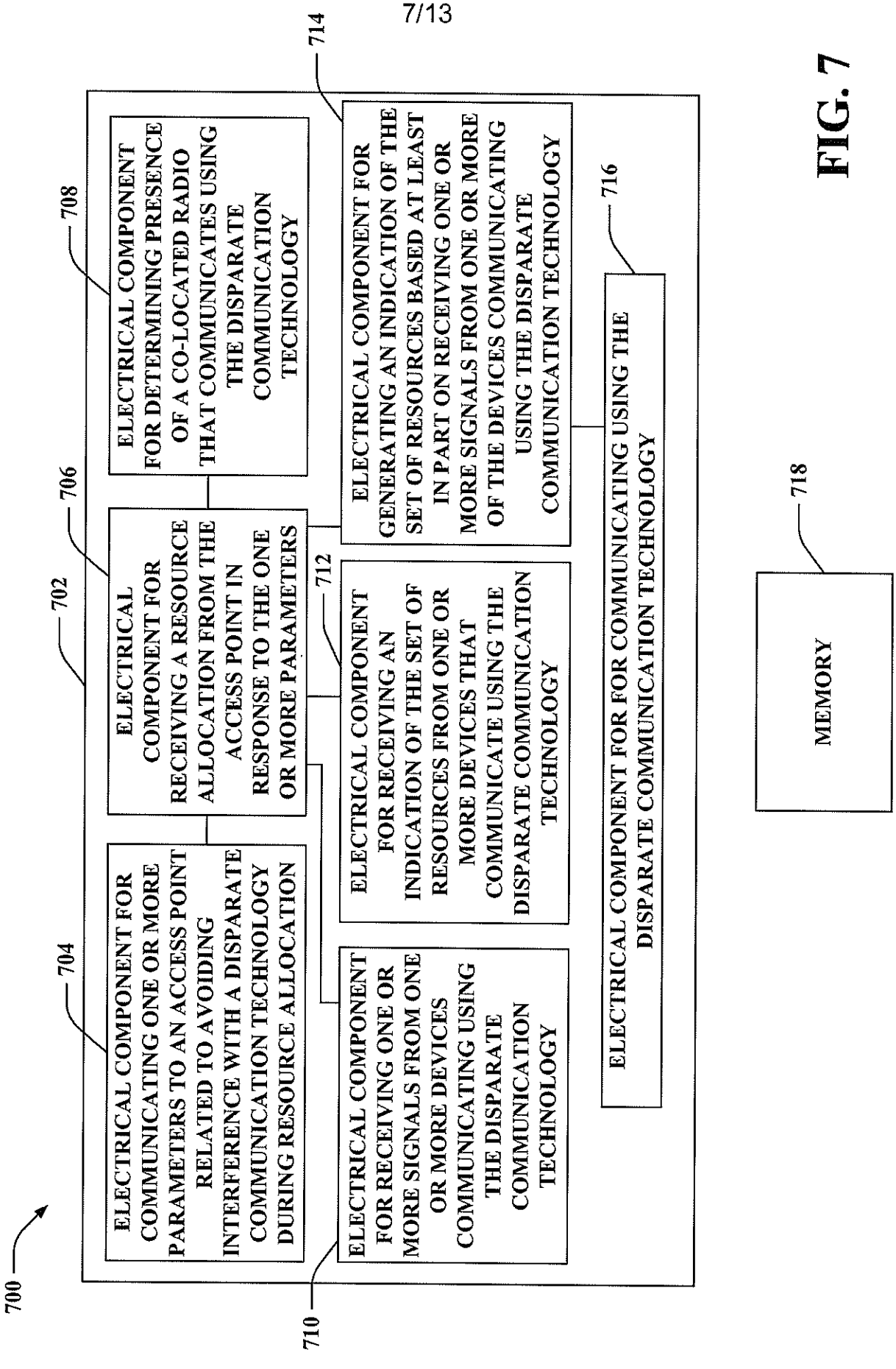

700
710
702
704
706
708
712
714
716
718
ELECTRICAL COMPONENT FOR COMMUNICATING ONE OR MORE PARAMETERS TO AN ACCESS POINT RELATED TO AVOIDING INTERFERENCE WITH A DISPARATE COMMUNICATION TECHNOLOGY DURING RESOURCE ALLOCATION
ELECTRICAL COMPONENT FOR RECEIVING A RESOURCE ALLOCATION FROM THE ACCESS POINT IN RESPONSE TO THE ONE OR MORE PARAMETERS
ELECTRICAL COMPONENT FOR DETERMINING PRESENCE OF A CO-LOCATED RADIO THAT COMMUNICATES USING THE DISPARATE COMMUNICATION TECHNOLOGY
ELECTRICAL COMPONENT FOR RECEIVING ONE OR MORE SIGNALS FROM ONE OR MORE DEVICES COMMUNICATING USING THE DISPARATE COMMUNICATION TECHNOLOGY
ELECTRICAL COMPONENT FOR RECEIVING AN INDICATION OF THE SET OF RESOURCES FROM ONE OR MORE DEVICES THAT COMMUNICATE USING THE DISPARATE COMMUNICATION TECHNOLOGY
ELECTRICAL COMPONENT FOR GENERATING AN INDICATION OF THE SET OF RESOURCES BASED AT LEAST IN PART ON RECEIVING ONE OR MORE SIGNALS FROM ONE OR MORE OF THE DEVICES COMMUNICATING USING THE DISPARATE COMMUNICATION TECHNOLOGY
ELECTRICAL COMPONENT FOR FOR COMMUNICATING USING THE DISPARATE COMMUNICATION TECHNOLOGY
MEMORY

FIG. 7

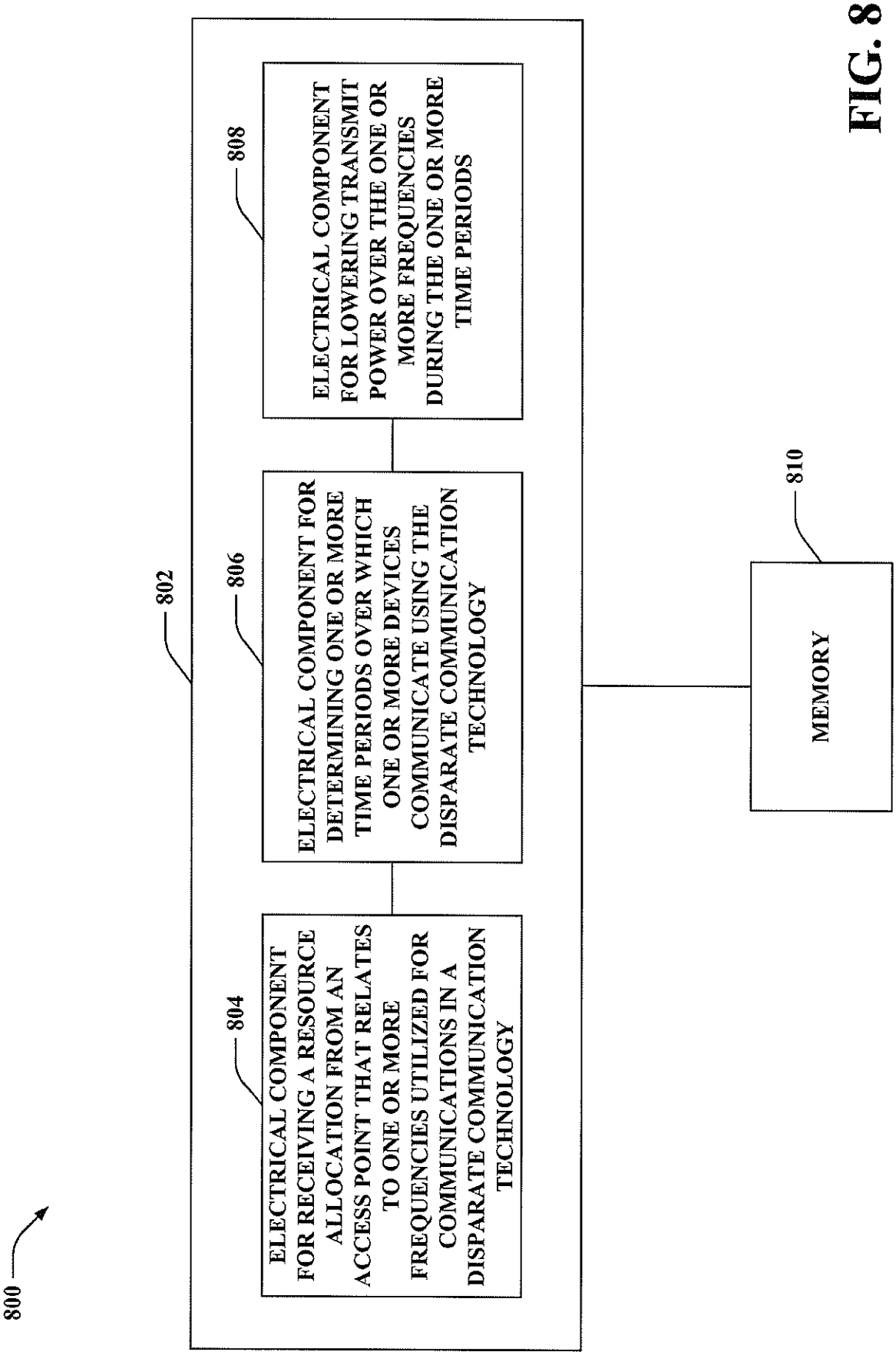
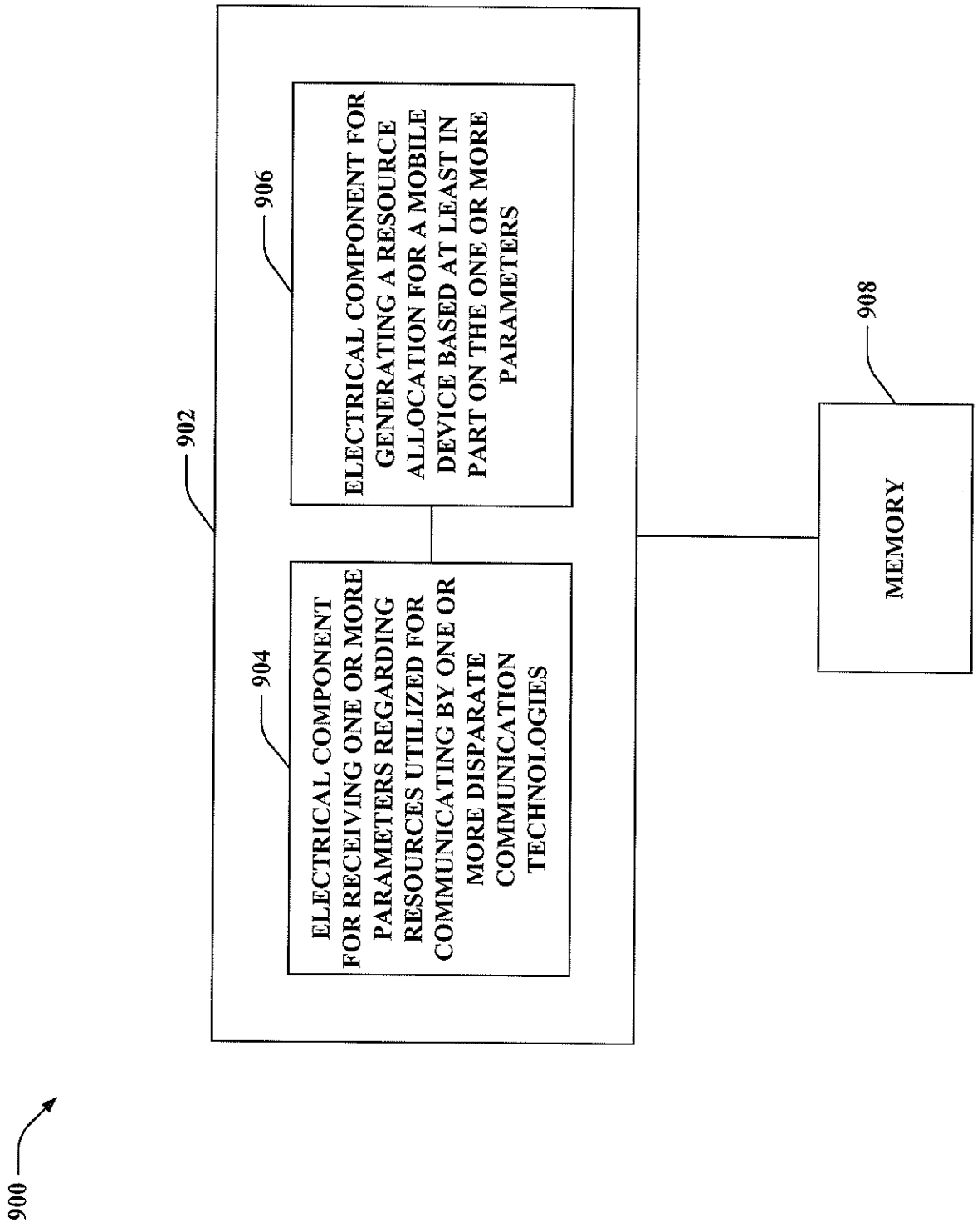

800
802
804
ELECTRICAL COMPONENT FOR RECEIVING A RESOURCE ALLOCATION FROM AN ACCESS POINT THAT RELATES TO ONE OR MORE FREQUENCIES UTILIZED FOR COMMUNICATIONS IN A DISPARATE COMMUNICATION TECHNOLOGY
806
ELECTRICAL COMPONENT FOR DETERMINING ONE OR MORE TIME PERIODS OVER WHICH ONE OR MORE DEVICES COMMUNICATE USING THE DISPARATE COMMUNICATION TECHNOLOGY
808
ELECTRICAL COMPONENT FOR LOWERING TRANSMIT POWER OVER THE ONE OR MORE FREQUENCIES DURING THE ONE OR MORE TIME PERIODS
810
MEMORY

FIG. 8

FIG. 9


900
902
904
ELECTRICAL COMPONENT FOR RECEIVING ONE OR MORE PARAMETERS REGARDING RESOURCES UTILIZED FOR COMMUNICATING BY ONE OR MORE DISPARATE COMMUNICATION TECHNOLOGIES
906
ELECTRICAL COMPONENT FOR GENERATING A RESOURCE ALLOCATION FOR A MOBILE DEVICE BASED AT LEAST IN PART ON THE ONE OR MORE PARAMETERS
908
MEMORY

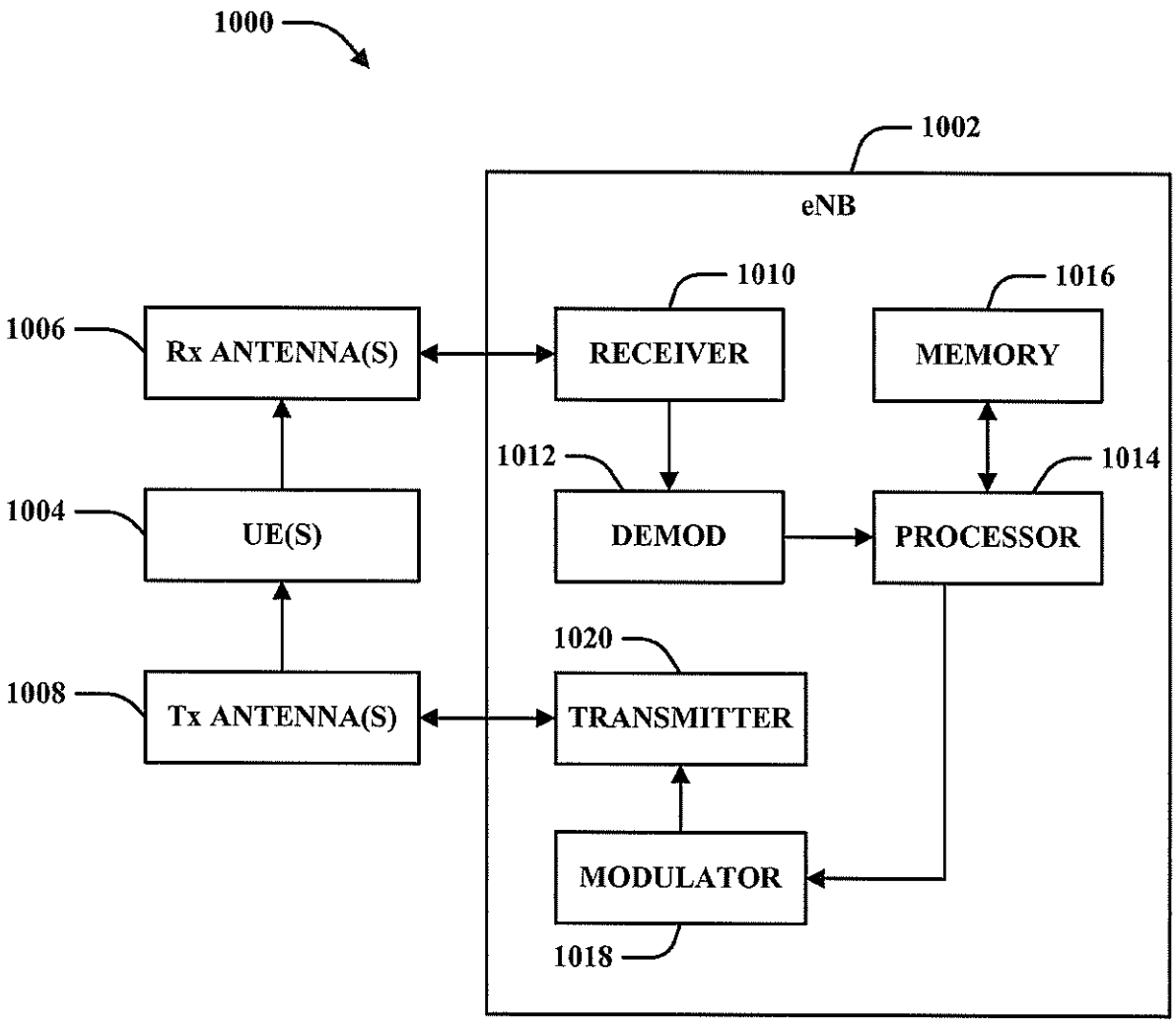

1000
1002
eNB
1010
1016
1006
Rx ANTENNA(S)
RECEIVER
MEMORY
1012
1014
1004
UE(S)
DEMOD
PROCESSOR
1020
1008
Tx ANTENNA(S)
TRANSMITTER
MODULATOR
1018

FIG. 10

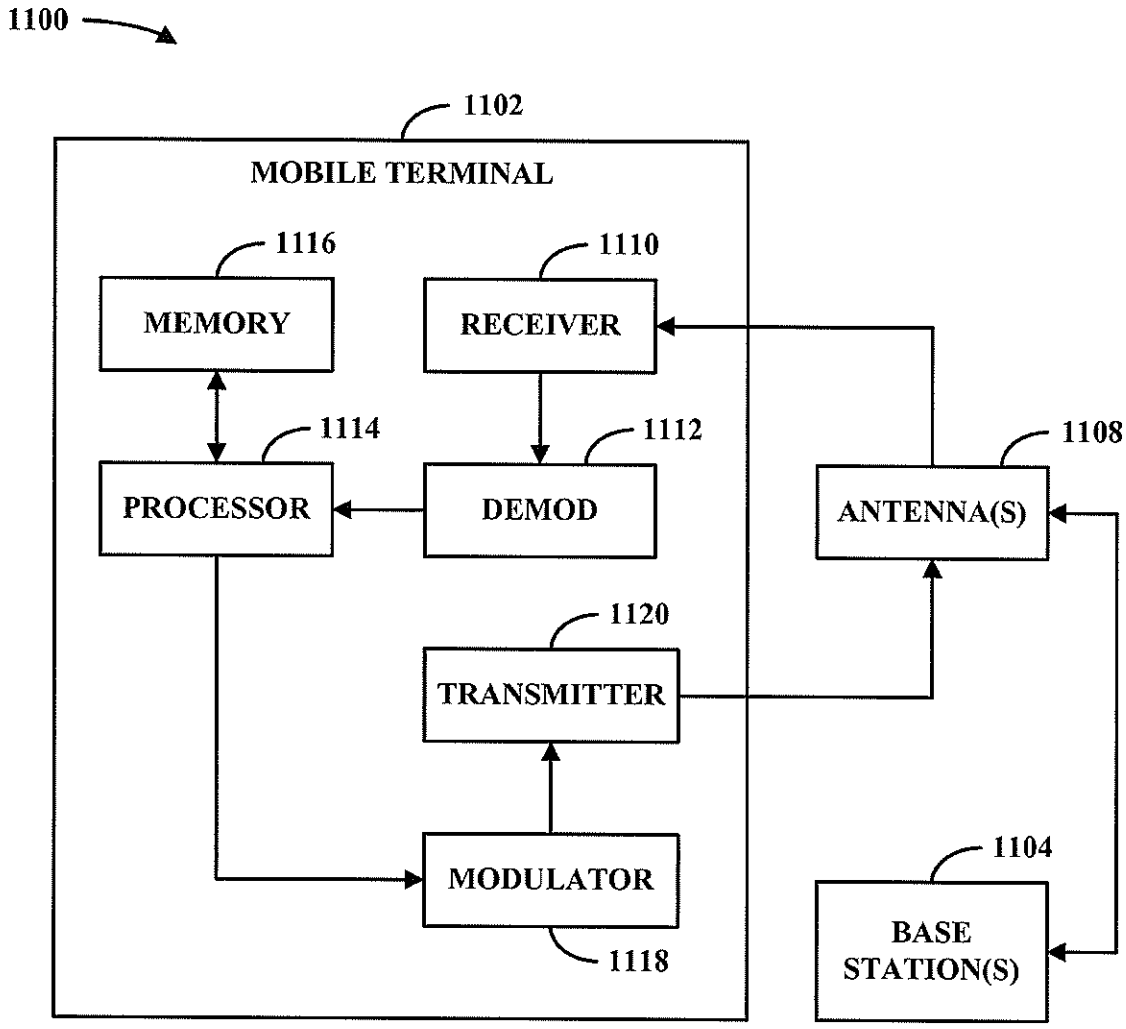

1100
1102
MOBILE TERMINAL
1116
MEMORY
1110
RECEIVER
1114
PROCESSOR
1112
DEMOD
1108
ANTENNA(S)
1120
TRANSMITTER
MODULATOR
1118
1104
BASE STATION(S)

FIG. 11

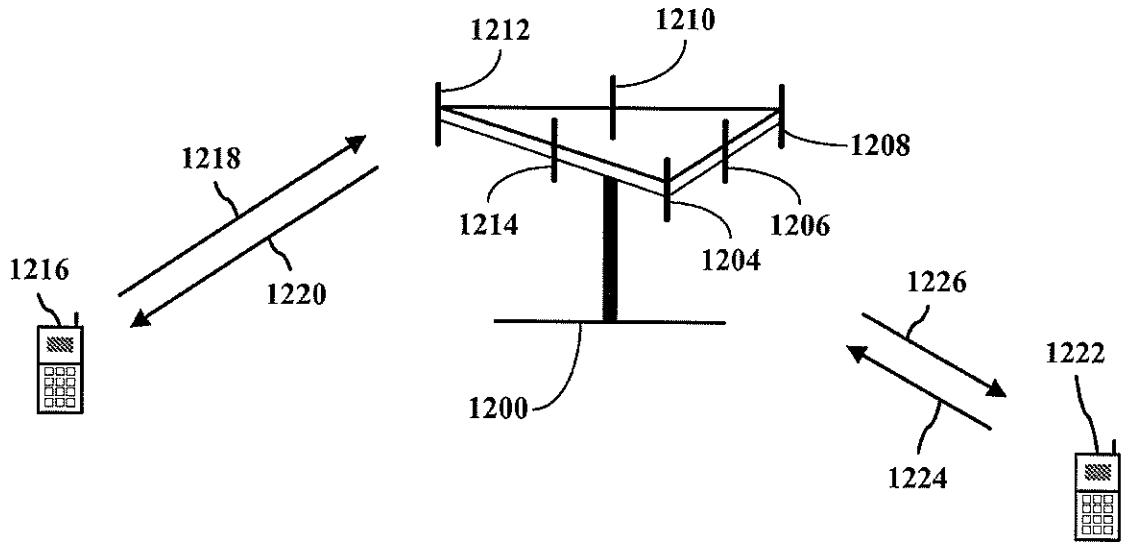

1212
1210
1208
1218
1214
1206
1216
1204
1220
1226
1222
1200
1224

FIG. 12

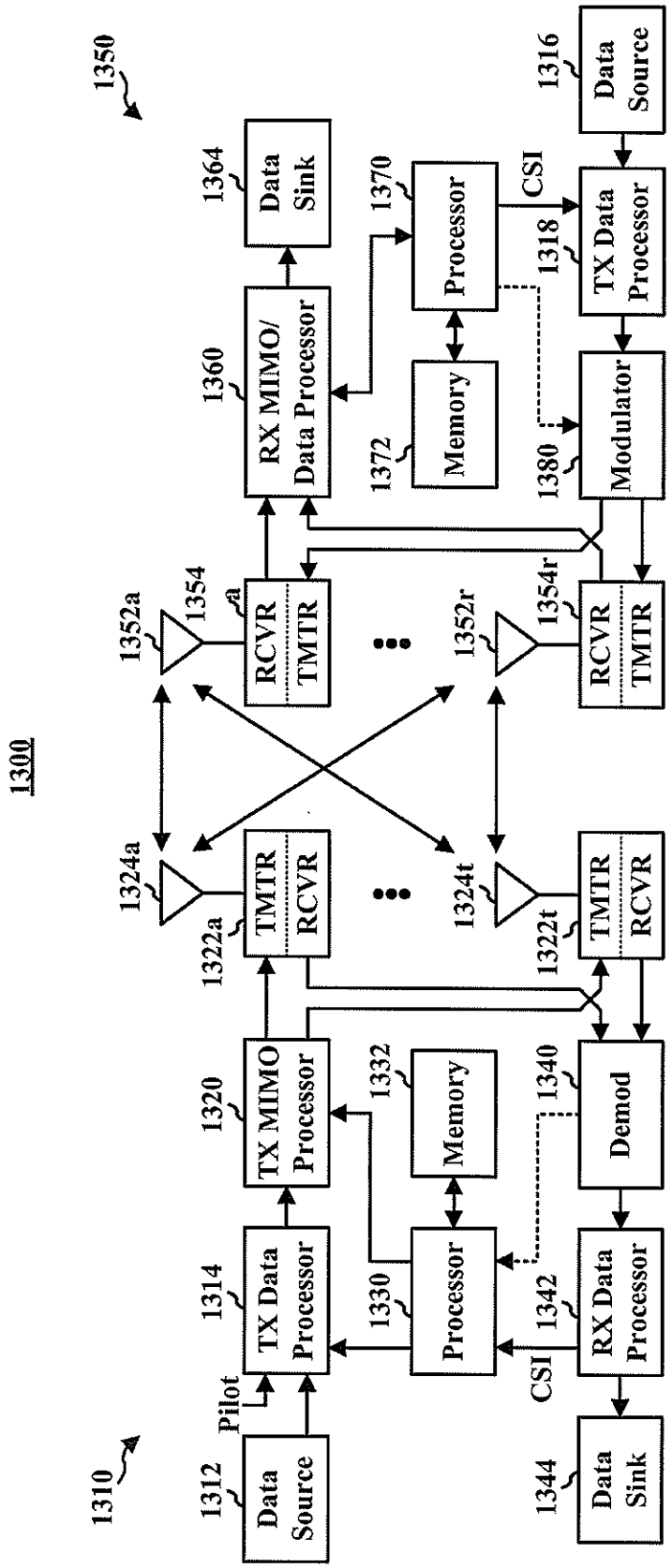

1300
1310
1312
Data Source
Pilot
1314
TX Data Processor
1320
TX MIMO Processor
1322a
TMTR
RCVR
1322t
TMTR
RCVR
1324a
1324t
1330
Processor
1332
Memory
1340
Demod
1342
RX Data Processor
CSI
1344
Data Sink
1350
1352a
1352r
1354a
RCVR
TMTR
1354r
RCVR
TMTR
1360
RX MIMO/ Data Processor
1364
Data Sink
1370
Processor
1372
Memory
1380
Modulator
1316
Data Source
1318
TX Data Processor
CSI

FIG. 13