

ABSTRACTQuieting Receiver During Power Interruption

5 A receiver comprises a synthesizing tuner circuit (22)
for detecting a selected input signal and an audio circuit for
reproducing the audio content in the detected signal. The tuner
circuit includes a local oscillator (54), a prescaler, a phase
locked loop (70) and an integrator (92, 94), and has a tendency to
10 uncontrollably detune during AC power interruption and generate
audible noise during the detuning. The audible noise is
introduced into the audio content of the detected signal. An AC
power interruption detector (Fig 3; Fig. 4) monitors a run power
supply and generates a control signal when the run supply falls
15 below a predetermined threshold. A control circuit (12, 84)
responsive to the control signal first disables normal operation
of the tuner circuit and then disables the audio circuit, thereby
reducing the audible noise. The tuner circuit may be disabled by
decoupling the output of the phase locked loop from the input to
20 the integrator, enabling the input to float. In the absence of a
corrective signal from the phase locked loop, the integrator
maintains a substantially constant output tuning voltage and
prevents sudden detuning.

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