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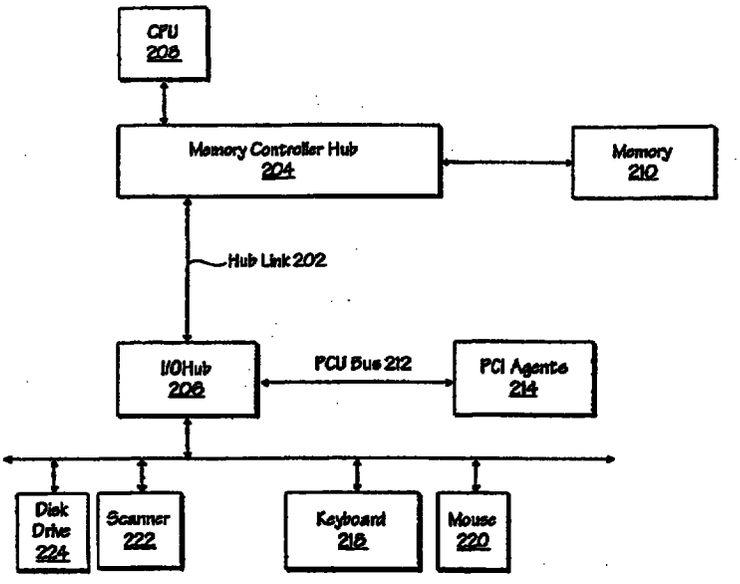
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(54) Title: METHOD AND APPARATUS FOR AN IMPROVED INTERFACE BETWEEN COMPUTER COMPONENTS



(57) Abstract

An interface to transfer data between a memory controller hub (204) and an input/output (I/O) hub (206) of a chipset within a computer system. One embodiment of the interface includes a bi-directional data signal path (202) and a pair of source synchronous strobe signals. The data signal path transmits data in packets via split transactions. In addition, the packets include a request packet and a completion packet, if necessary. Furthermore, in one embodiment, the request packets include a transaction descriptor.