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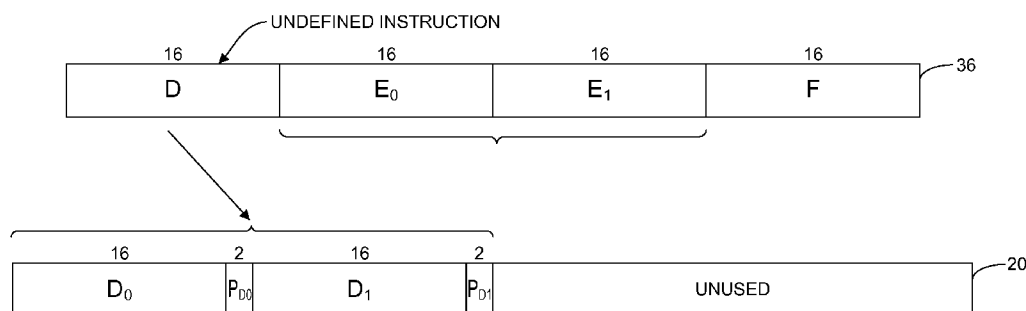
- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
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(54) Title: PRE-DECODING VARIABLE LENGTH INSTRUCTIONS



(57) Abstract: A pre-decoder in a variable instruction length processor indicates properties of instructions in pre-decode bits stored in an instruction cache with the instructions. When all the encodings of pre-decode bits associate with one length instruction are defined, a property of an instruction of that length may be indicated by altering the instruction to emulate an instruction of a different length, and encoding the property in the pre-decode bits associated with instructions of the different length. One example of a property that may be so indicated is an undefined instruction.