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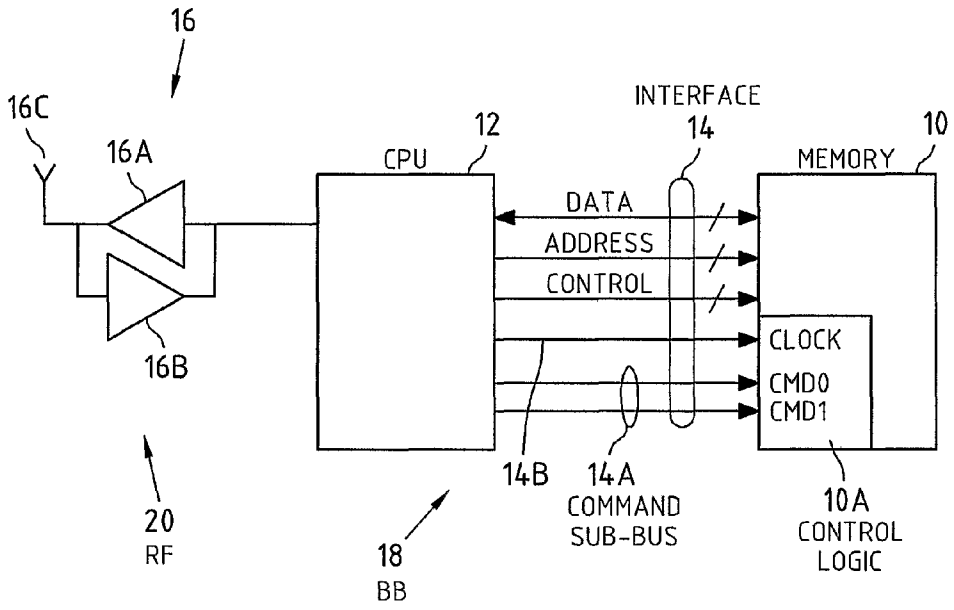
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(54) Title: MEMORY INTERFACE FOR VOLATILE AND NON-VOLATILE MEMORY DEVICES



(57) Abstract: Accessing data stored in a memory device through an interface, with addressing data on the memory device through at least one address bus, controlling at least data flow to and from the memory device through at least one command bus, and transferring data to and from the memory through at least one data bus wherein commands on the command bus are adjusted depending on the type of memory connected to the interface.

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