

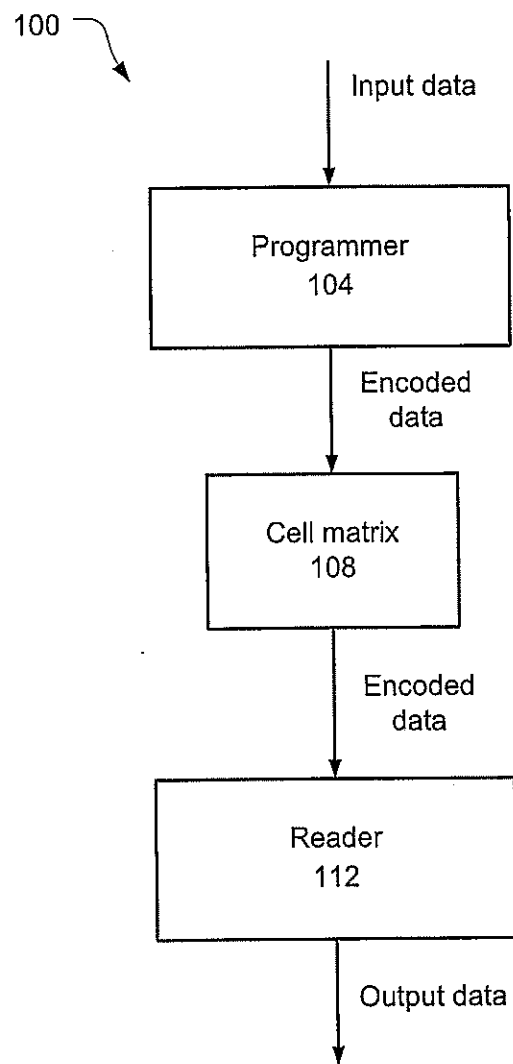


Figure 1

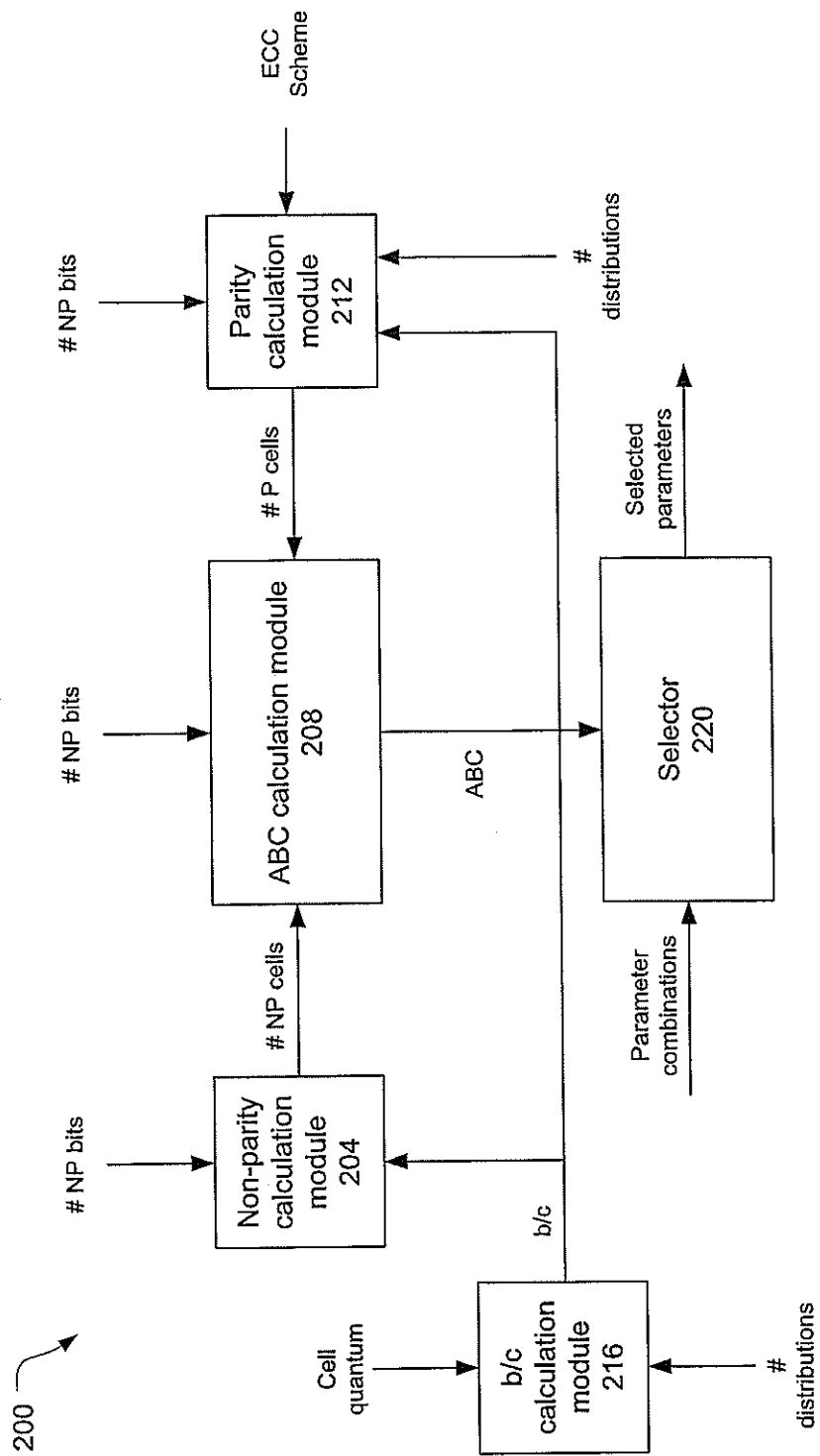


Figure 2

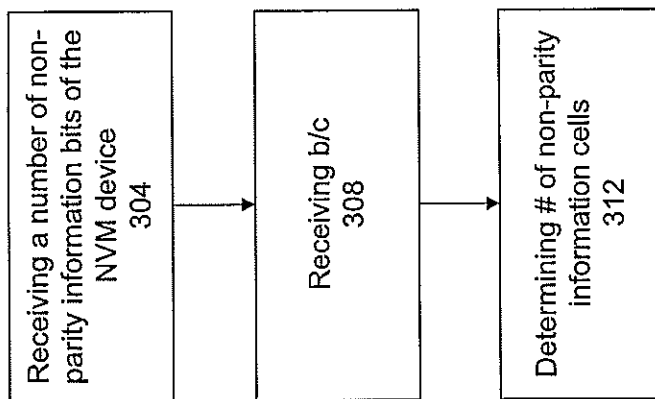


Figure 3a

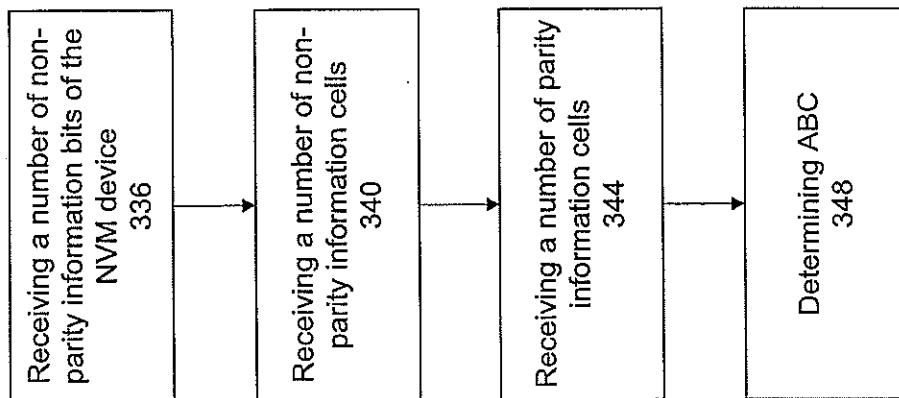


Figure 3b

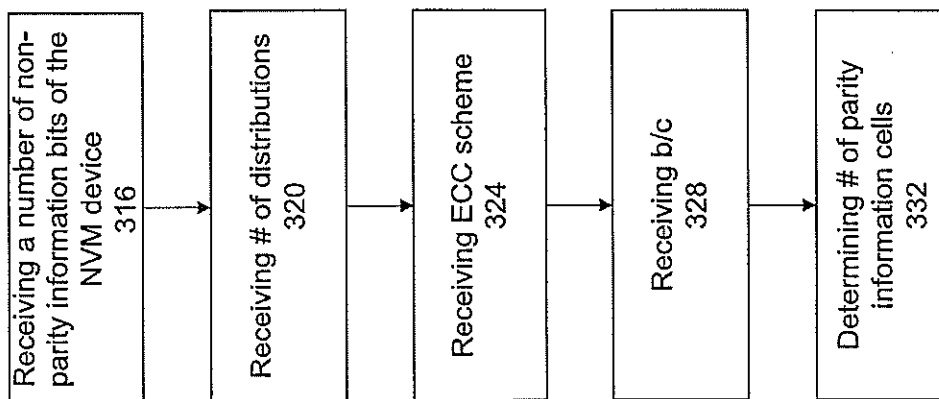


Figure 3c

400 →

	A	B	C	D	E	F	G
Device Size (Mbit)	32768	32768	32768	32768	32768	32768	32768
bit/cell device size/ n. cell (non-parity)	2	3	3	2.67	2.67	2.5	2.5
N. Cell (M)	16384	10923	10923	12273	12273	13107	13107
ECC Value	6	164	66	100	34	50	15
Parity Overhead (%)	2	49	32	31	15	16	7
Total N. Cell (M) (non-parity + parity)	16712	16275	14418	16078	12414	15205	14025
% Gain (wrt 2b/c)	0	2.61	13.73	3.79	15.55	9.02	16.08
ECC Scheme	BCH	BCH	TCM 12 + RS	BCH	TCM8+ RS	BCH	TCM 8+RS
Number of Distributions	4	8	12	7	8	6	8
Cell quantum	1	1	2	1	3	1	2
ABC (actual b/c)	1.96	2.01	2.72	2.03	2.32	2.15	2.33

Figure 4

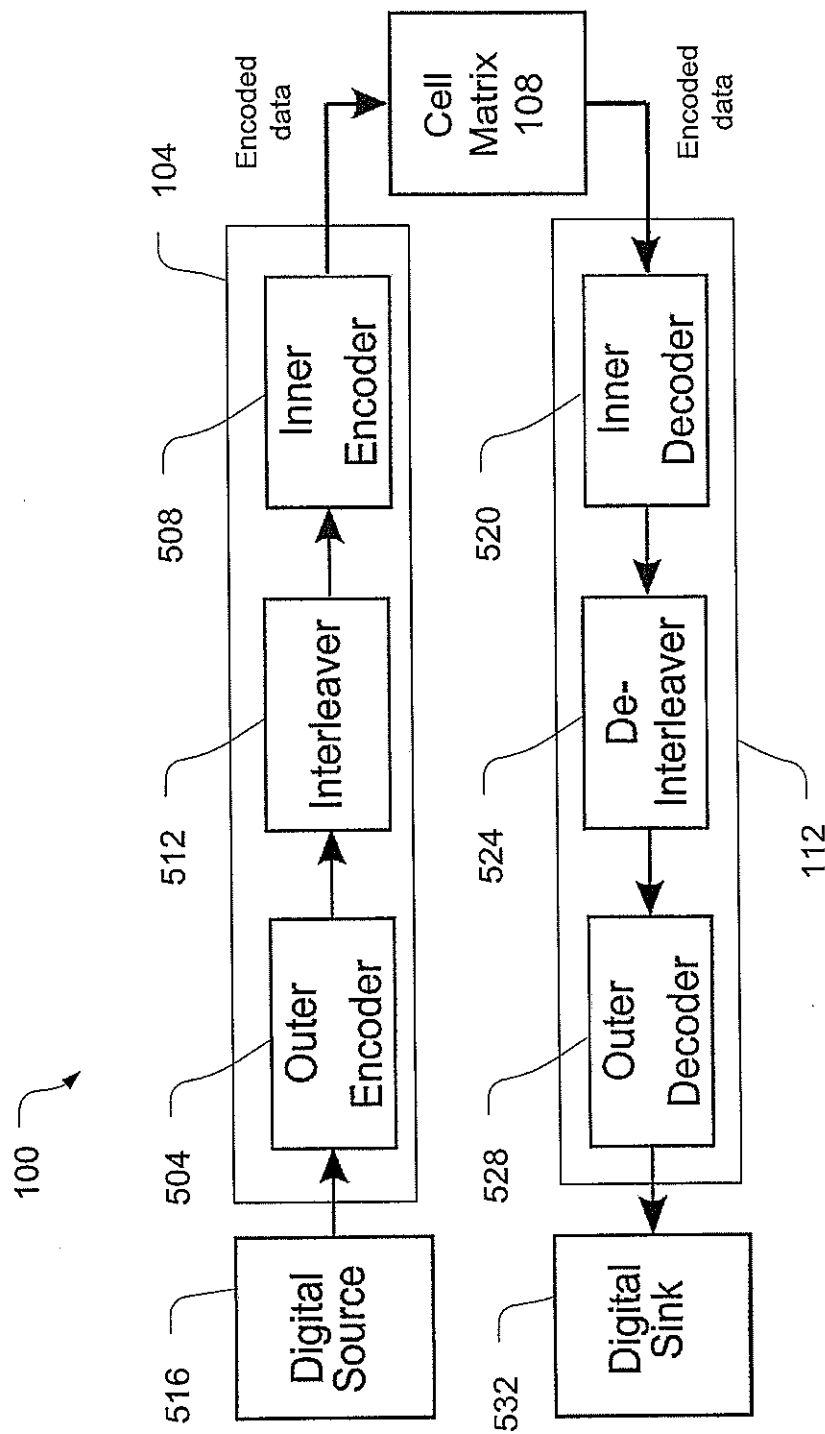


Figure 5

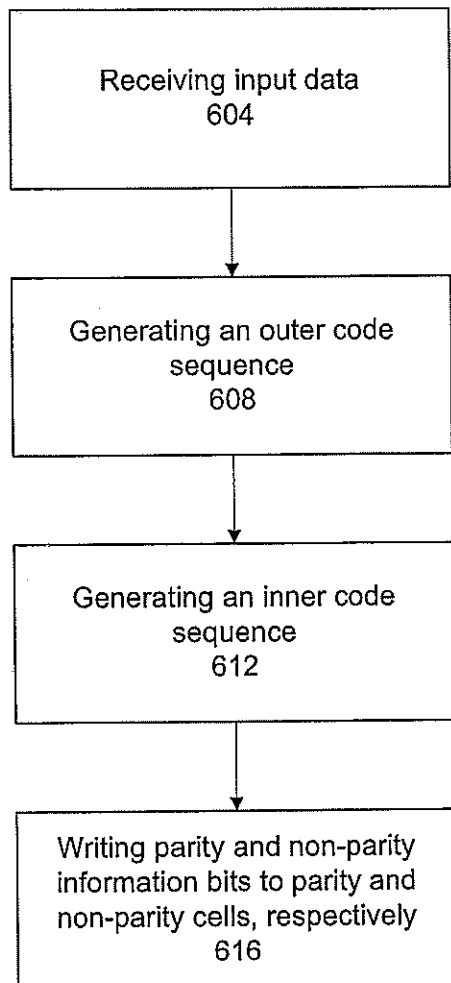


Figure 6a

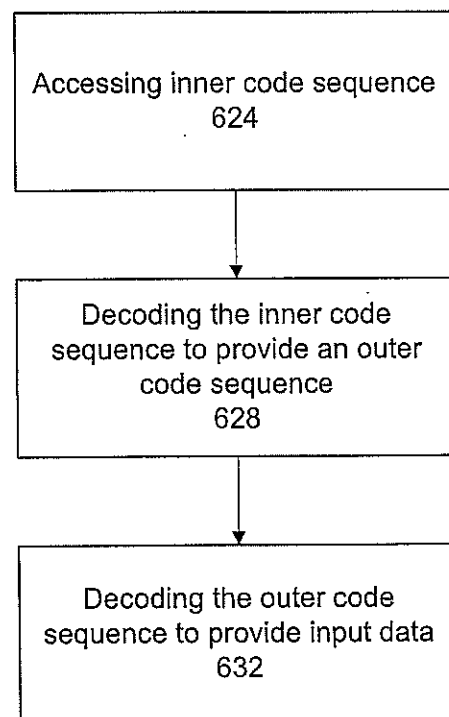


Figure 6b

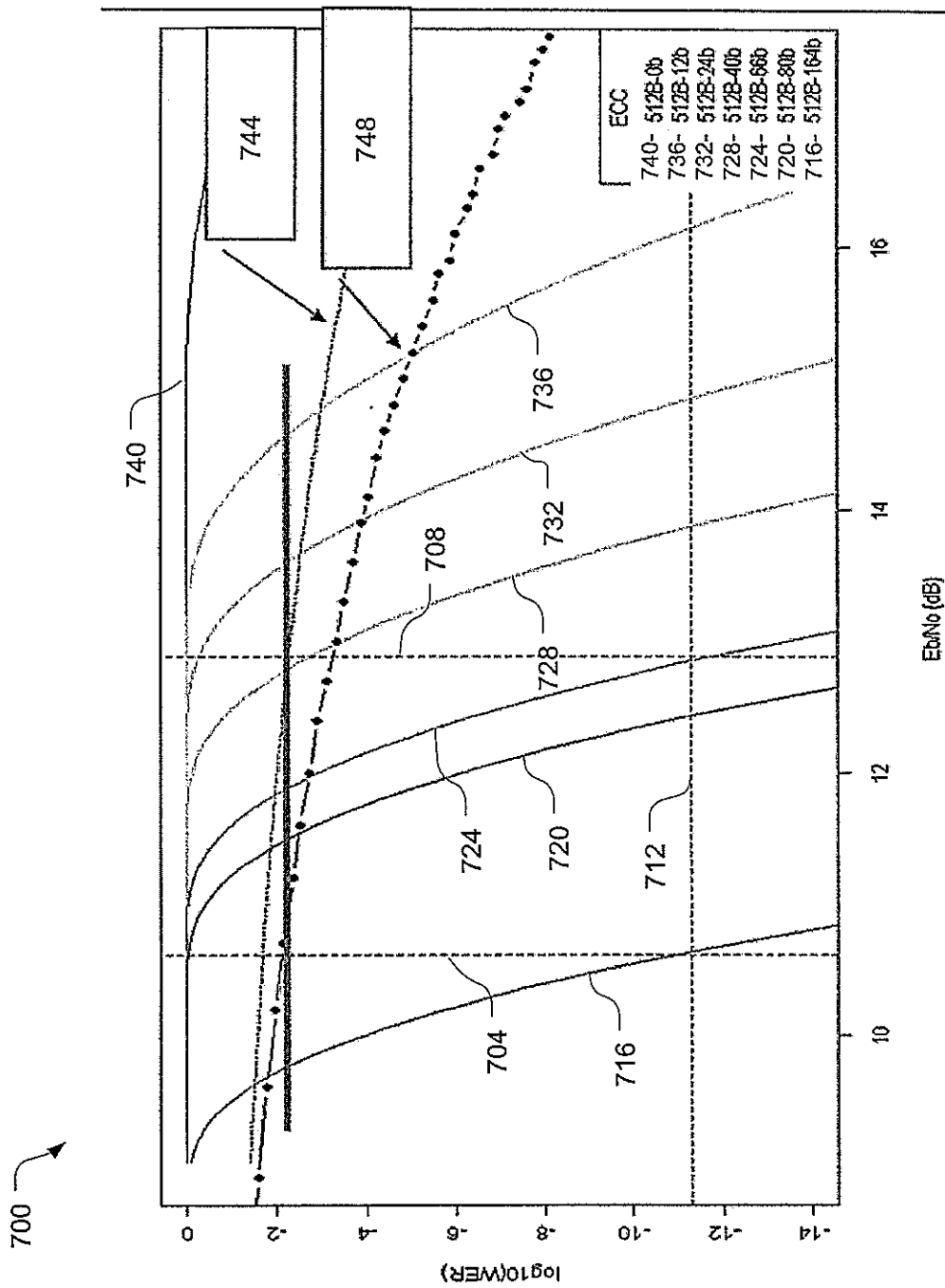


Figure 7

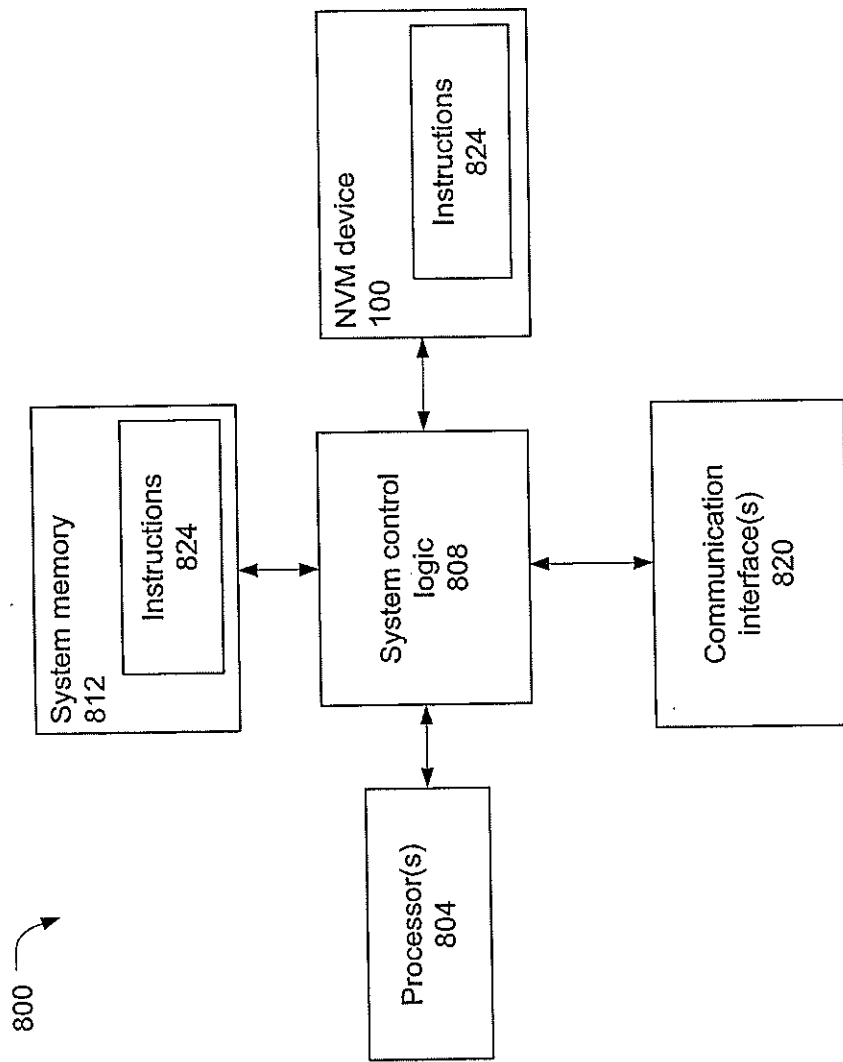


Figure 8