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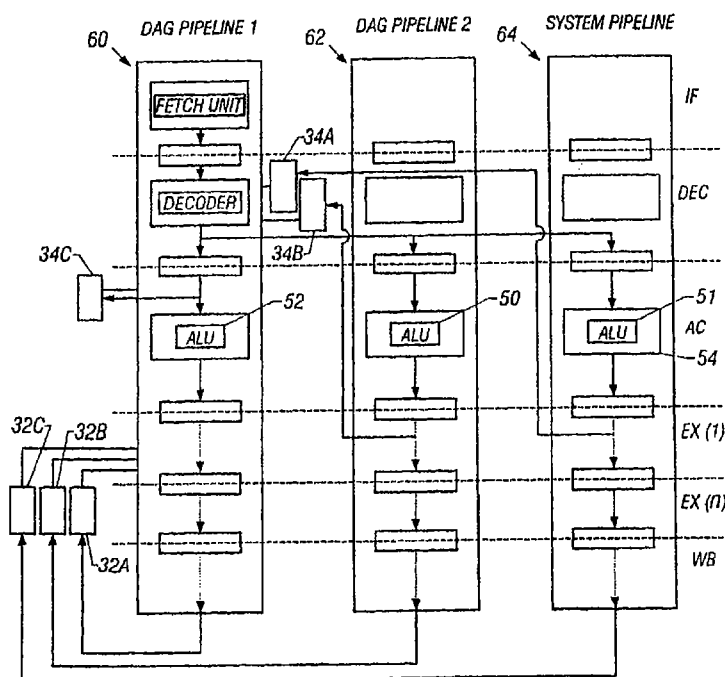
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(54) Title: METHOD AND APPARATUS FOR PROCESSING PROGRAM LOOPS IN PARALLEL



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(57) Abstract: In one embodiment, a programmable processor is adapted to support hardware loops. The processor may include hardware such as a first set of registers, a second set registers, a first pipeline, and a second pipeline. Furthermore, the processor may include a control unit adapted to efficiently implement the hardware when performing a hardware loop.