

CHIP THERMISTORS AND METHODS OF MAKING SAME**Abstract of the Disclosure**

A chip thermistor is produced by providing a planar rectangular thermistor block with a pair of ~~mutually insulated~~ electrodes formed on its surfaces, each of the electrode being formed so as to be in part on a different one of the main surfaces and extending continuously at least onto one of the side surfaces. The thermistor block thus prepared is thereafter cut transversely to obtain a plurality of thermistor elements. A specified number of these thermistor elements are then aligned and stacked one on top of another with their main surfaces facing each other. A layer of an insulating material such as glass with thickness greater than 10 μ m is inserted between each mutually adjacently stacked pair of these thermistor elements. Outer electrodes are formed on the outer surfaces of the stacked structure so as to electrically connect to the electrodes on the stacked thermistor elements on their aligned end surfaces.

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