

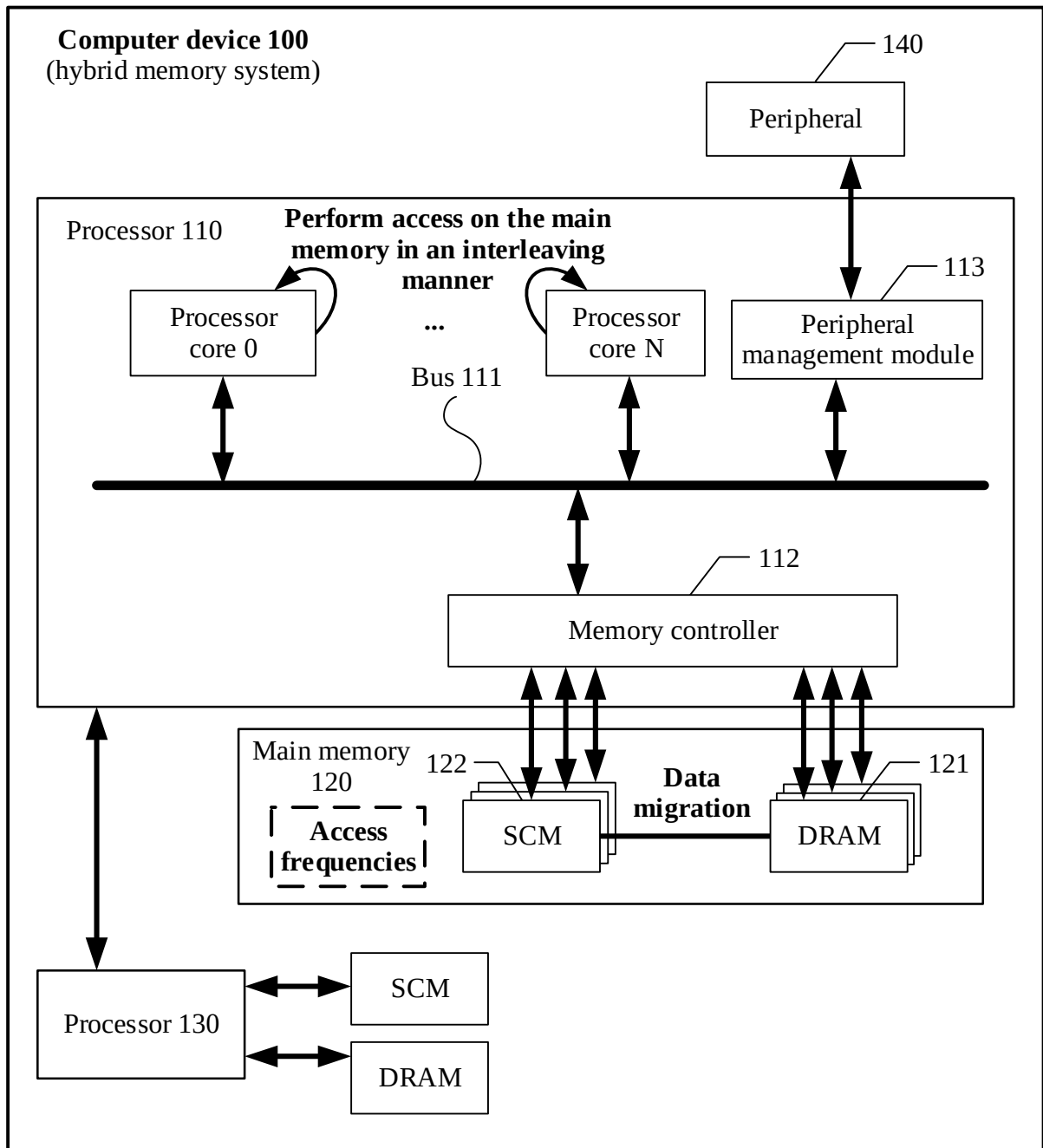
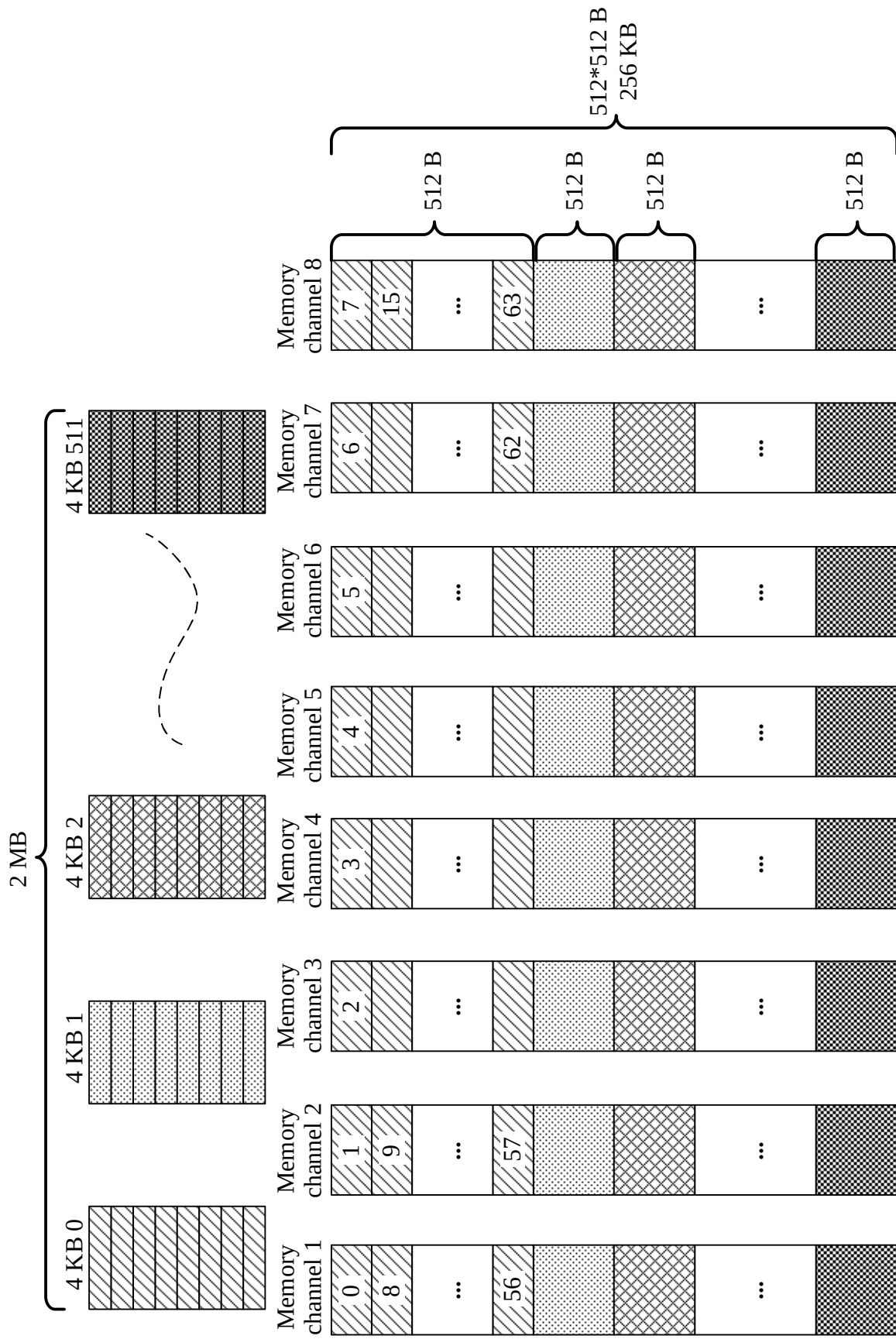
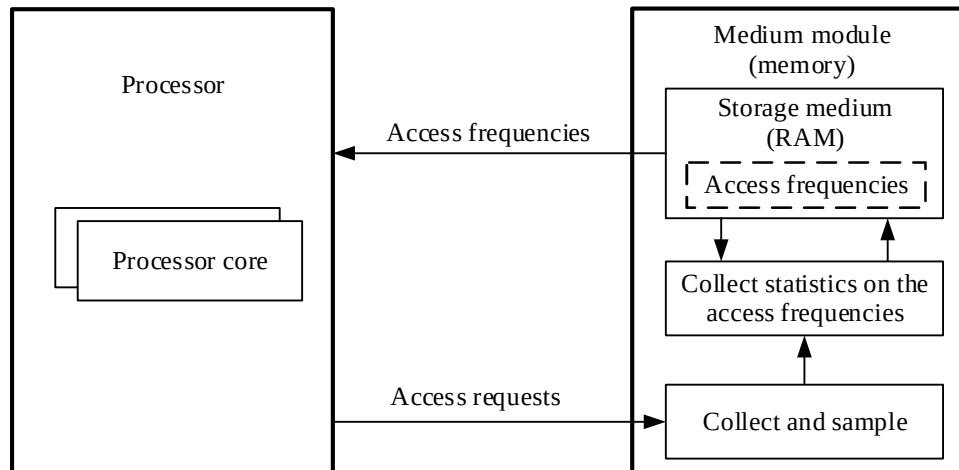
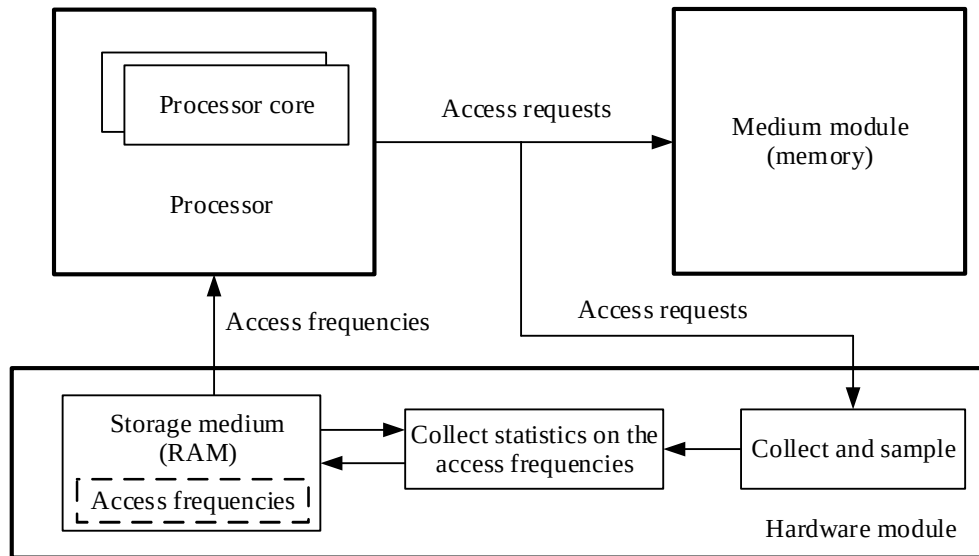


FIG. 1

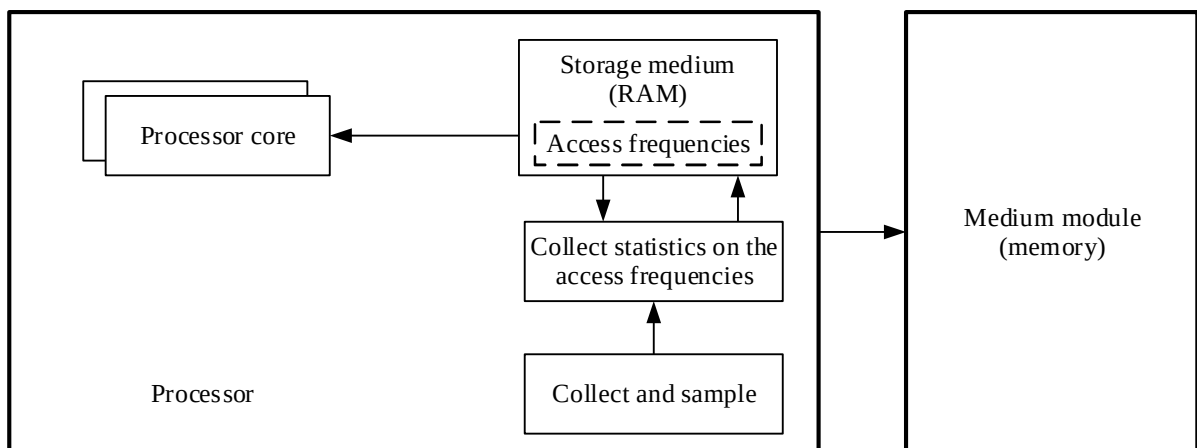




(a) **Scenario of collecting statistics on access temperatures of memories in the medium module**



(b) **Scenario of collecting statistics on access temperatures of memories in the hardware module**



(c) **Scenario of collecting statistics on access temperatures of memories in the processor**

FIG. 3

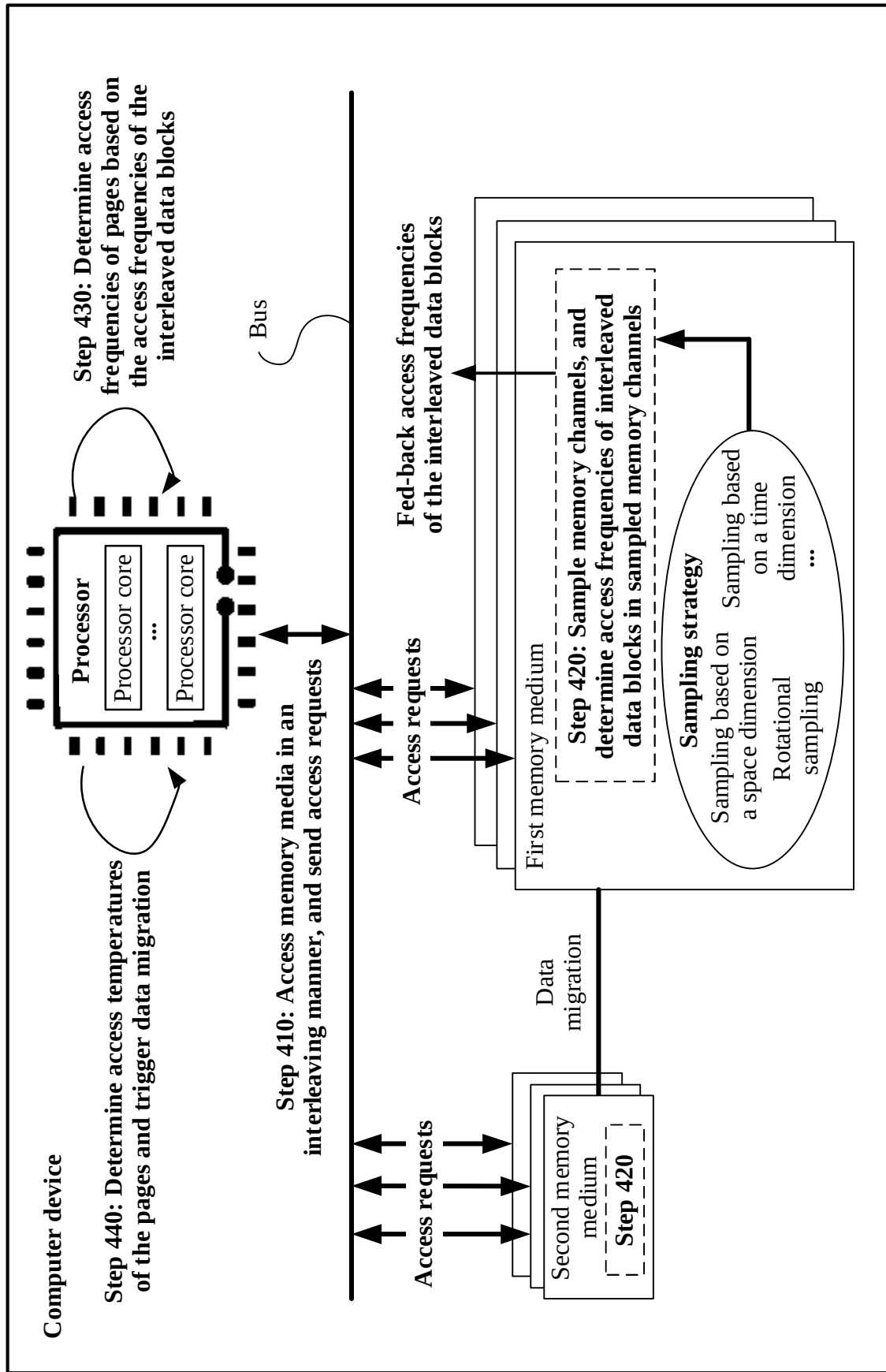


FIG. 4

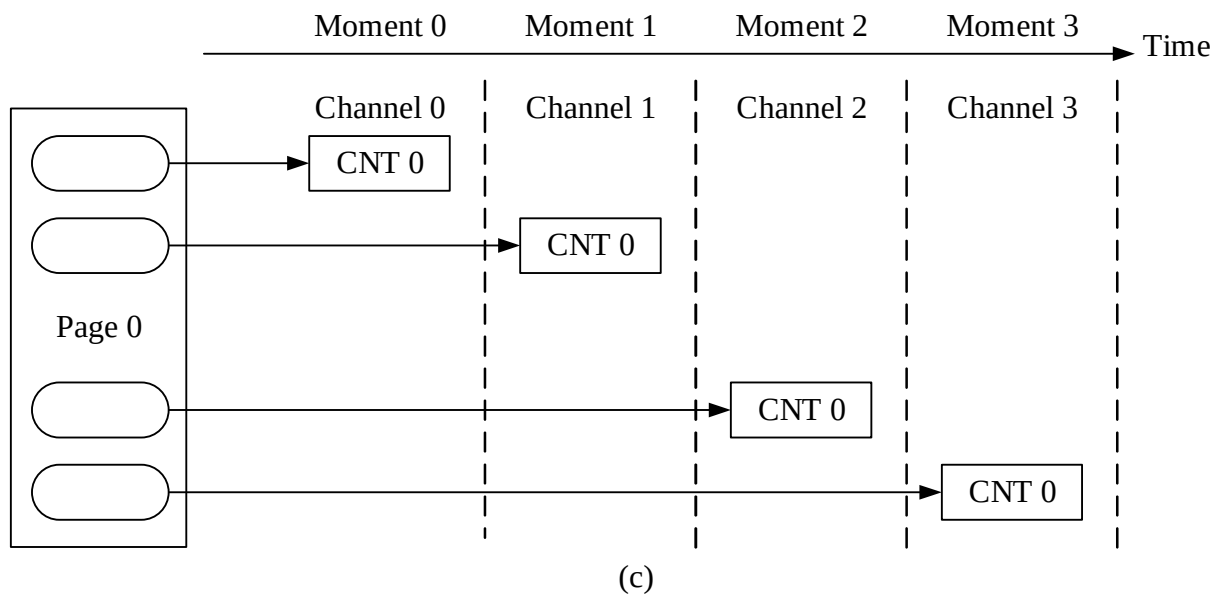
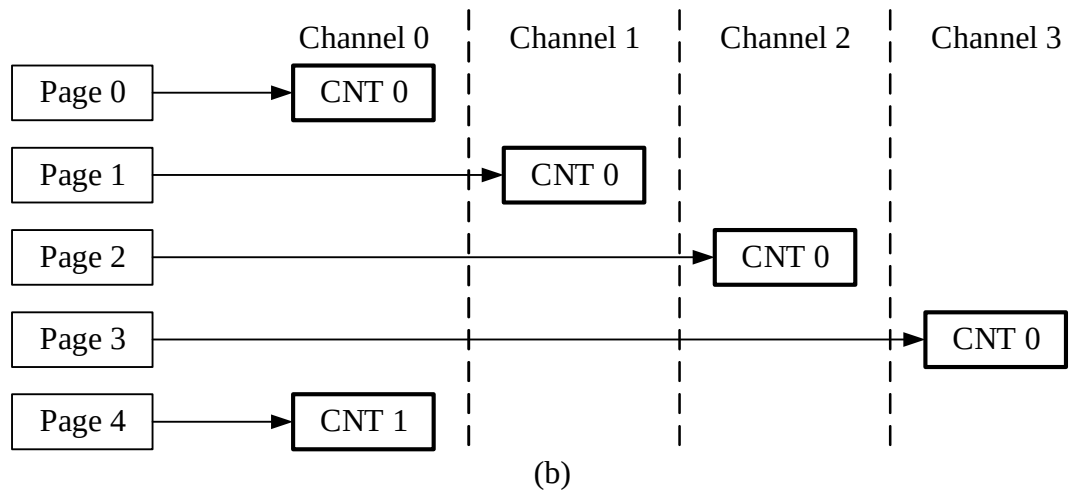
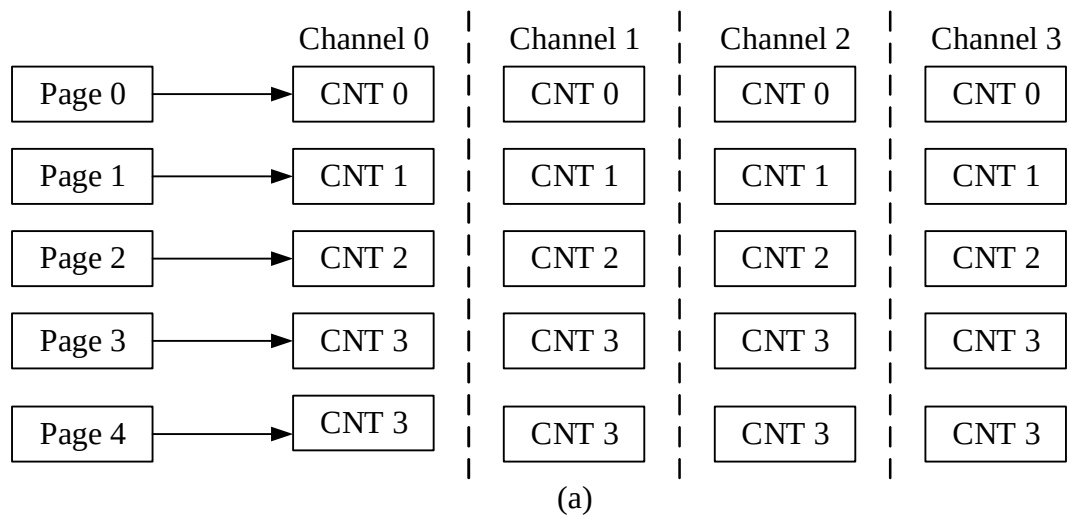


FIG. 5

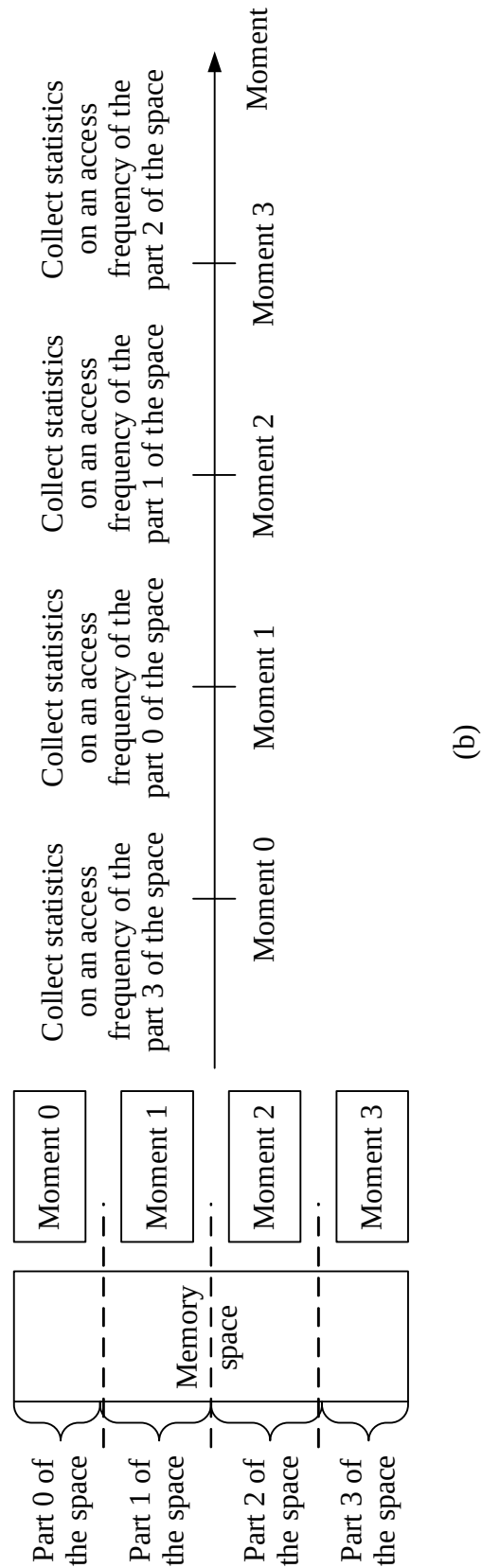
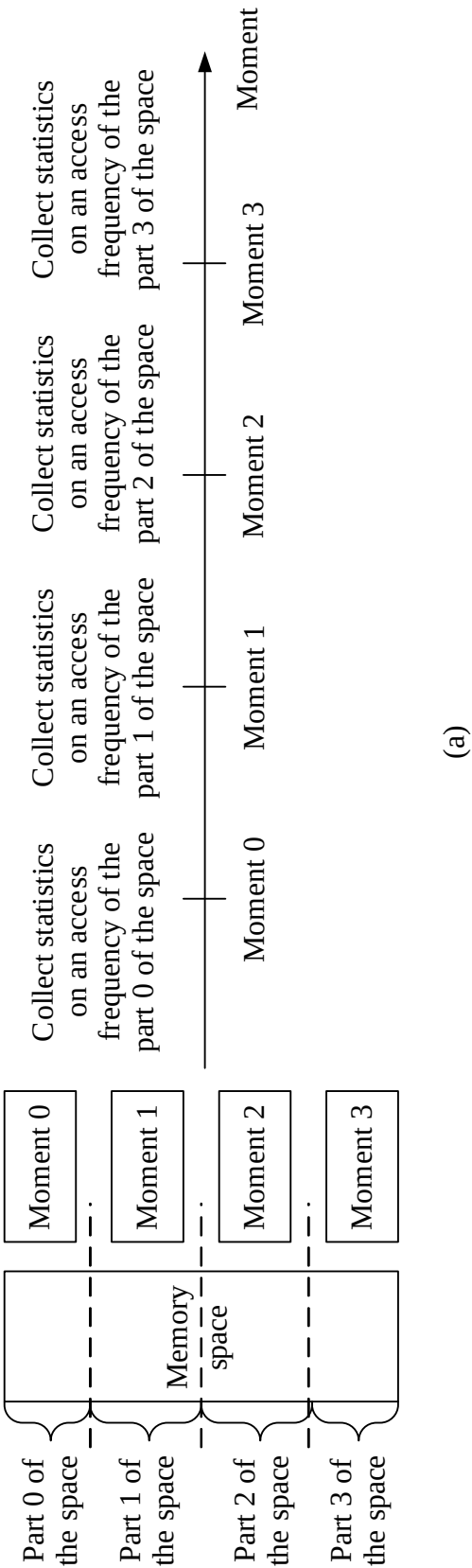


FIG. 6

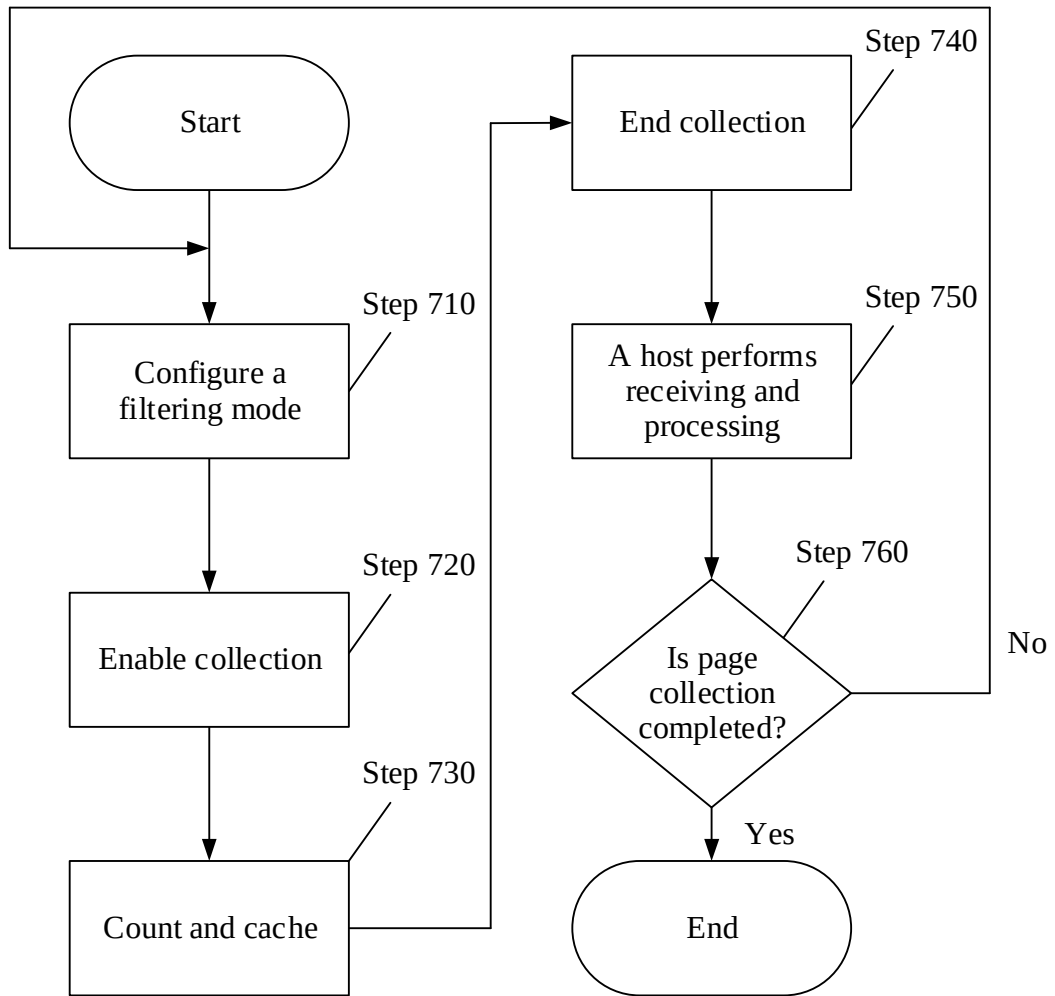


FIG. 7

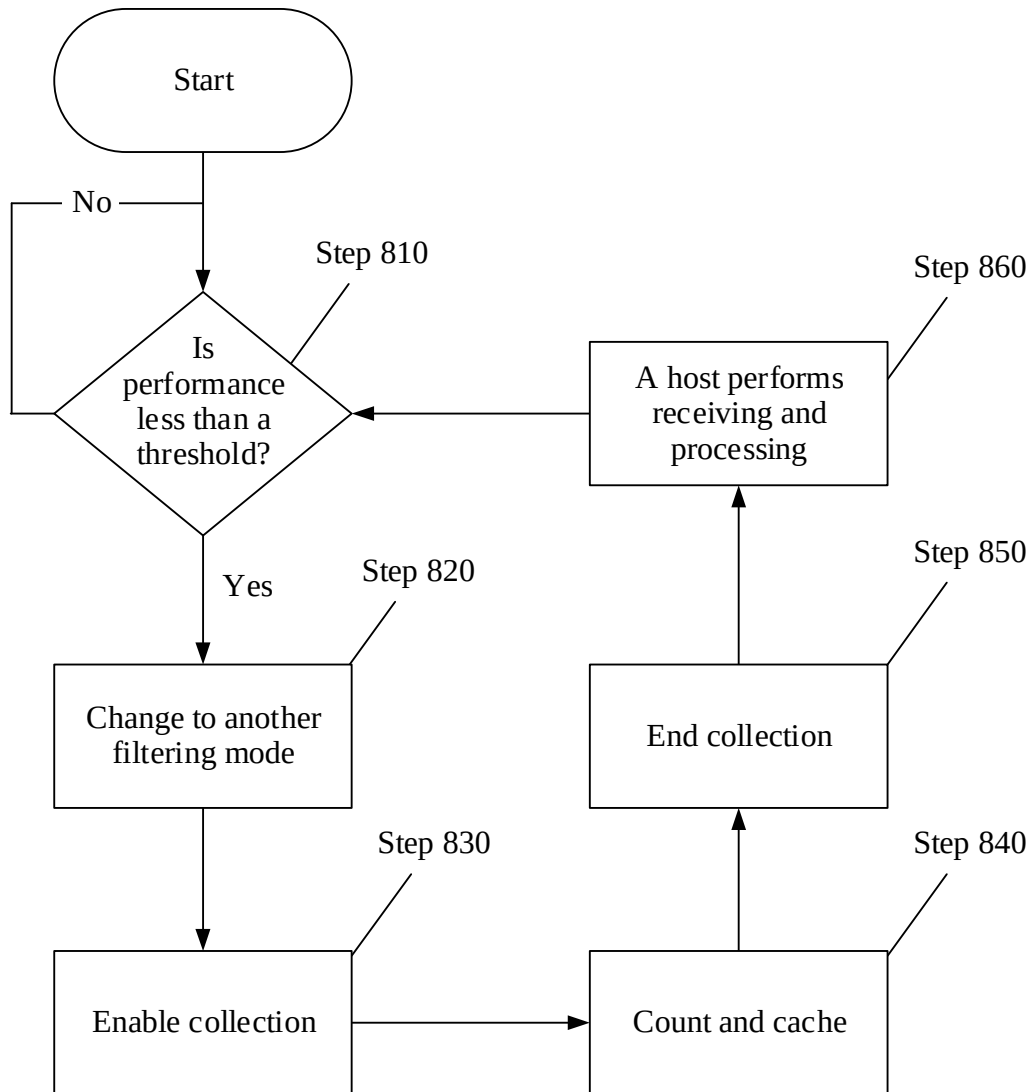


FIG. 8

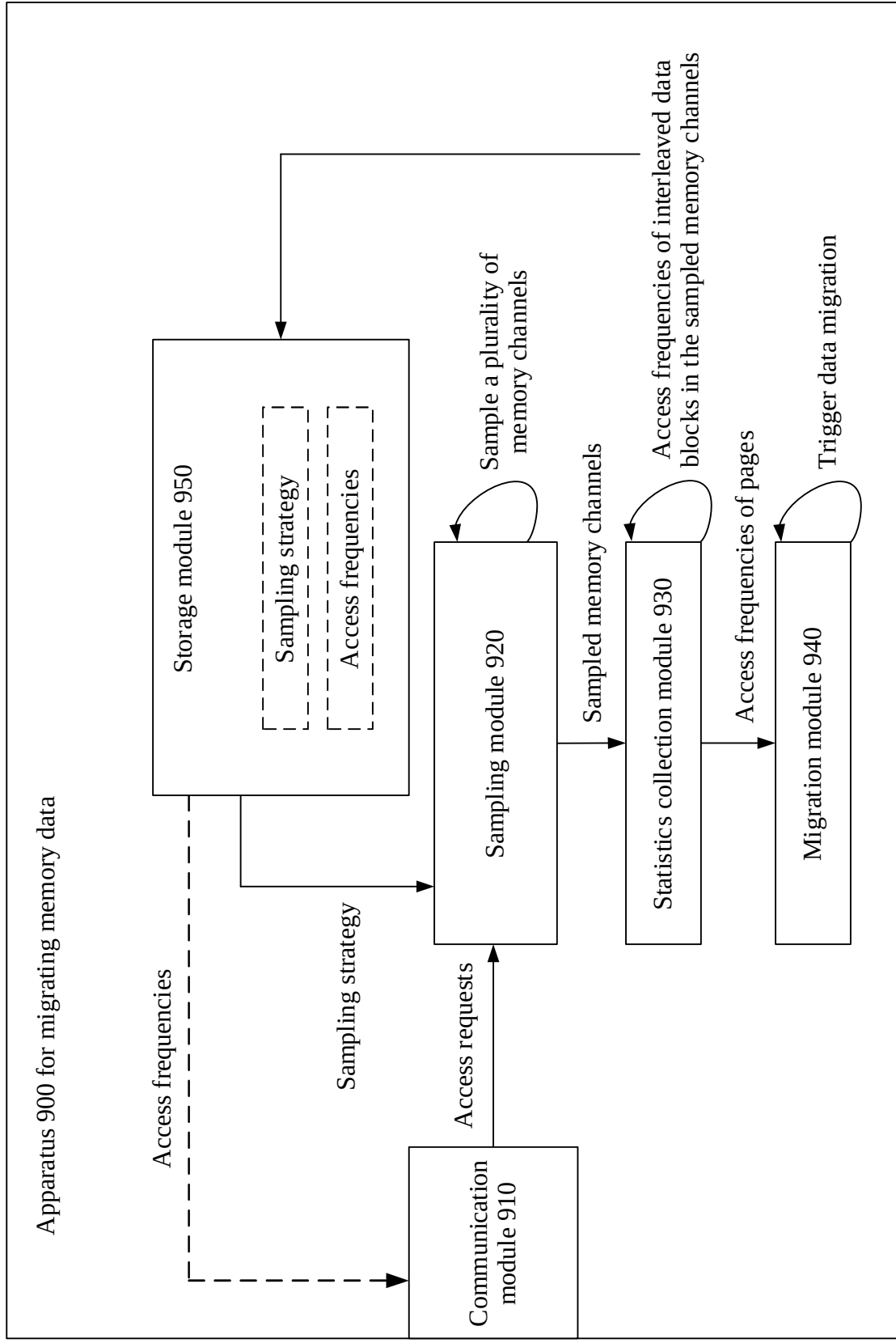


FIG. 9