



ABSTRACT OF THE DISCLOSURE

Disclosed herein is a digital/analog conversion output circuit comprising a digital/analog converter, a first amplifier circuit for producing a non-inverse output signal being in phase with a signal outputted from the digital/analog converter, in response to the output signal thereof, a second amplifier circuit for producing an inverse output signal opposite in phase to the output signal of the digital/analog converter in response to the output signal thereof, and an offset control circuit for detecting offset voltages of the first and second amplifier circuits, each corresponding to an analog middle voltage, and adding an average value of the digitized offset voltages to a digital signal to be inputted to the digital/analog converter.