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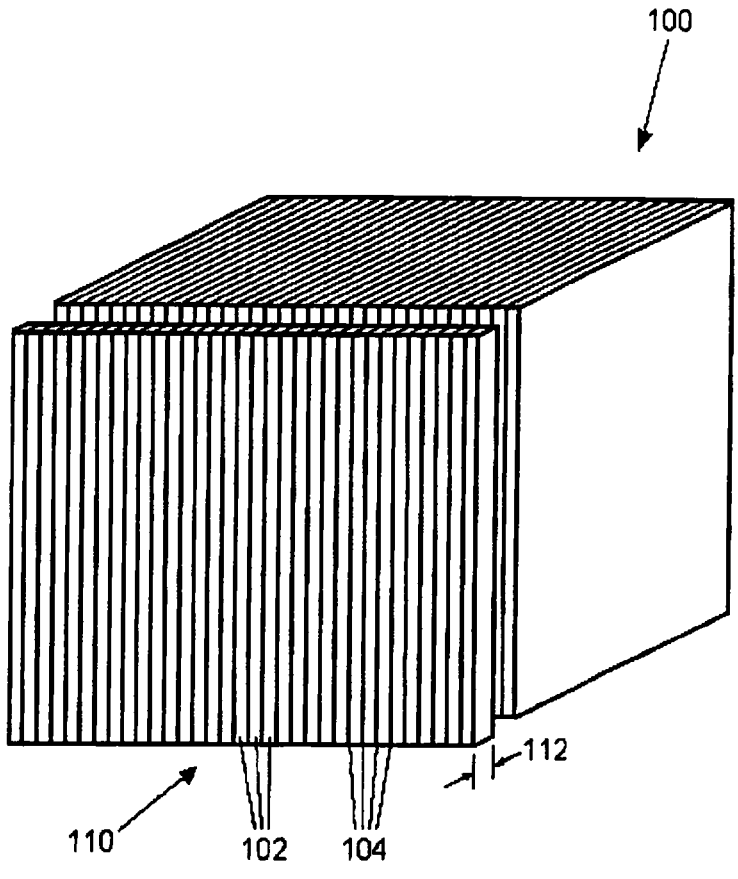
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(54) Title: PARALLEL PLANE SUBSTRATE



(57) Abstract: A microelectronic substrate (110) having a plurality of alternating substantially planar layers of dielectric material (102) and conductive material (104), and further having a first surface (116) and a second surface (130), wherein the dielectric material (102) and the conductive material (104) layers extend substantially perpendicularly between the first and second surfaces (116, 130).

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