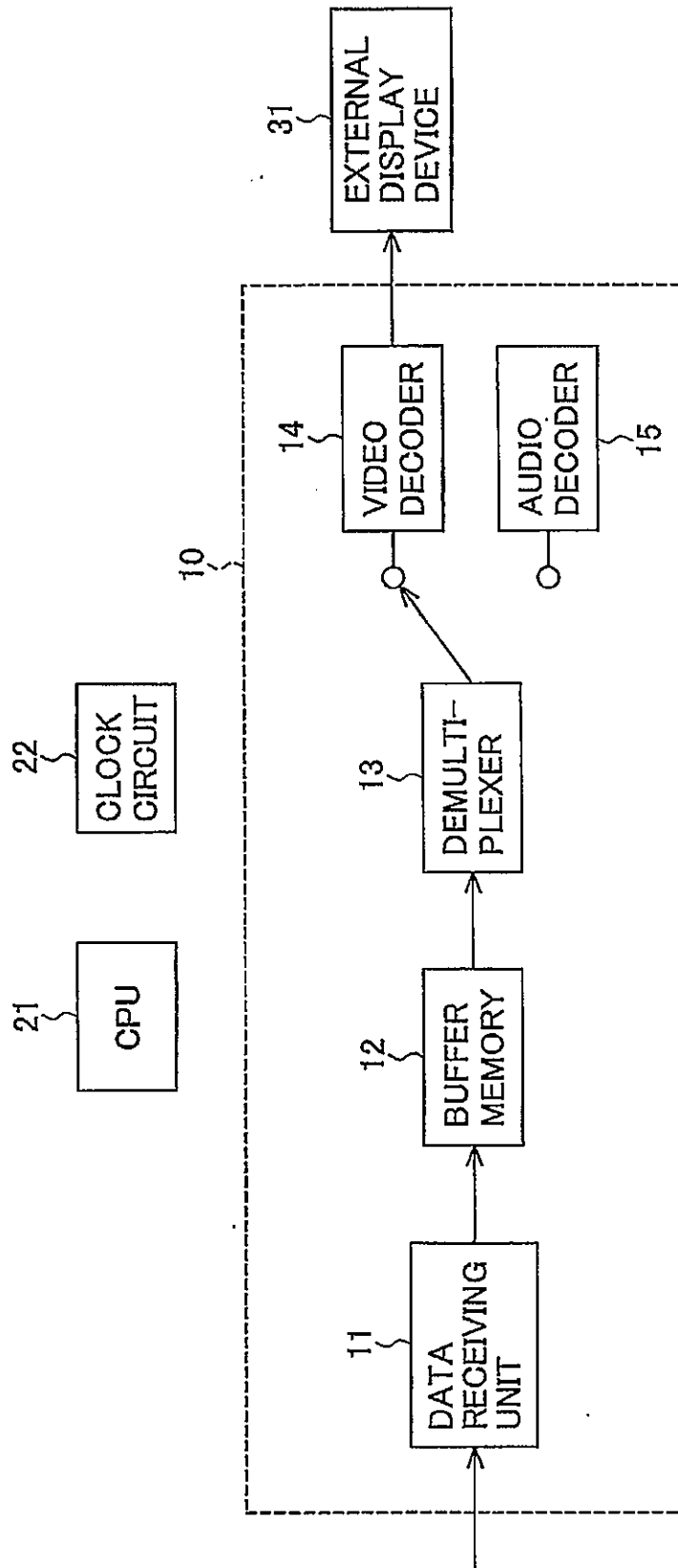
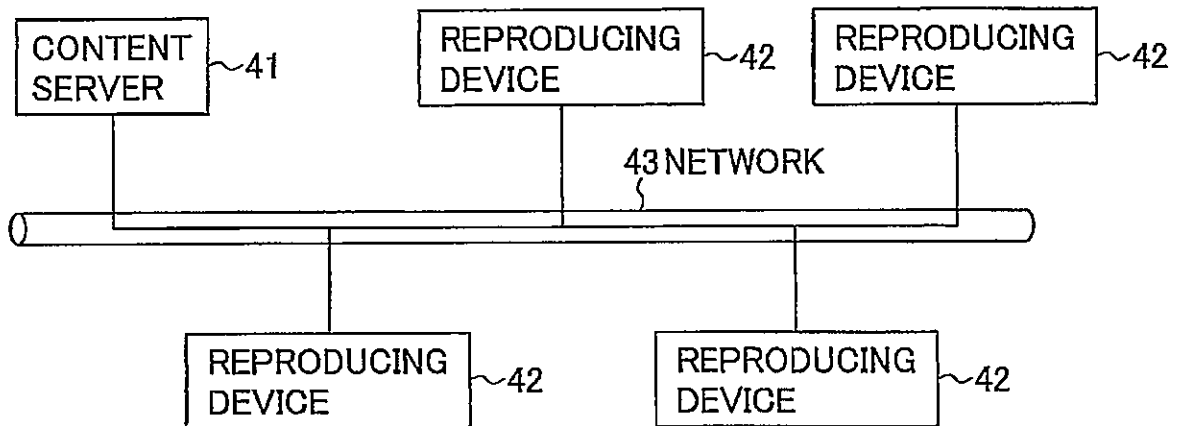


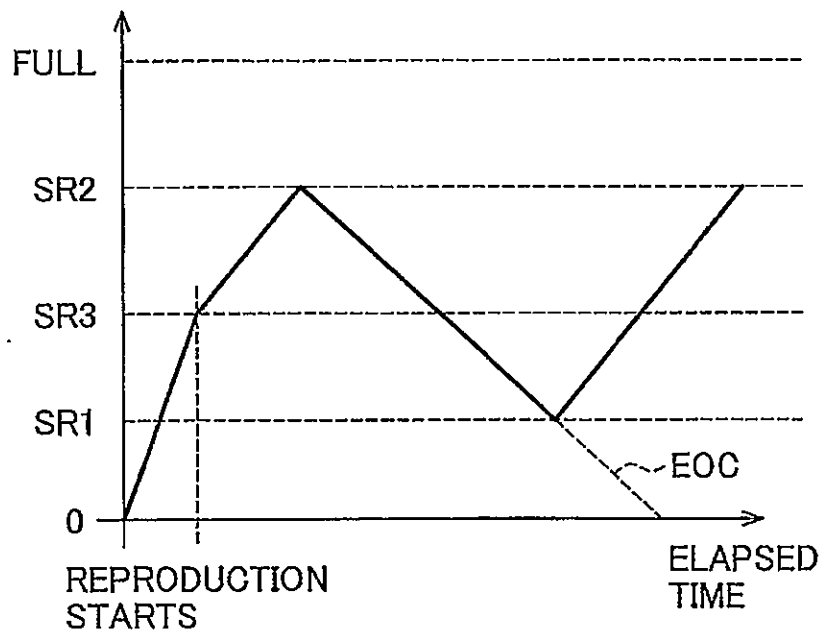
FIG.1



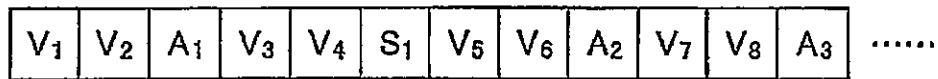
VIDEO REPRODUCING DEVICE
IN THE EMBODIMENT

FIG.2

OVERALL SYSTEM CONFIGURATION
IN THE EMBODIMENT

FIG.3

TRANSITIONS IN AMOUNT OF DATA
REMAINING IN BUFFER MEMORY

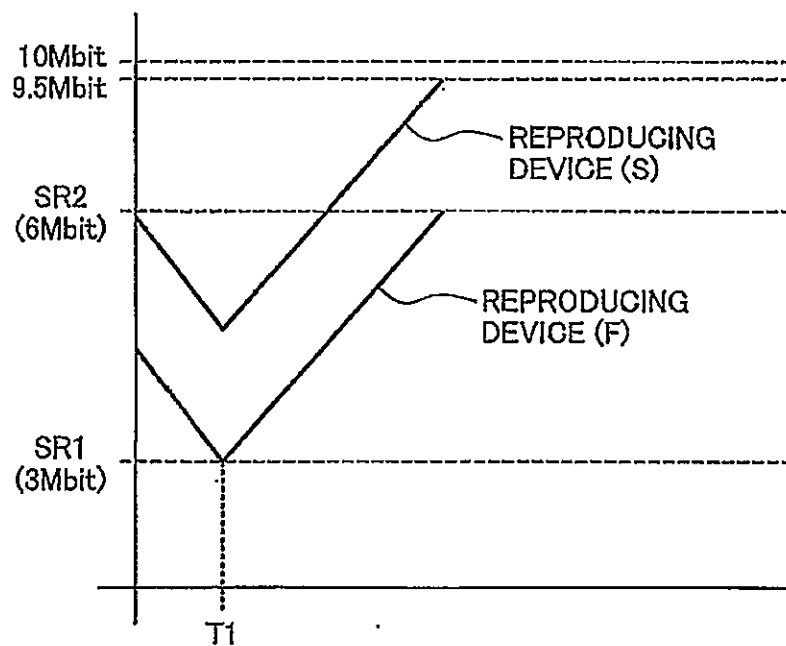
FIG.4

VIDEO PACKET : V₁, V₂, V₃

AUDIO PACKET : A₁, A₂, A₃

PSI/SI PACKET : S₁

GENERAL TS STREAM PACKET STRUCTURE

FIG.5

CHANGES IN AMOUNT OF DATA REMAINING
IN BUFFER OF REPRODUCING DEVICE WITH
FASTEST REPRODUCTION TIME AND REPRODUCING
DEVICE WITH SLOWEST REPRODUCTION TIME

FIG.6

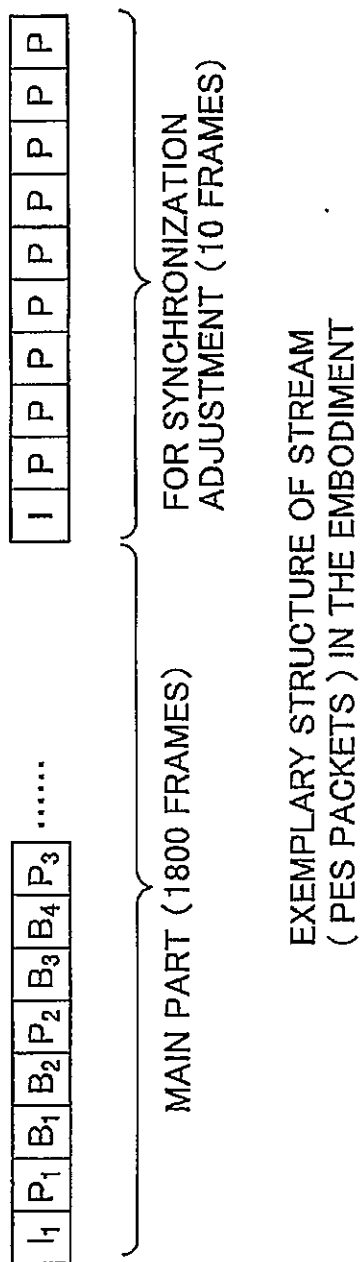
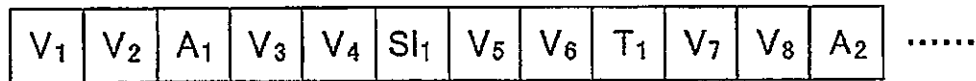


FIG.7

VIDEO PACKET : V₁, V₂, V₃

AUDIO PACKET : A₁, A₂, A₃

PSI/SI PACKET : SI₁

ADJUSTMENT INFORMATION PACKET : T₁

EXEMPLARY TS PACKET STRUCTURE
IN THE EMBODIMENT

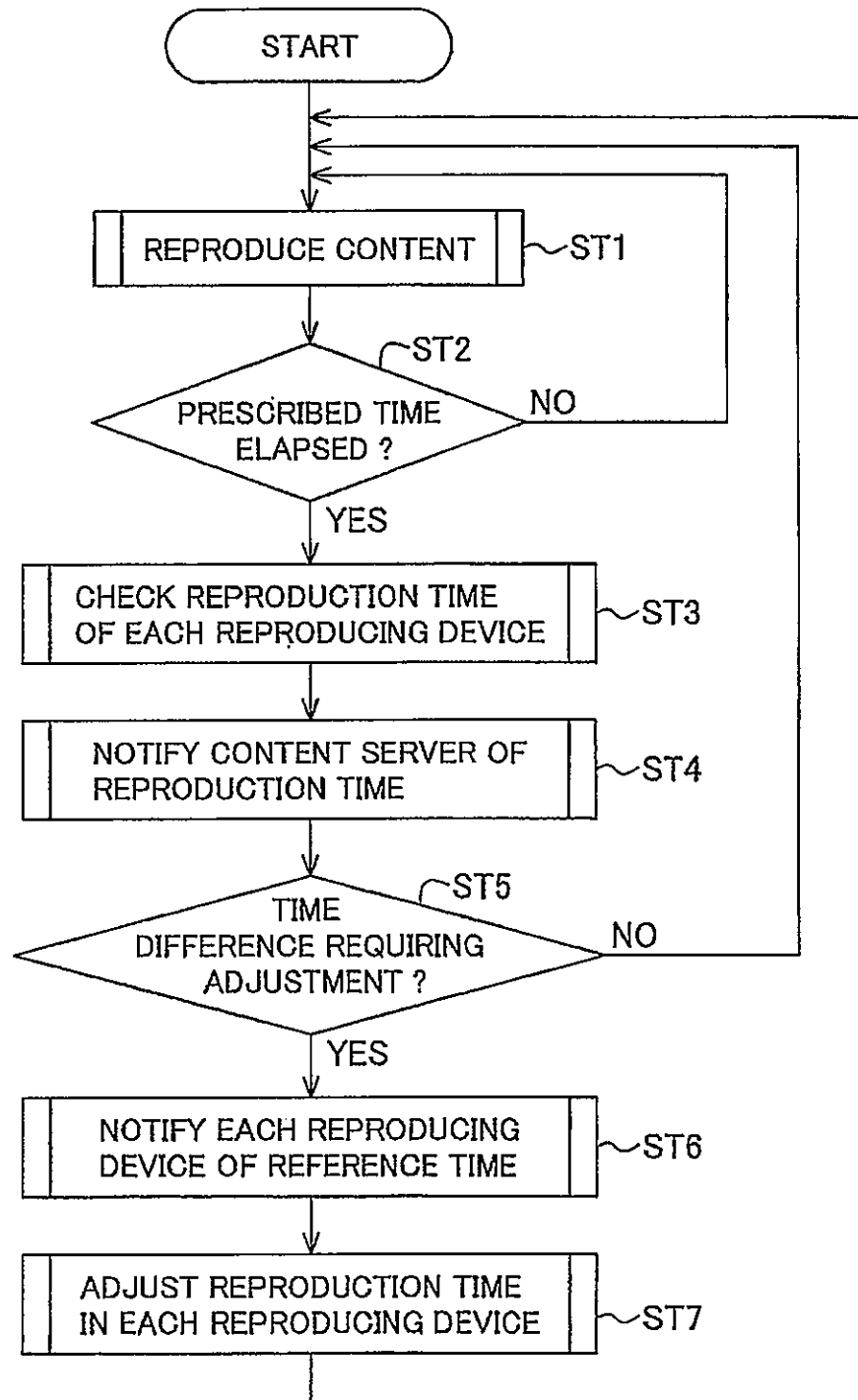
FIG.8

```

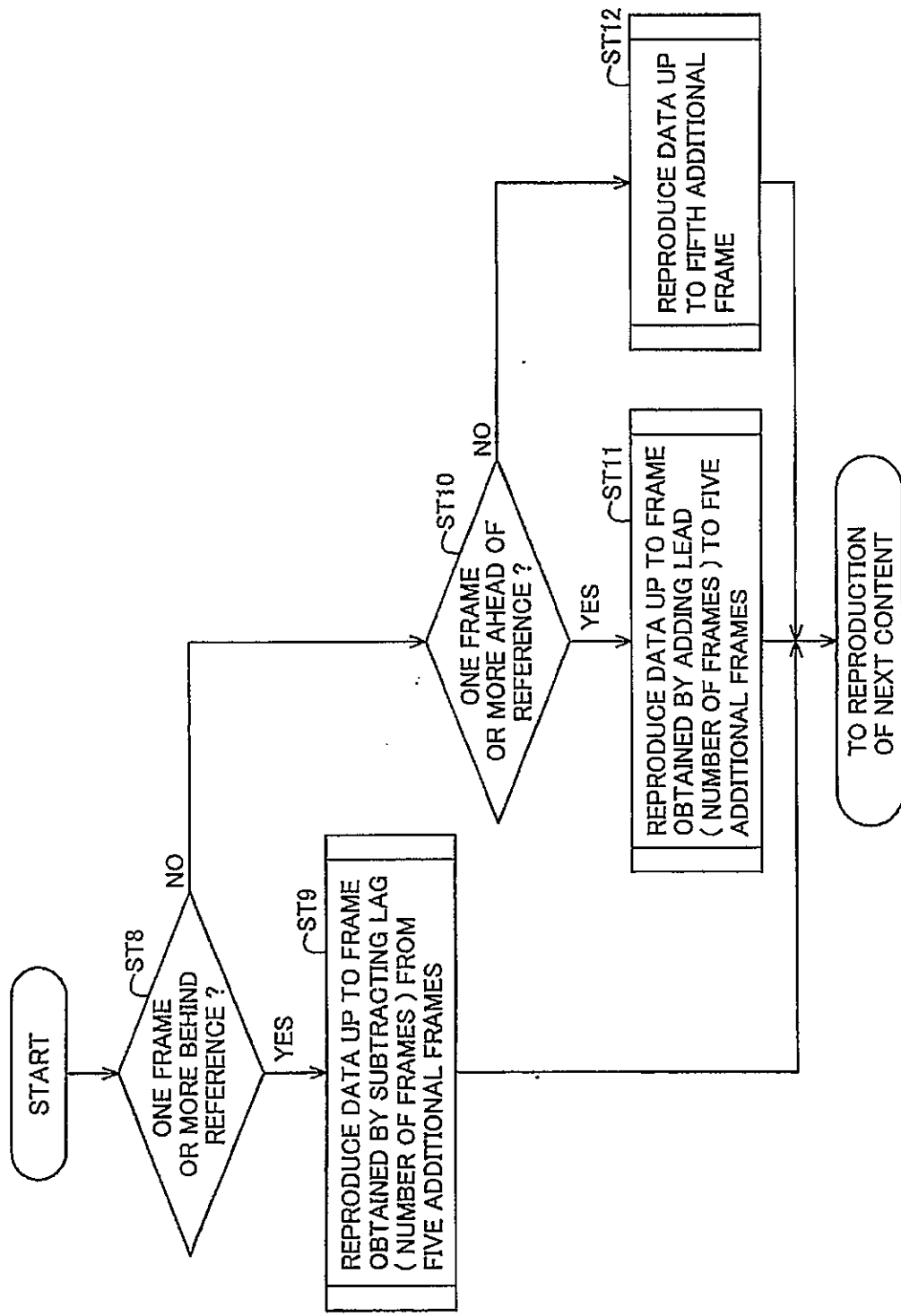
Adjust_info(){
    for(i=0 ; i < num_of_PES ; i++){
        PTS_for_adjust(i);
        SPN_for_adjust(i);
    }
}

```

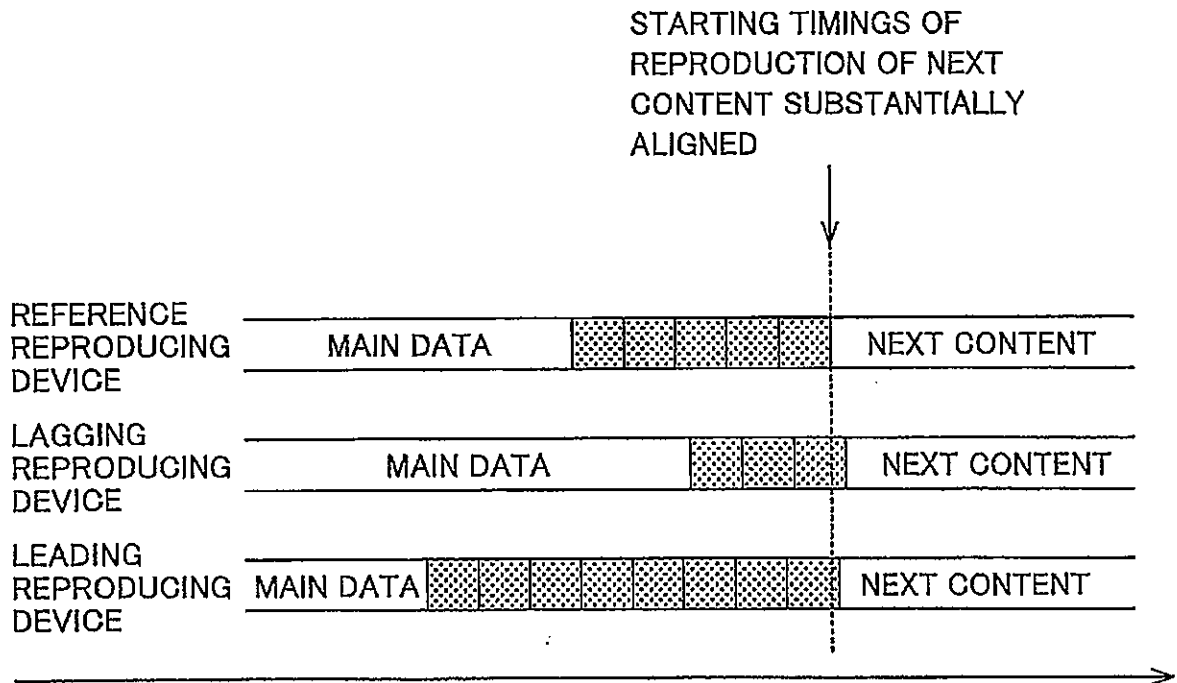
SYNTAX OF SYNCHRONIZATION ADJUSTMENT
PACKET IN THE EMBODIMENT

FIG.9

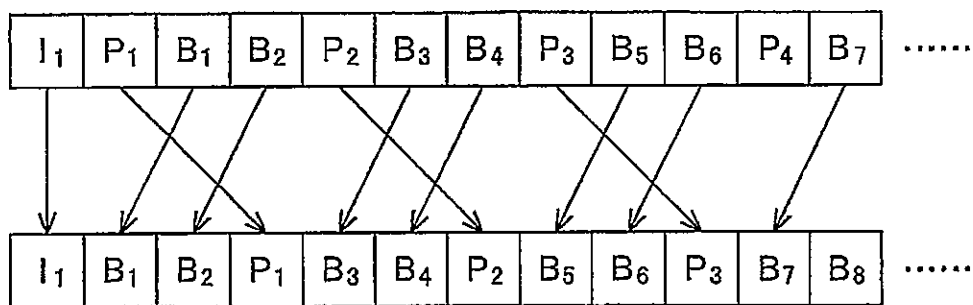
PROCESSING SEQUENCE
IN FIRST EMBODIMENT



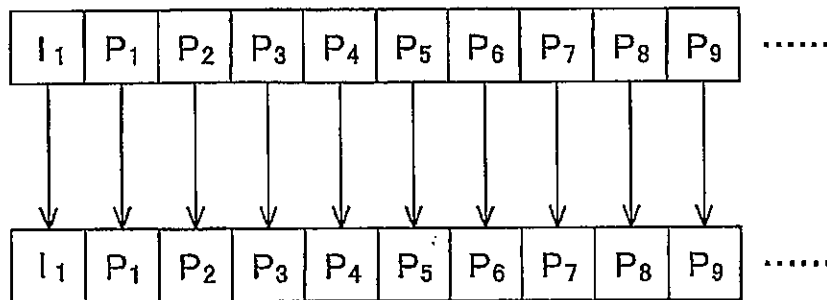
PROCESSING SEQUENCE IN
FIRST EMBODIMENT

FIG.11

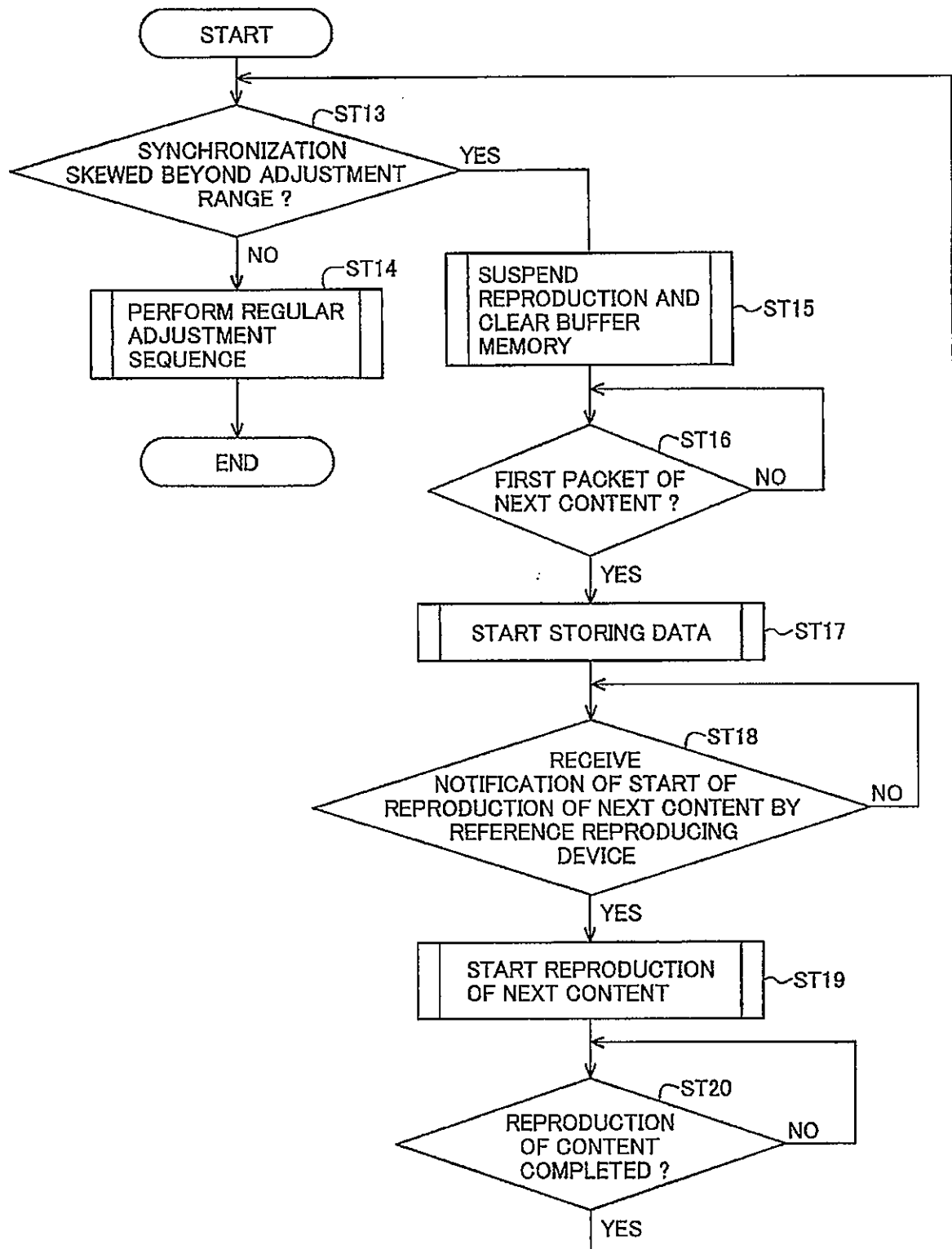
REPRODUCTION TIMINGS OF REPRODUCING DEVICES
AFTER SYNCHRONIZATION ADJUSTMENT

FIG.12

RELATIONSHIP BETWEEN TYPICAL MPEG DATA
DECODING AND DISPLAY SEQUENCES

FIG.13

RELATIONSHIP BETWEEN DECODING AND DISPLAY
SEQUENCES OF SYNCHRONIZATION ADJUSTMENT
FRAMES IN THE EMBODIMENT

FIG.14

PROCESSING SEQUENCE IN
SECOND EMBODIMENT