

## VIA/LINE INDUCTOR ON SEMICONDUCTOR MATERIAL

### ABSTRACT

A spiral inductor is provided including a substrate and an inductor dielectric layer  
5 over the substrate having a spiral opening provided therein. The spiral inductor is in the  
spiral opening with the spiral inductor including a plurality of parallel spiral vias connected  
together at center proximate and center distal ends of the spiral inductor.

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FIGURE 4