

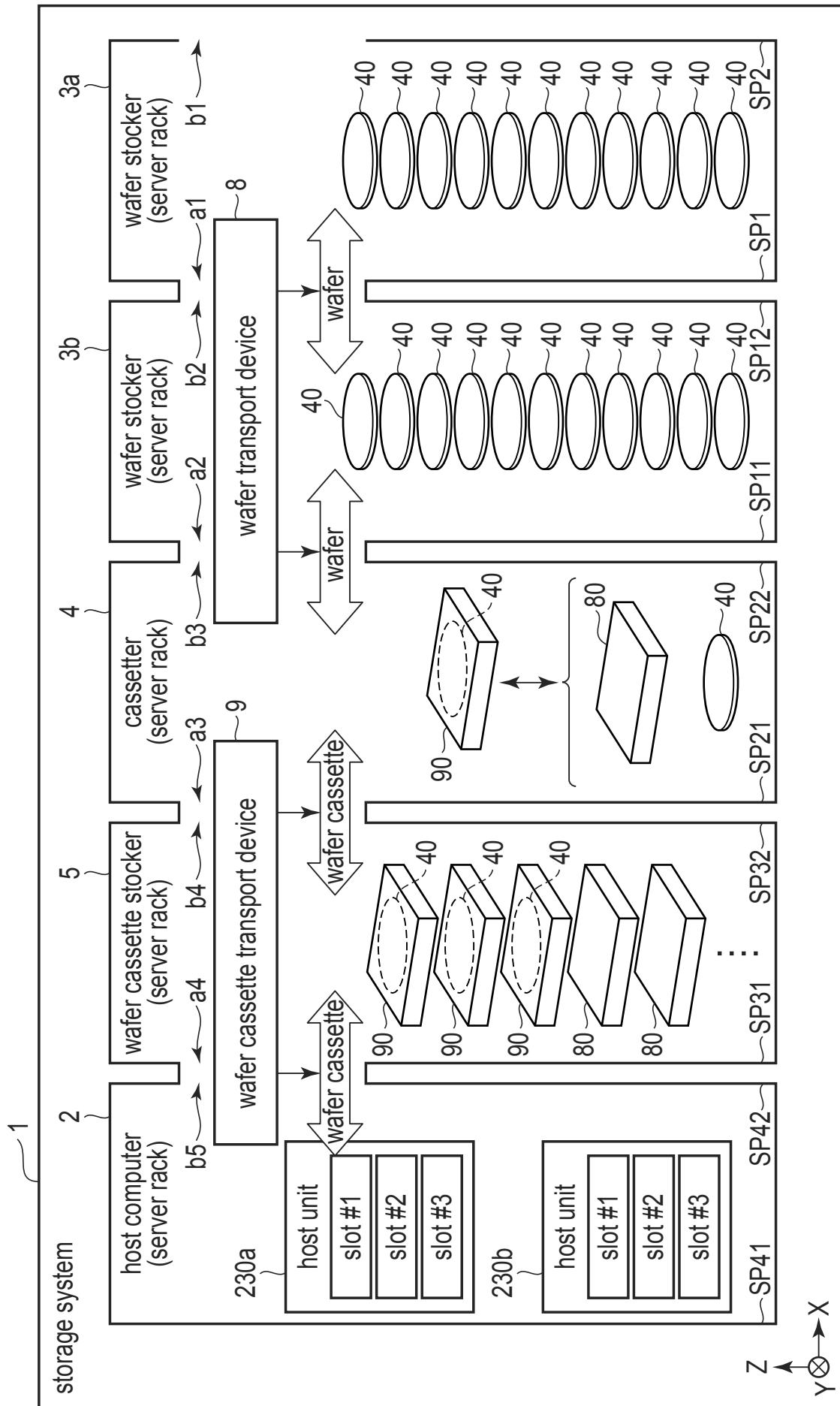
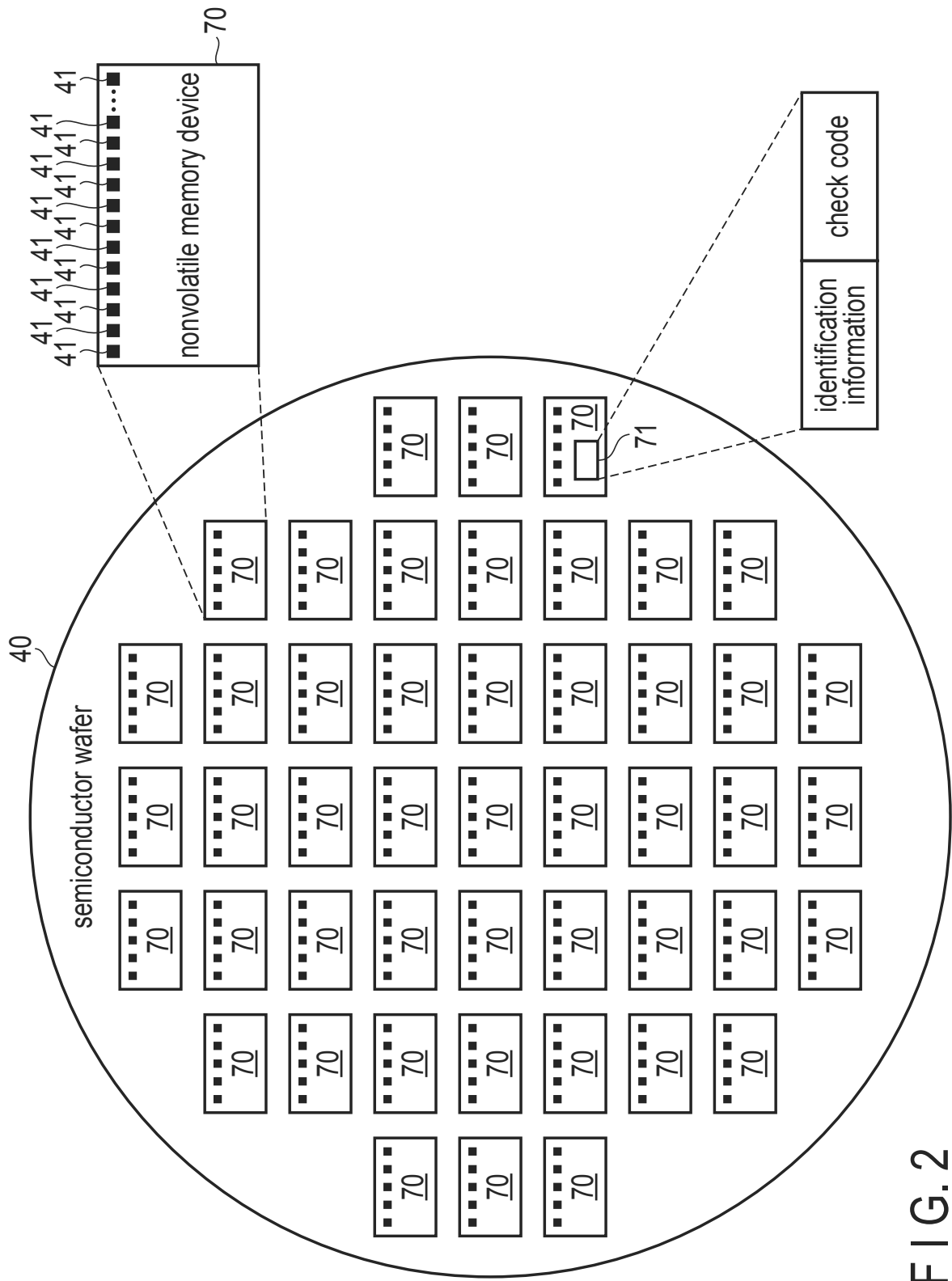


FIG. 1



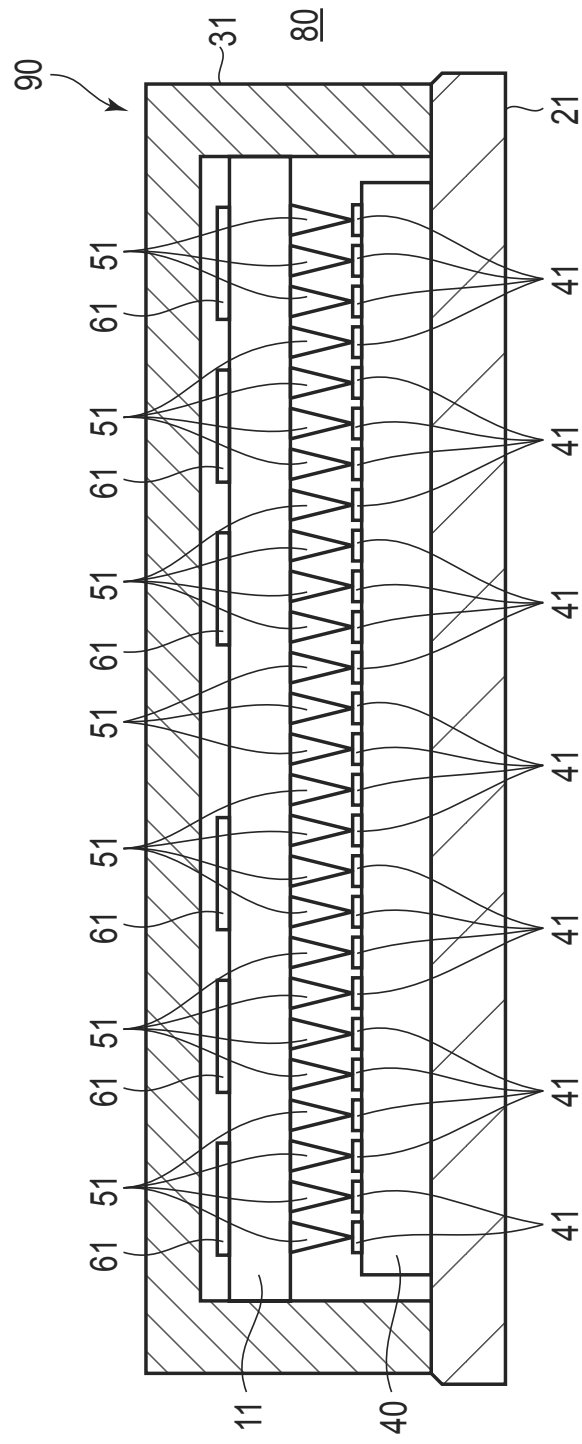


FIG. 3

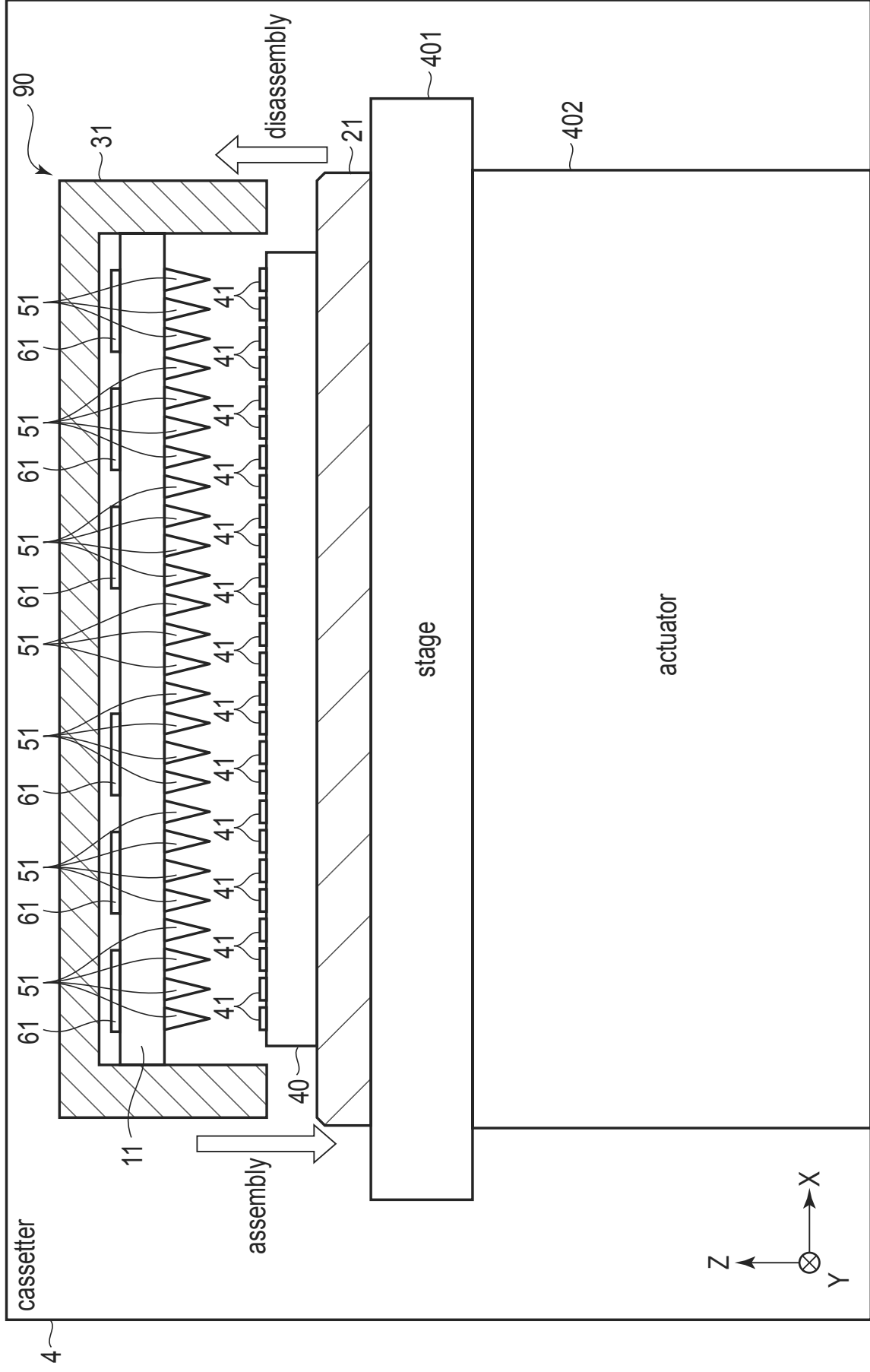


FIG. 4

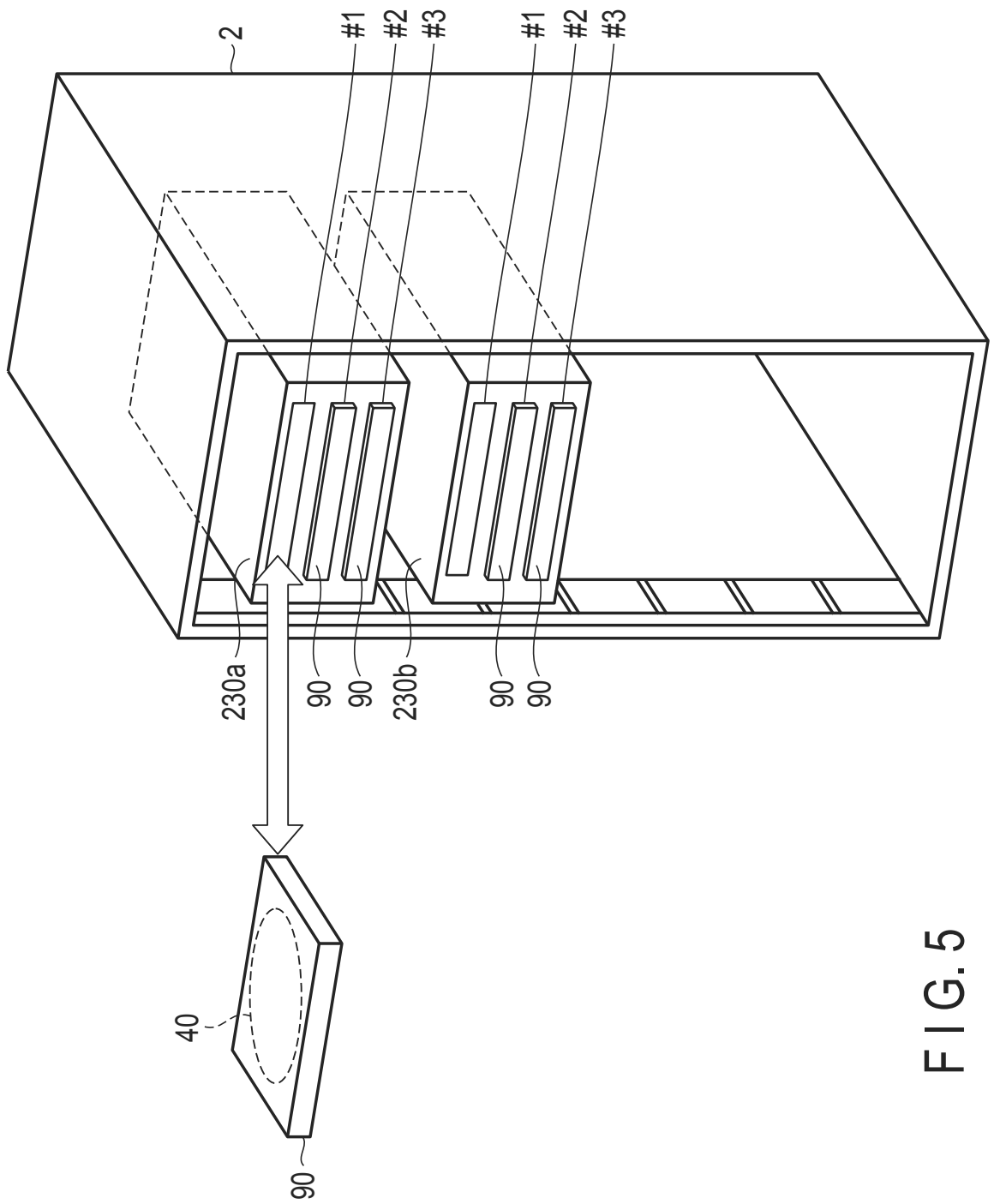


FIG. 5

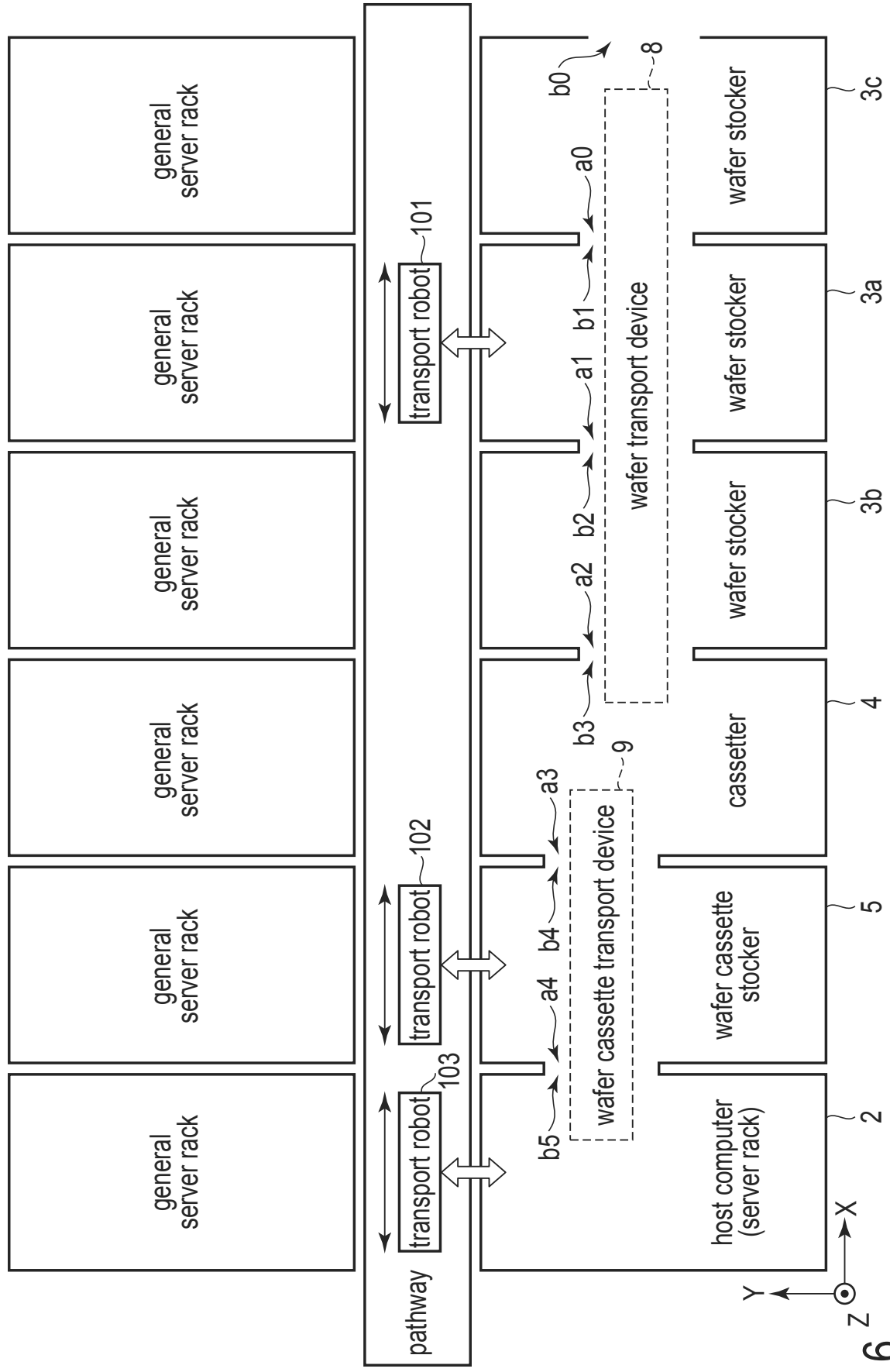


FIG. 6

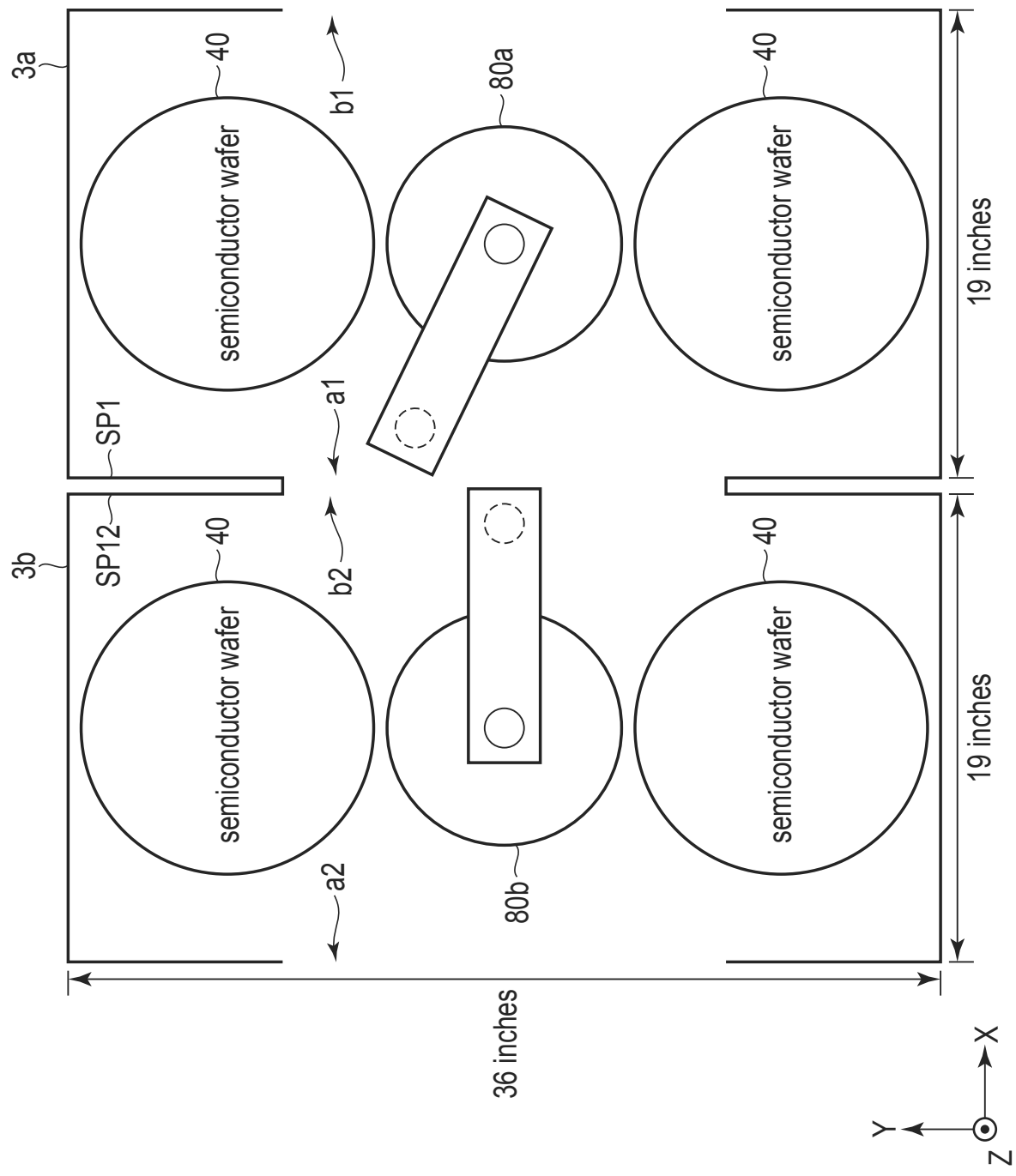


FIG. 7

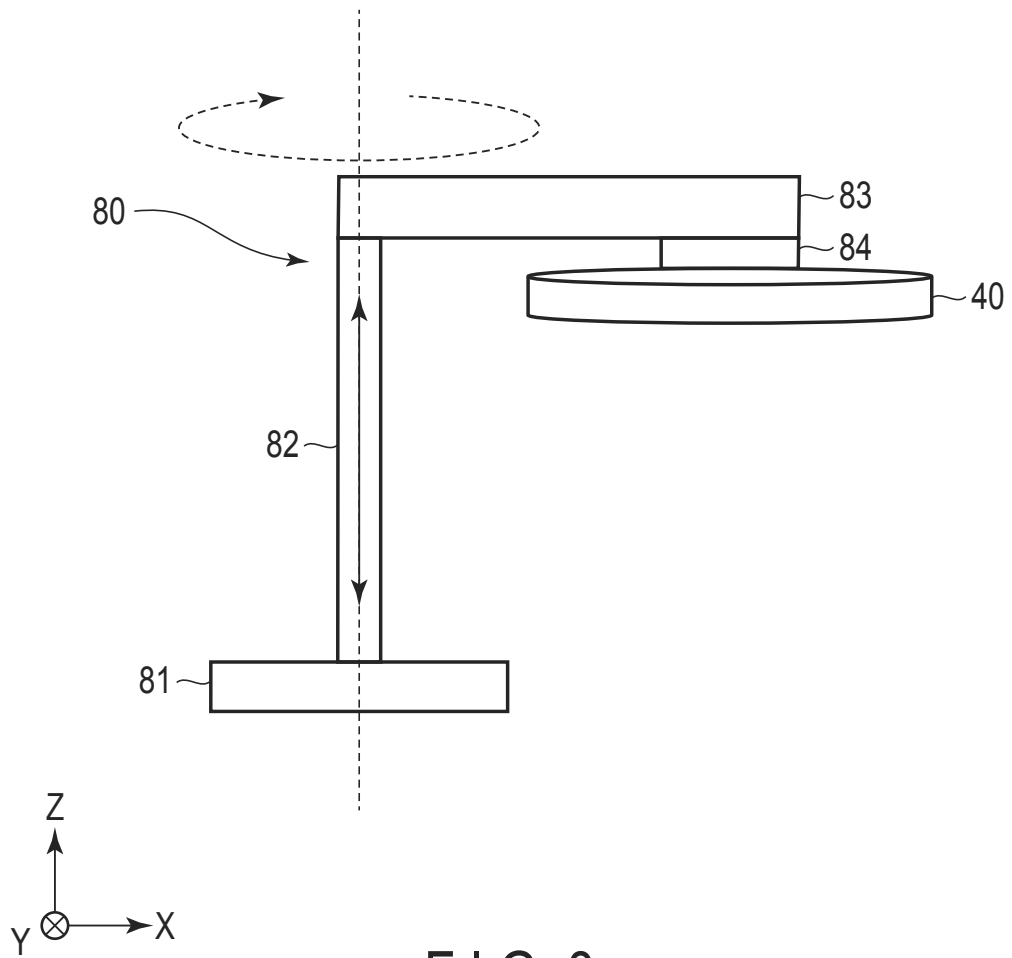


FIG. 8

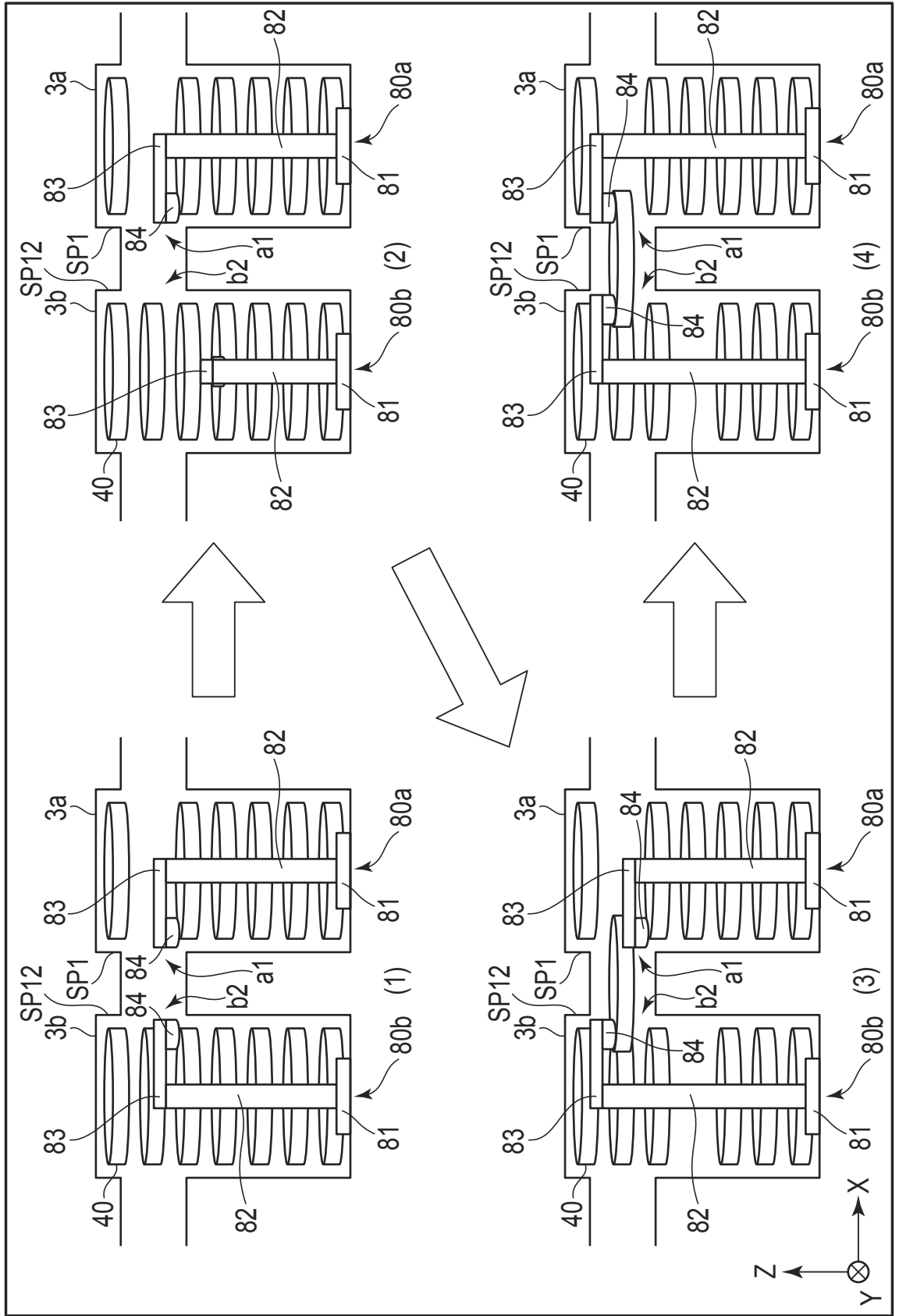


FIG. 9A

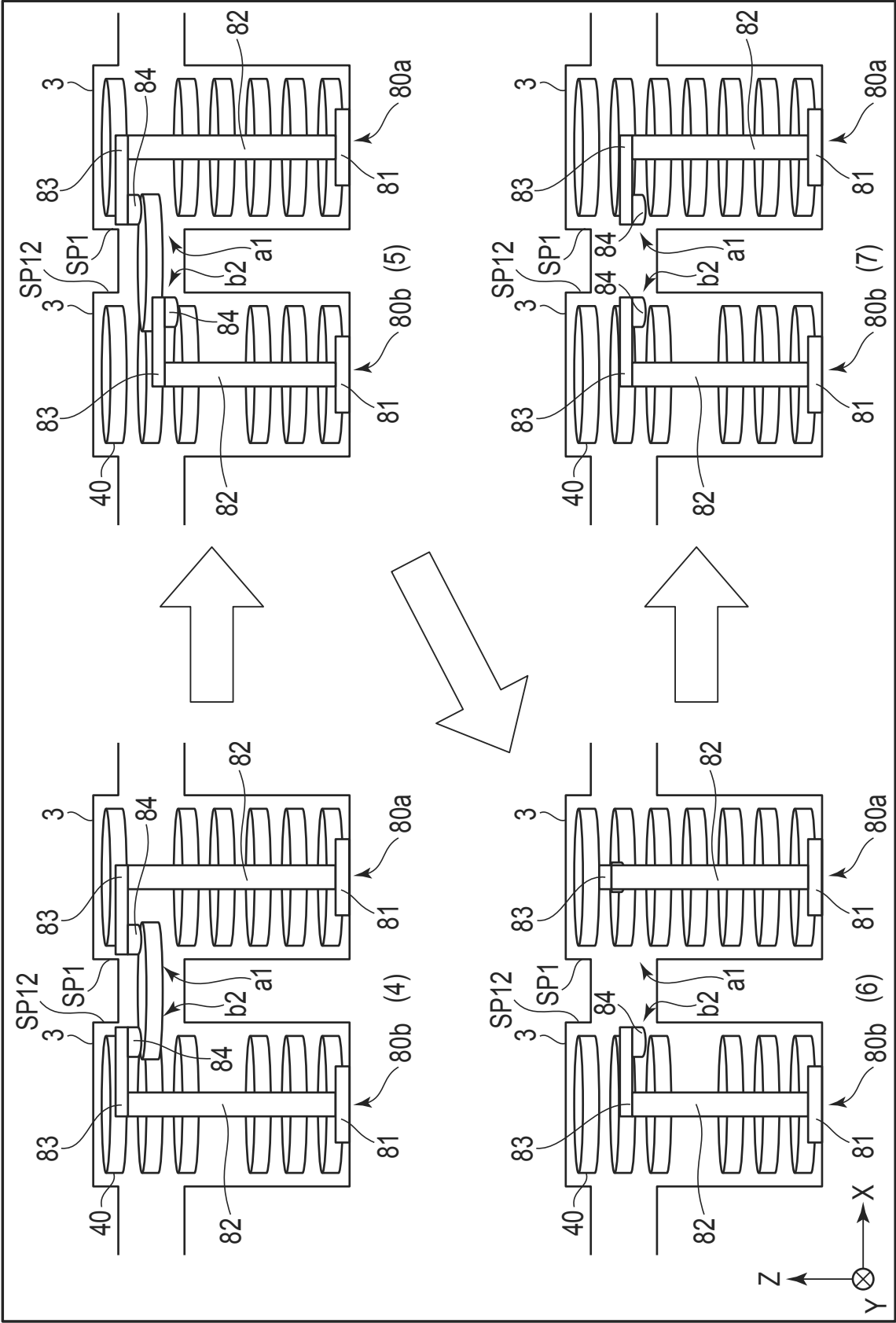


FIG. 9B

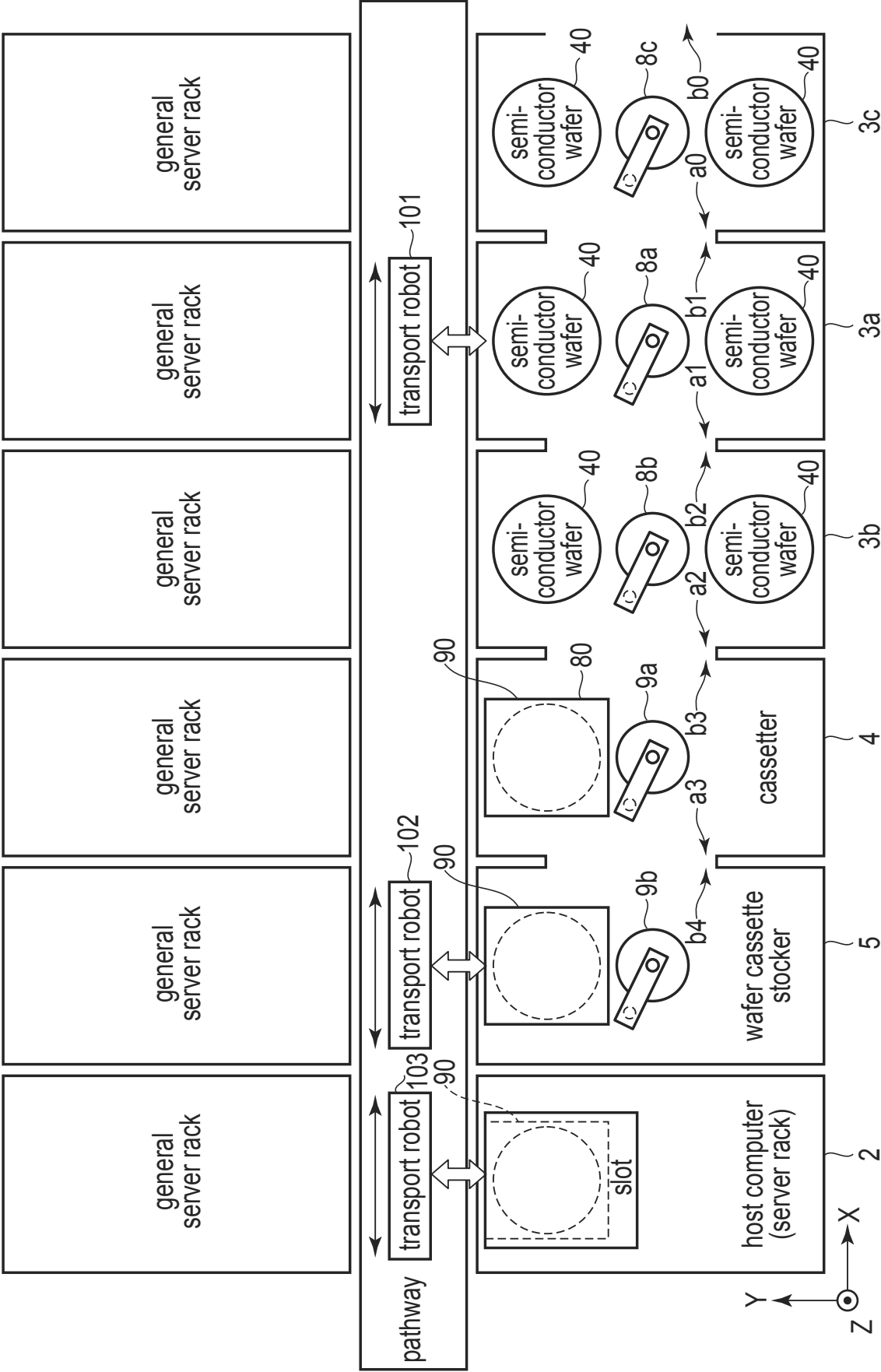


FIG. 10

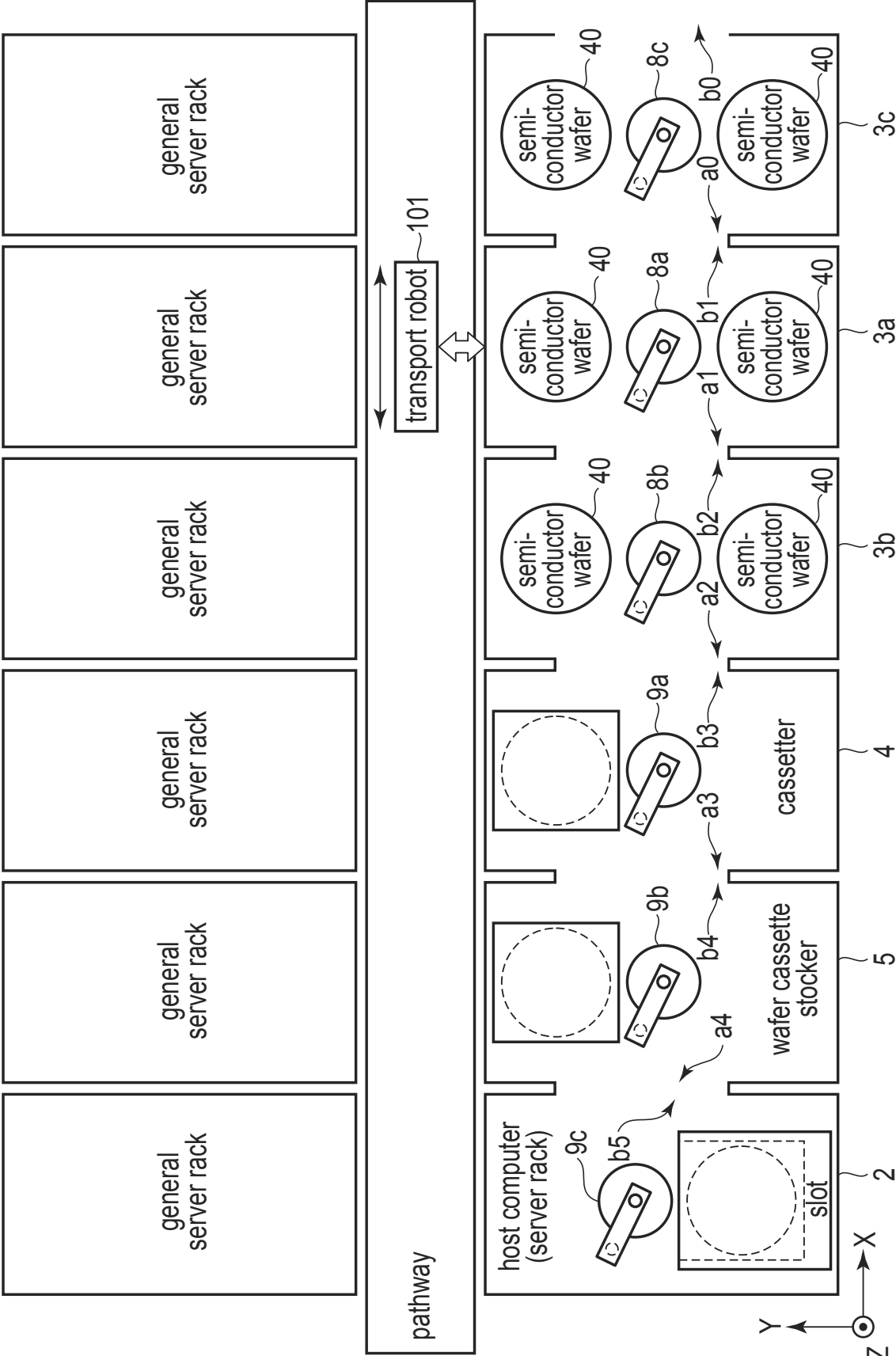


FIG. 11

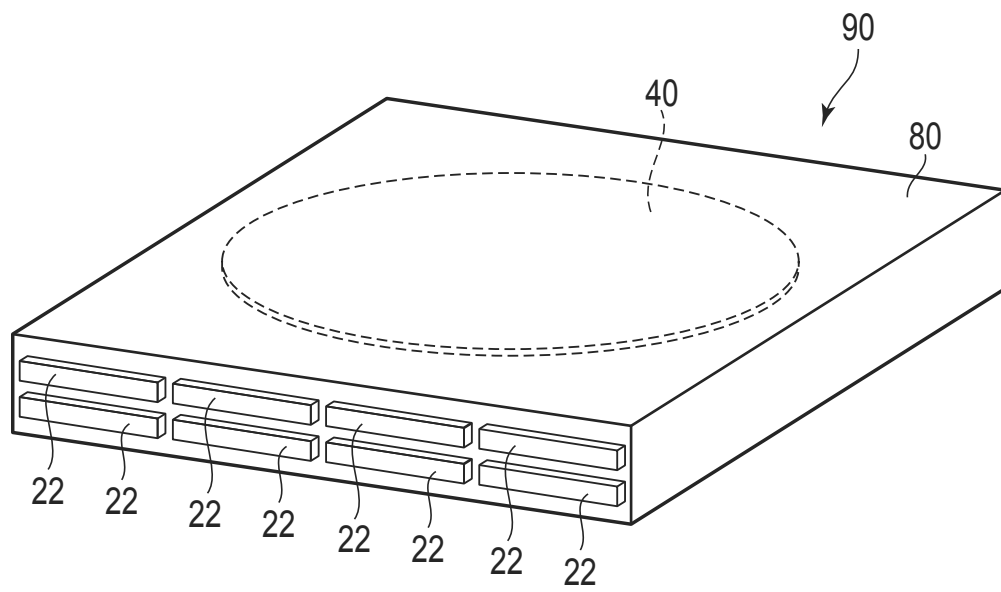


FIG. 12

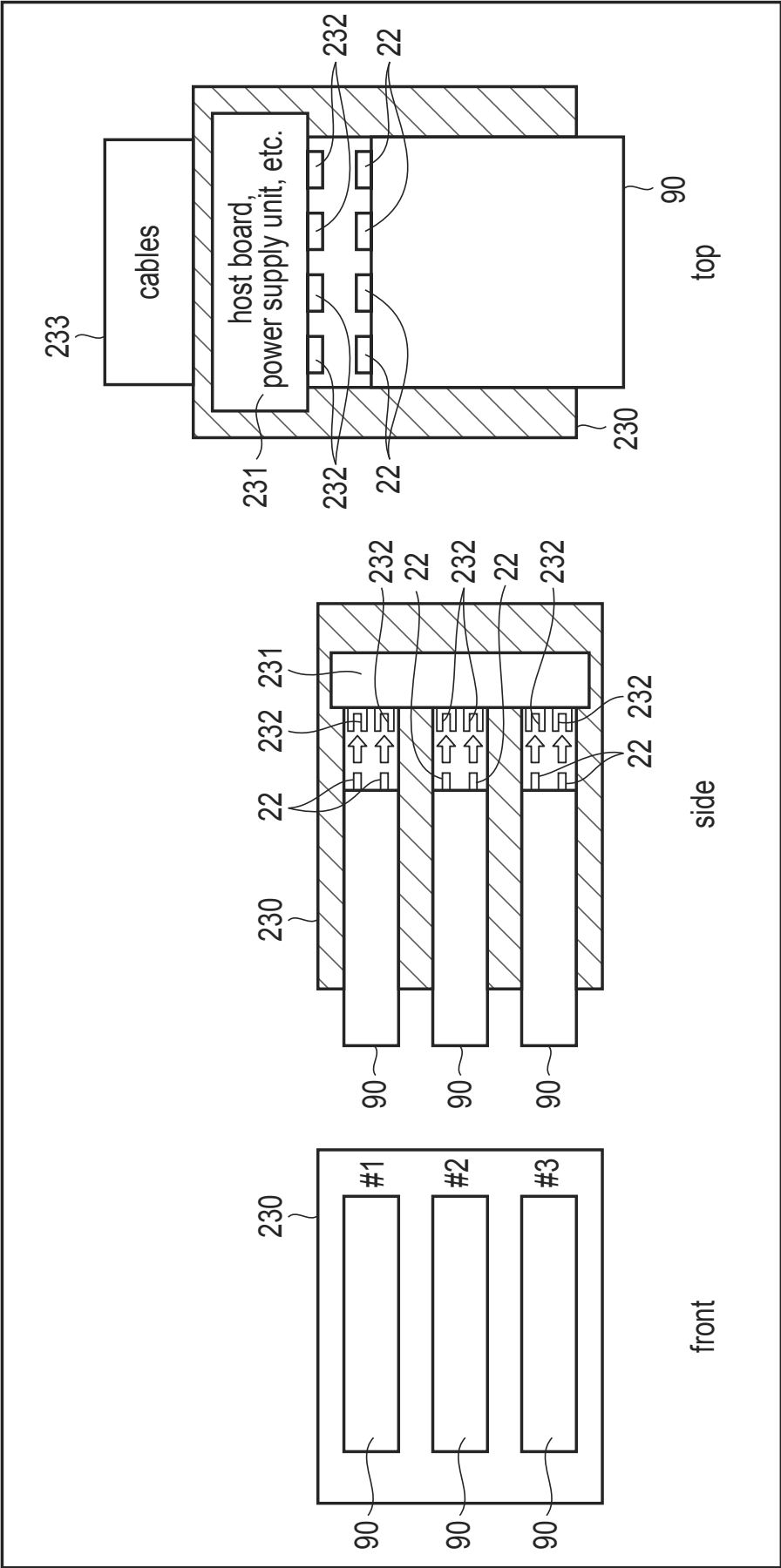


FIG. 13

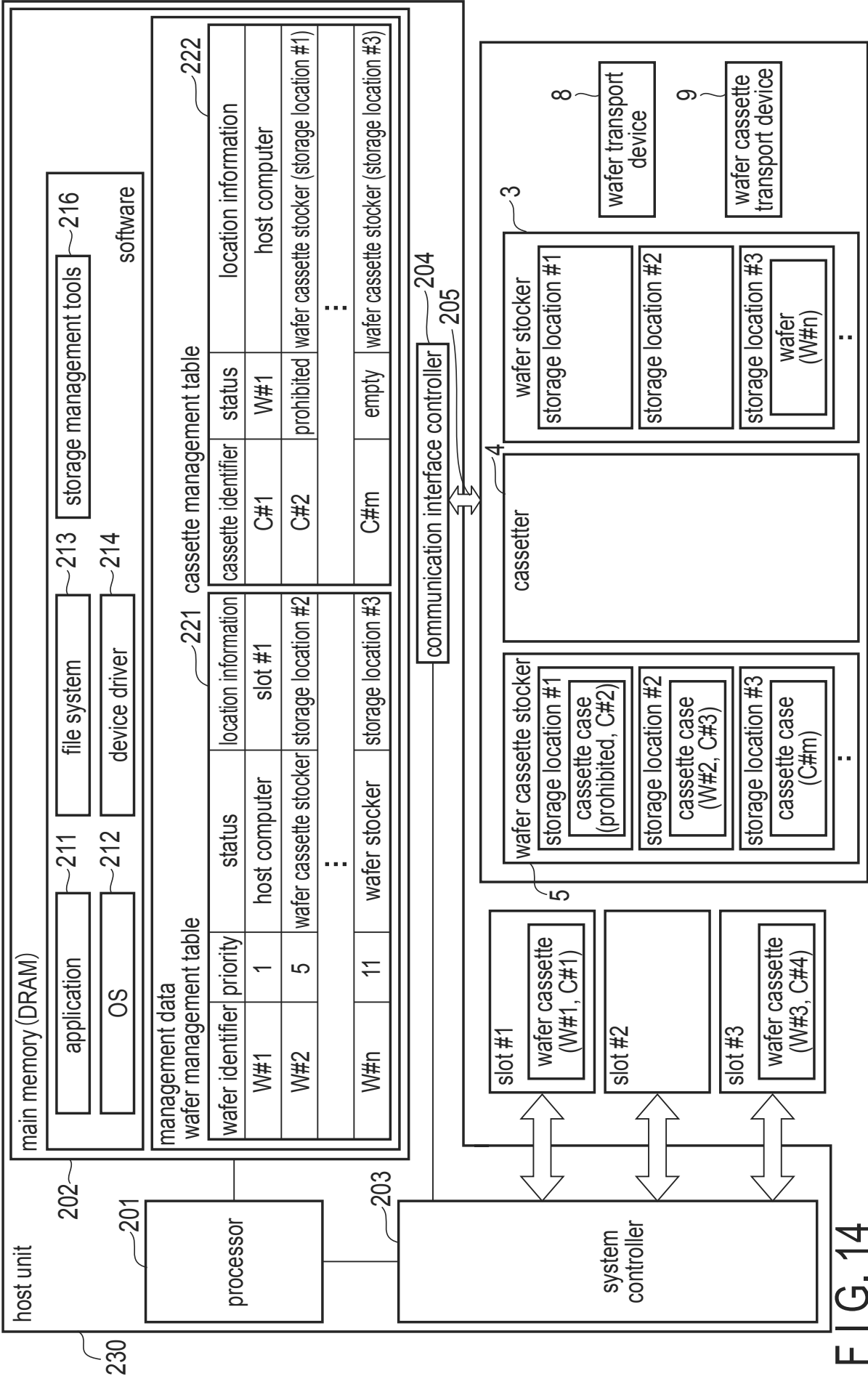


FIG. 14

•swap (when there is no free slot in host unit and there is a wafer to be accessed in wafer cassette stocker) 221

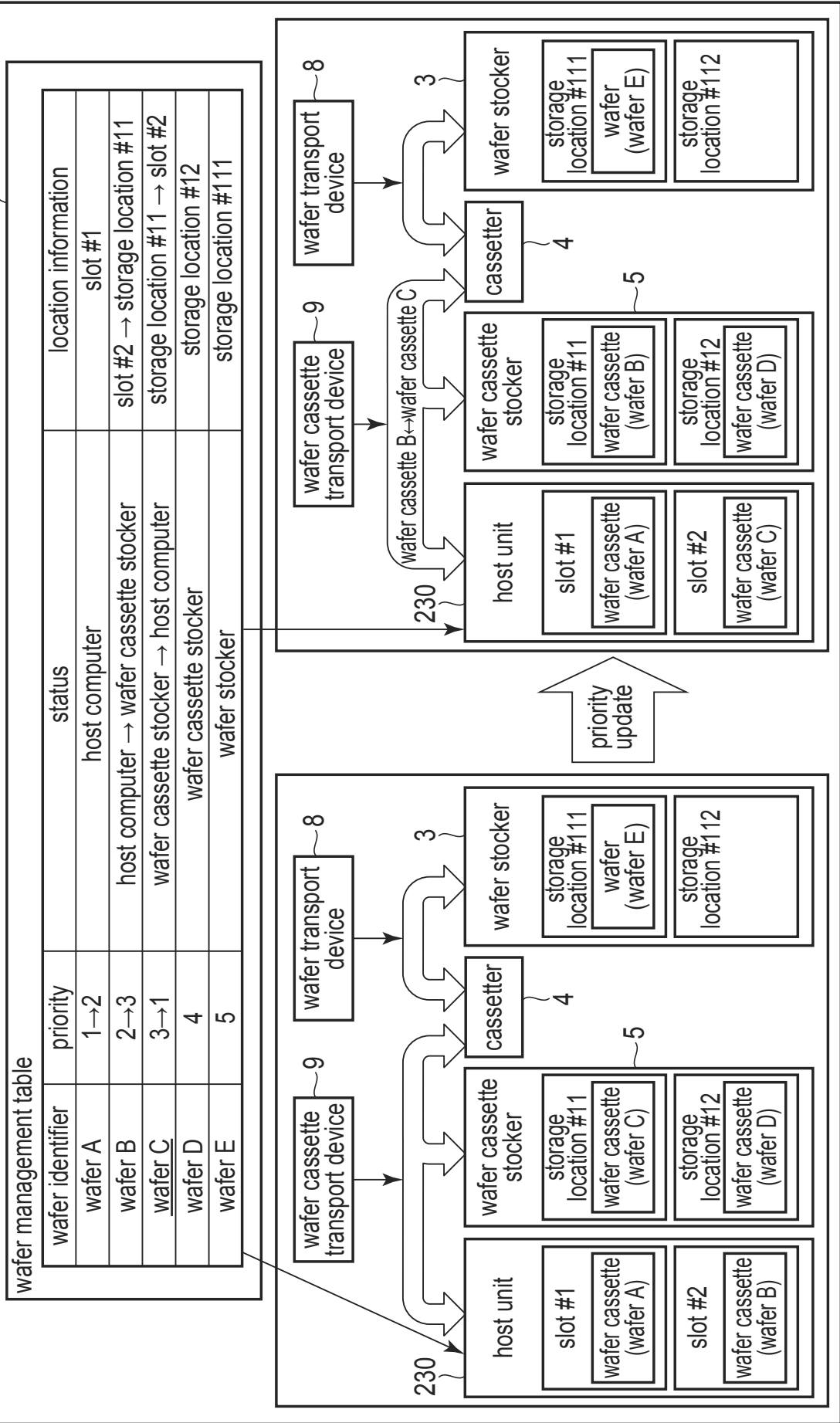


FIG. 15A

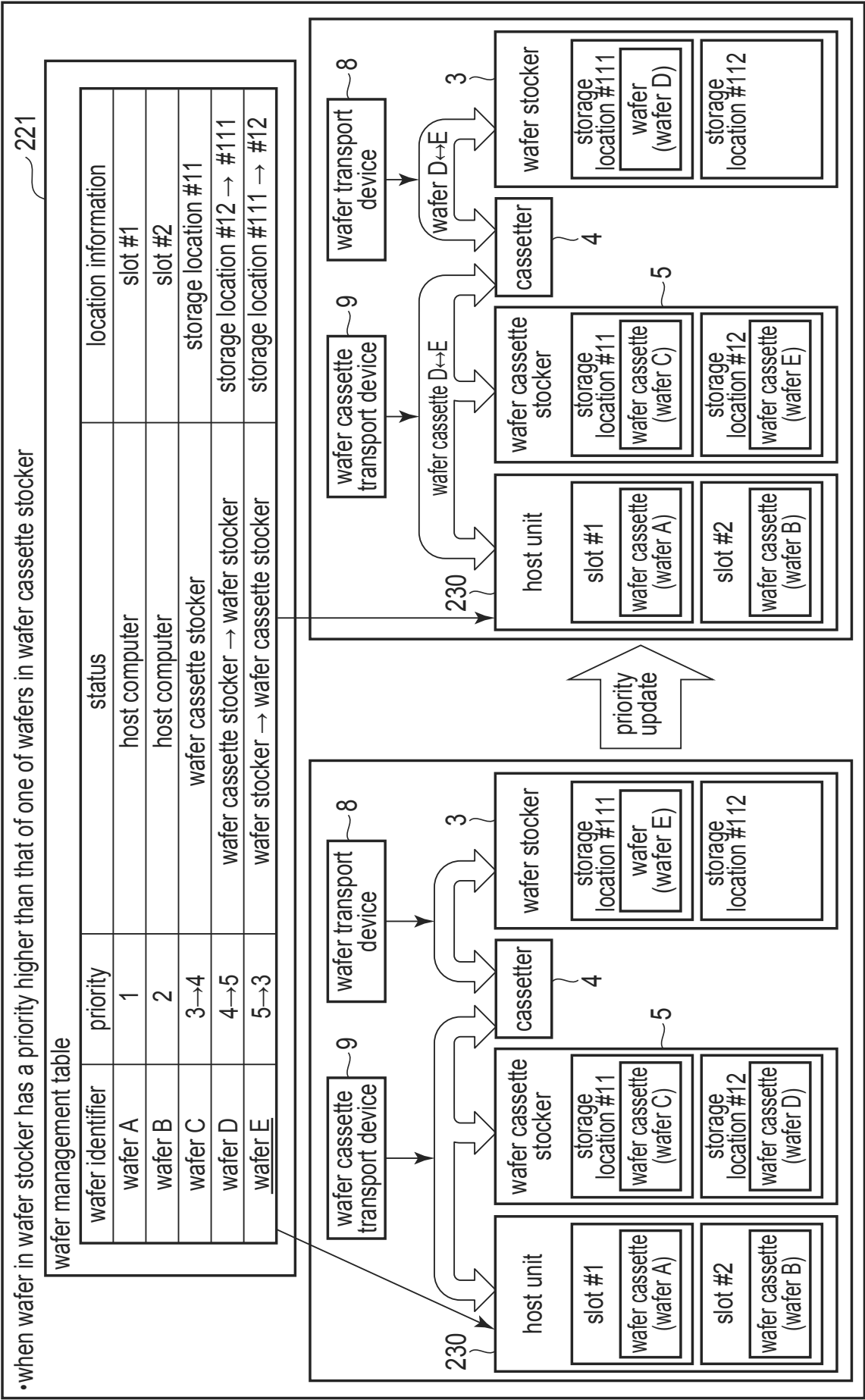


FIG. 15B

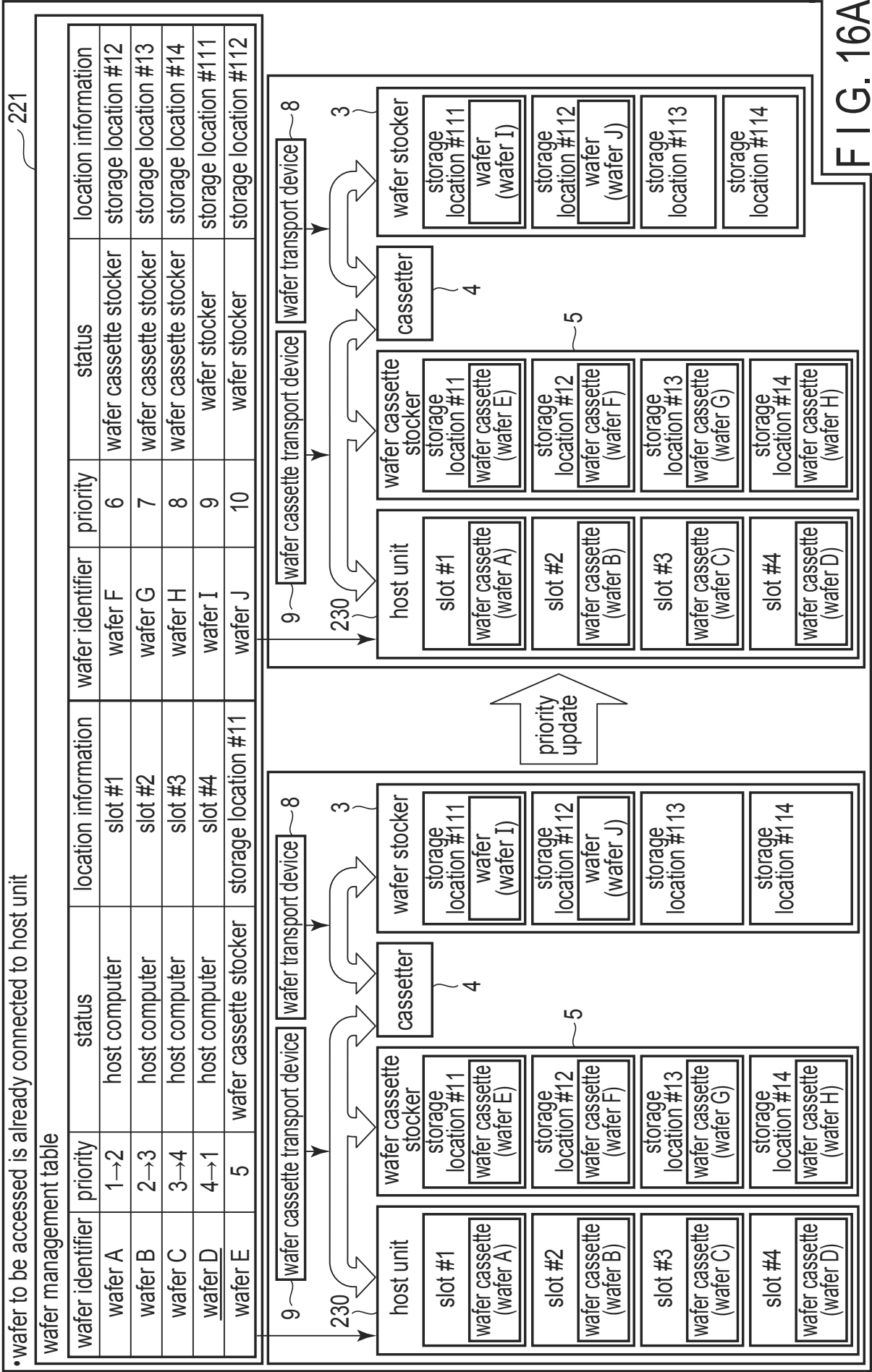
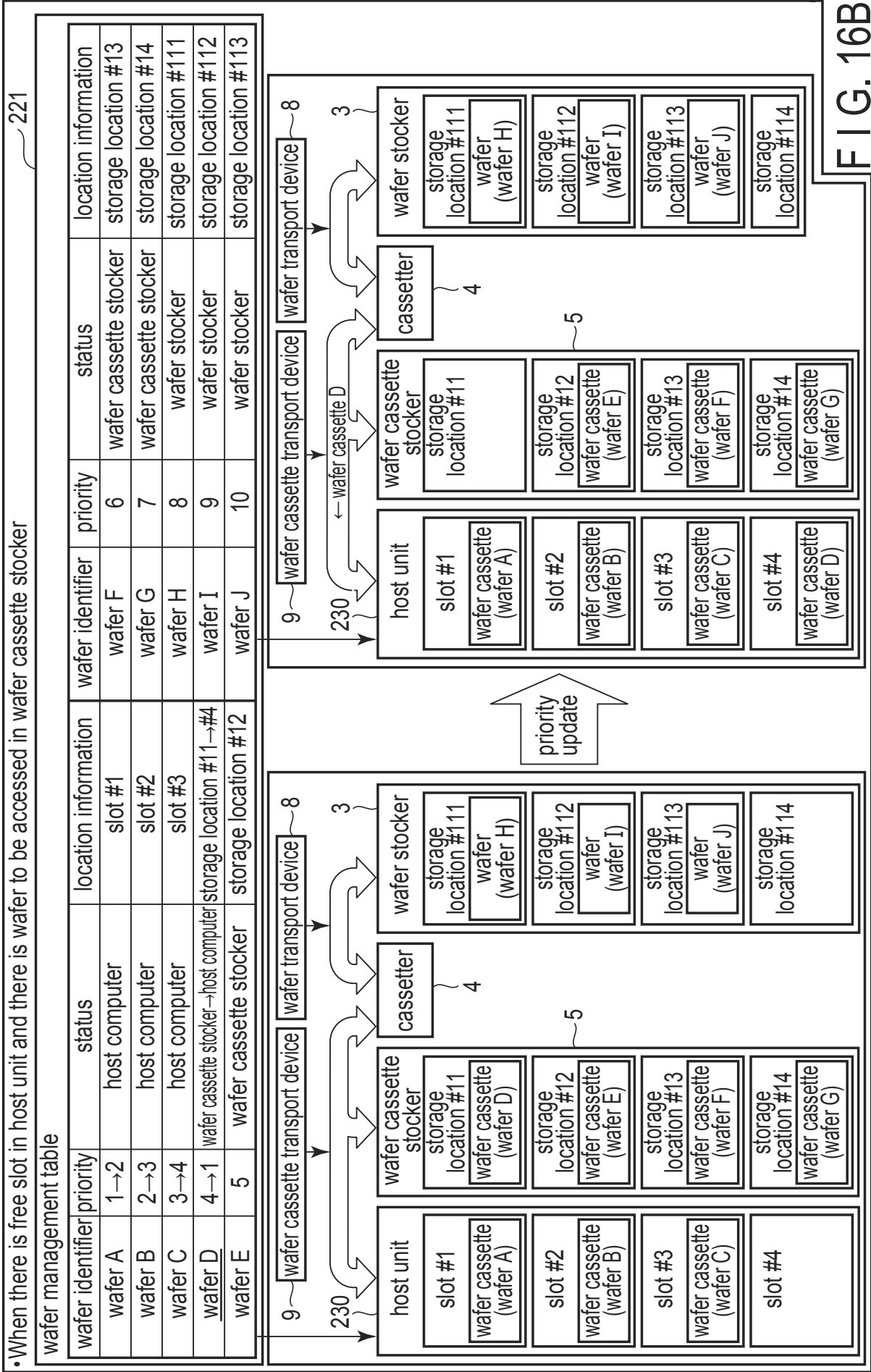
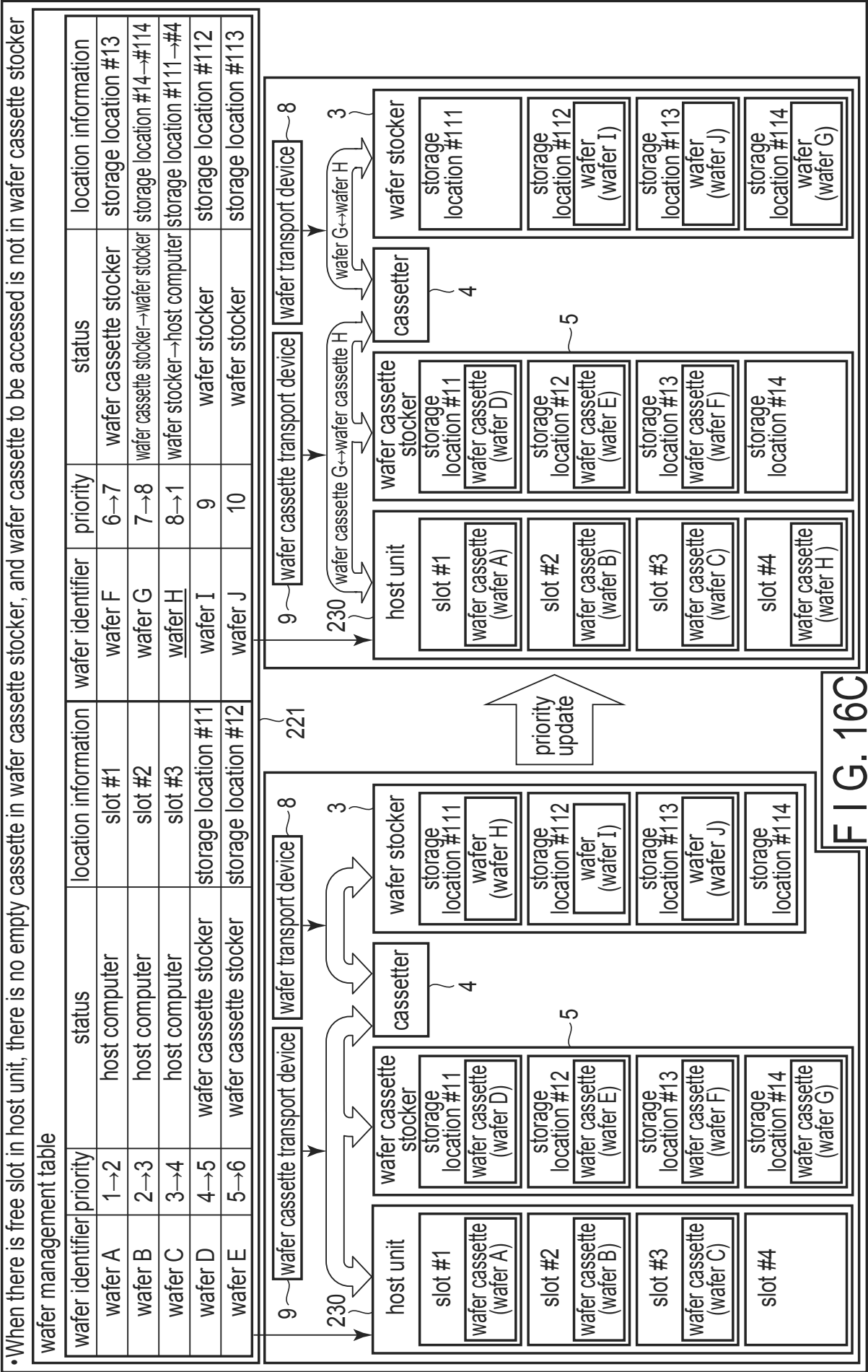


FIG. 16A





•When there is free slot in host unit, there is empty cassette in wafer cassette stocker, and wafer cassette to be accessed is not in wafer cassette stocker

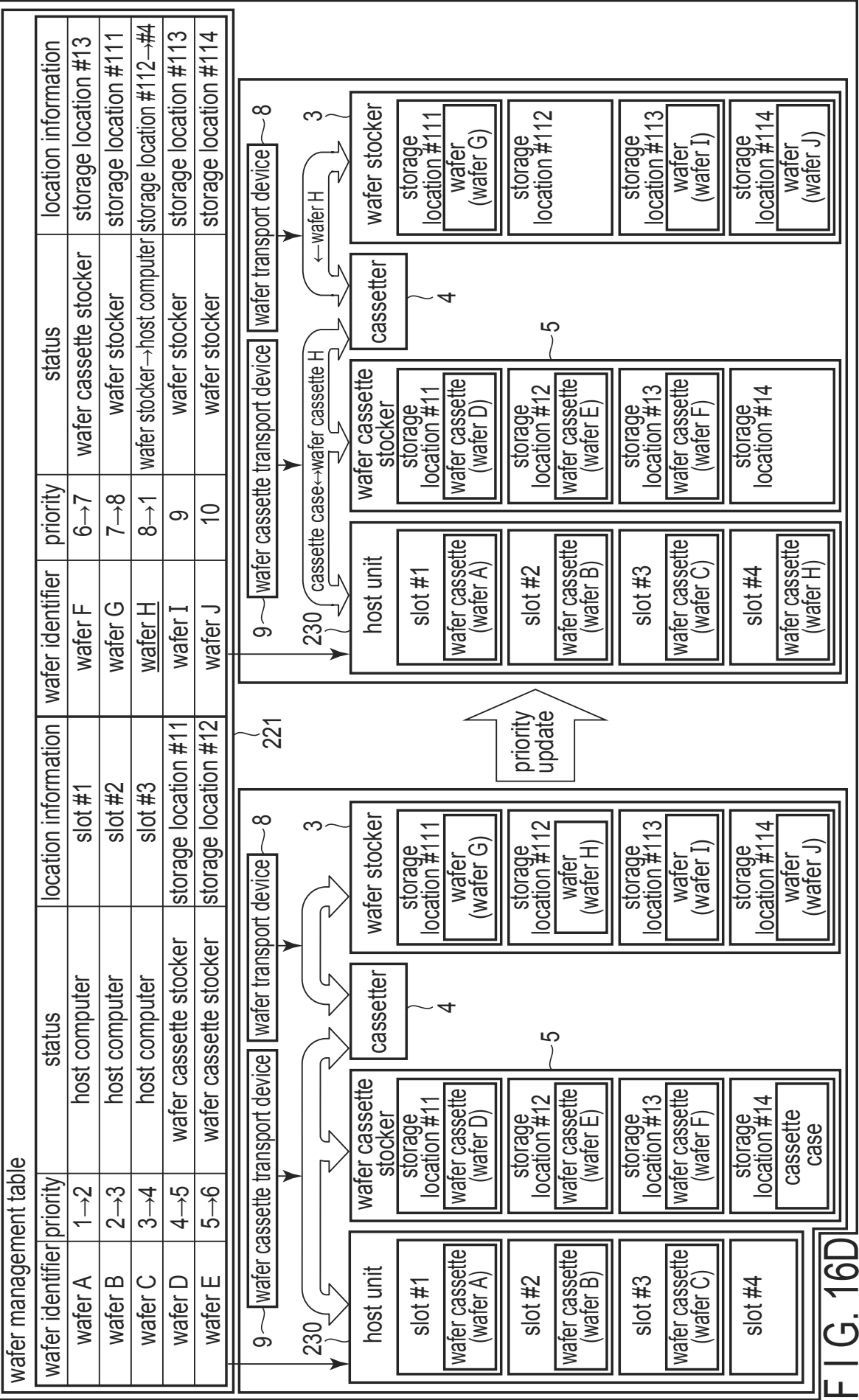
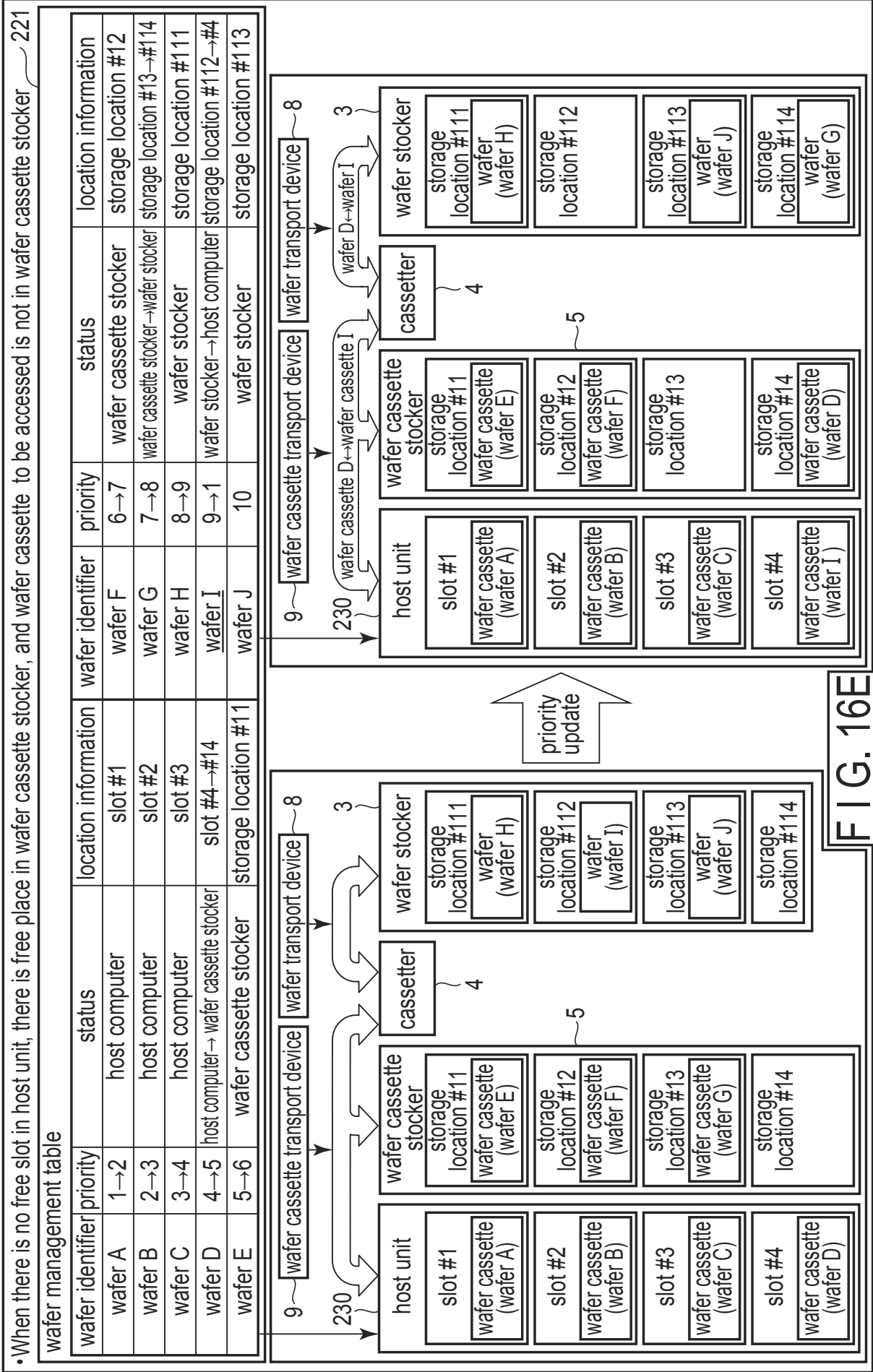


FIG. 16D



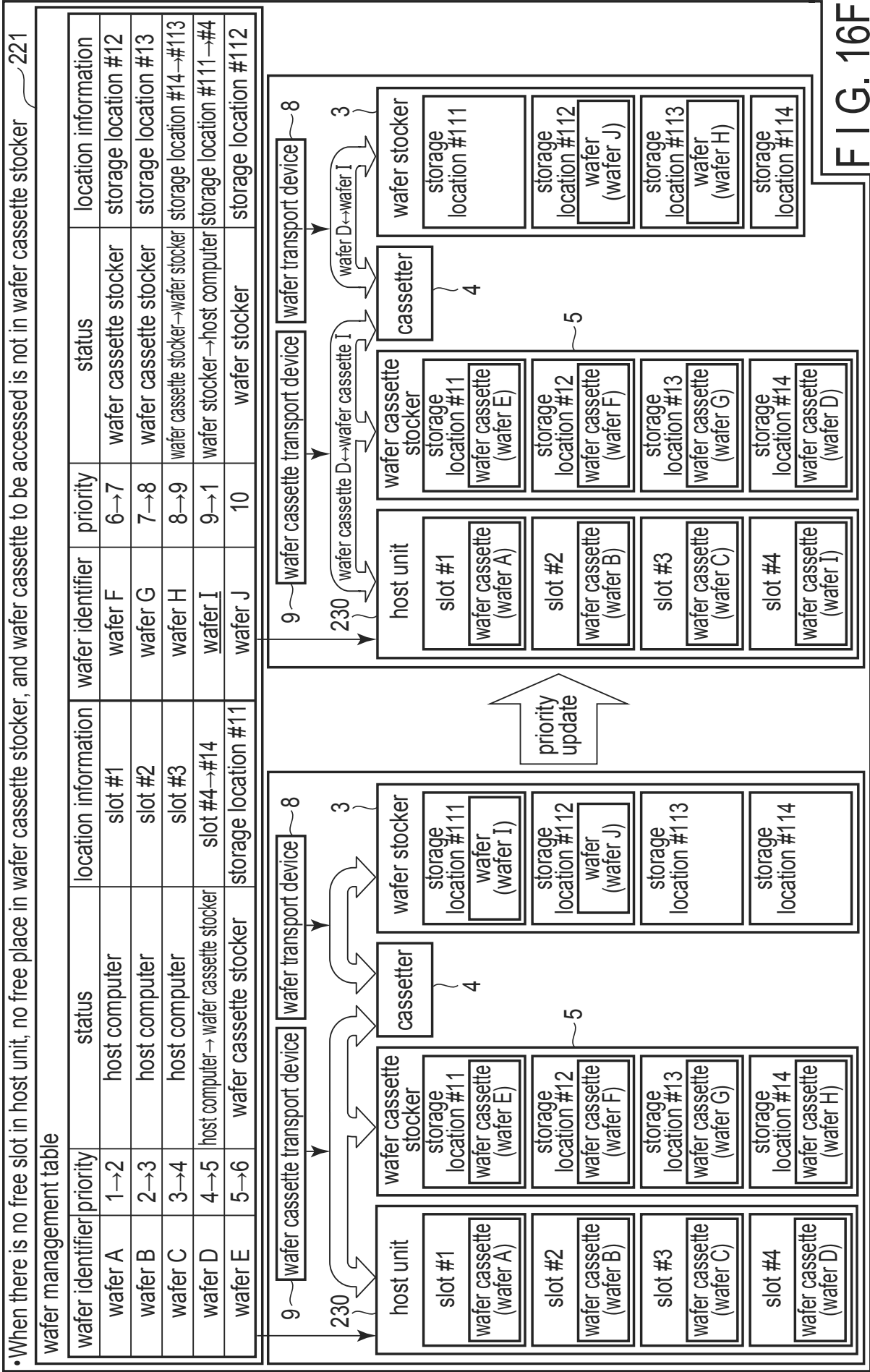


FIG. 16F

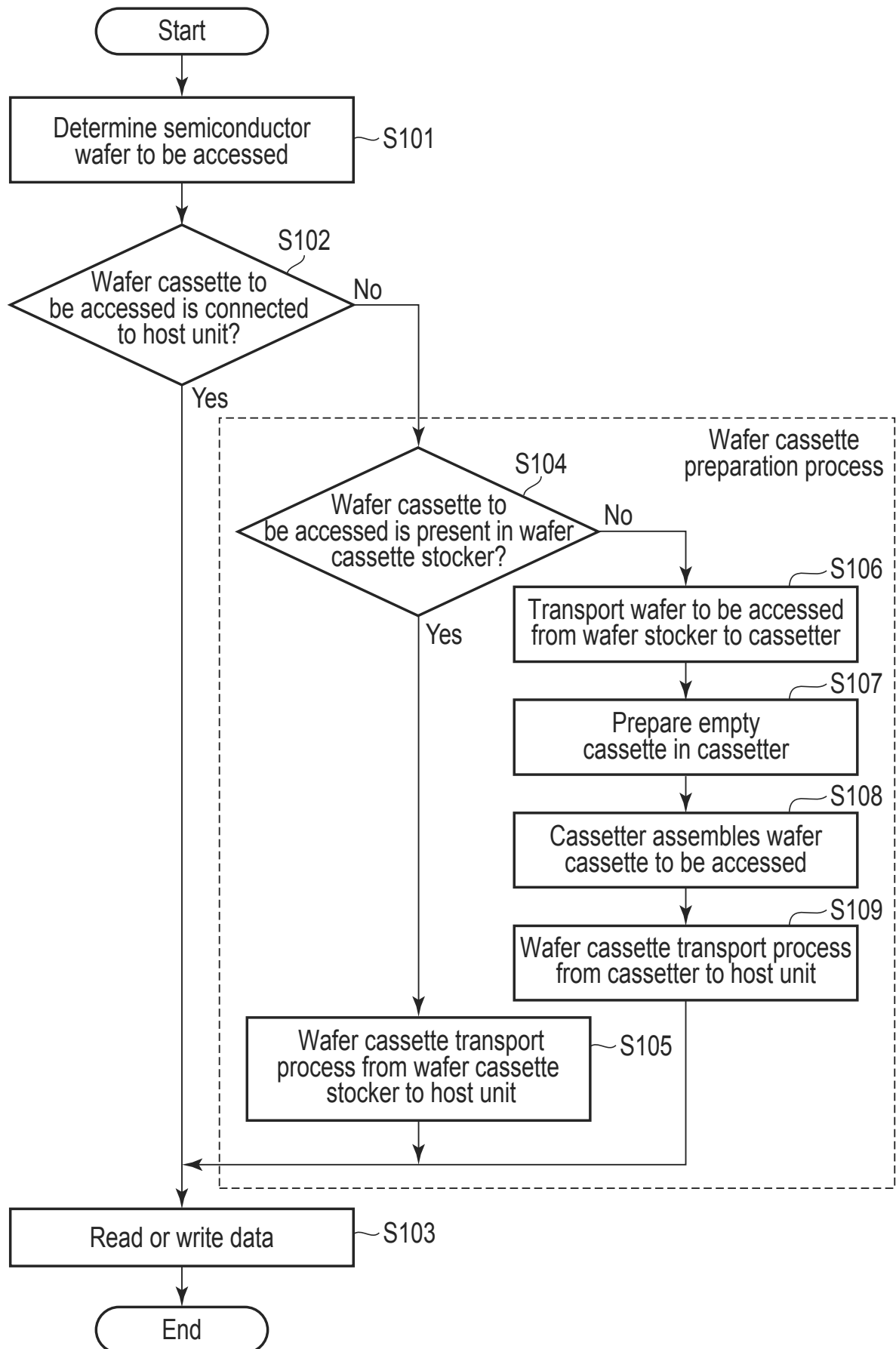


FIG. 17

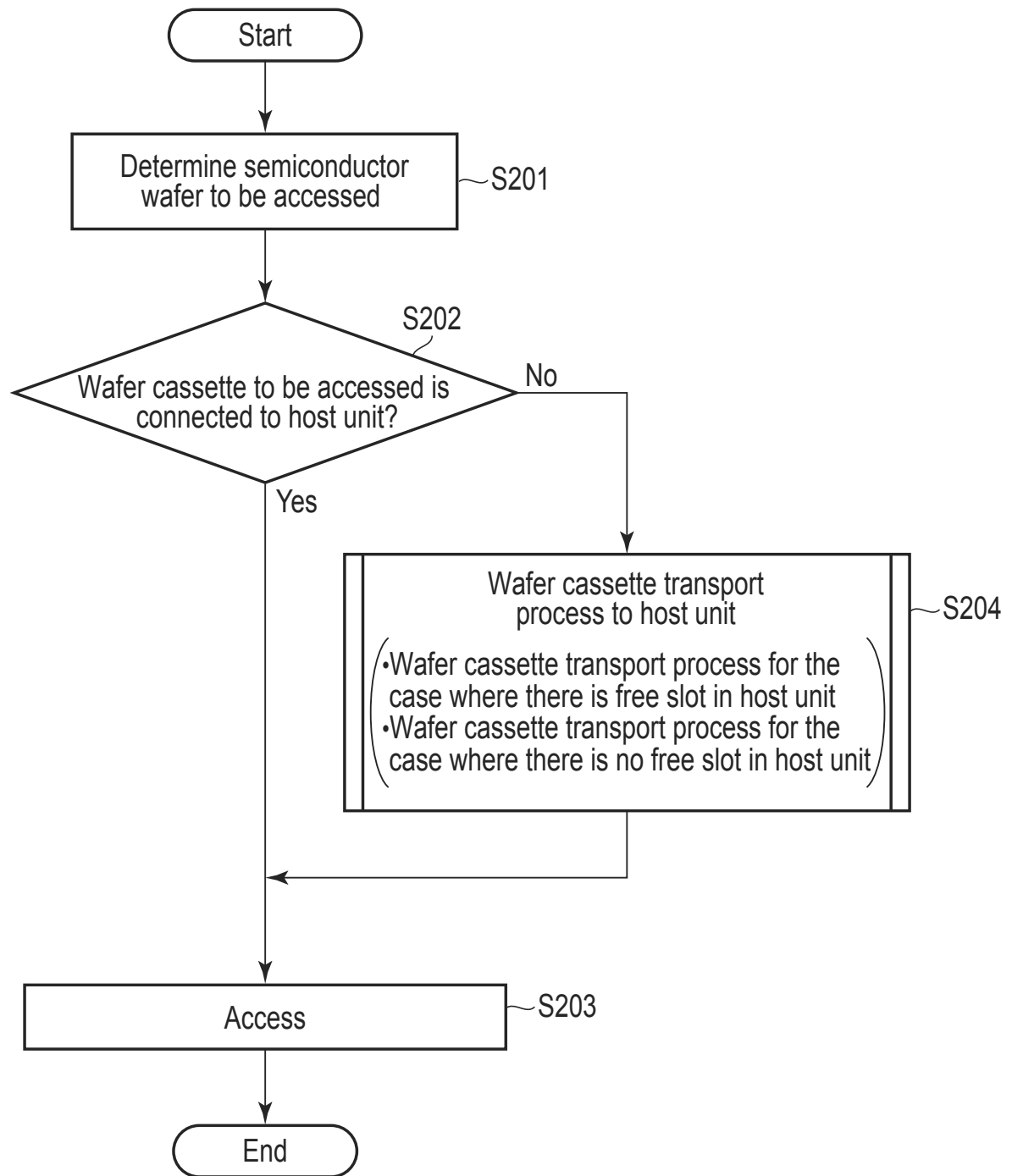


FIG. 18

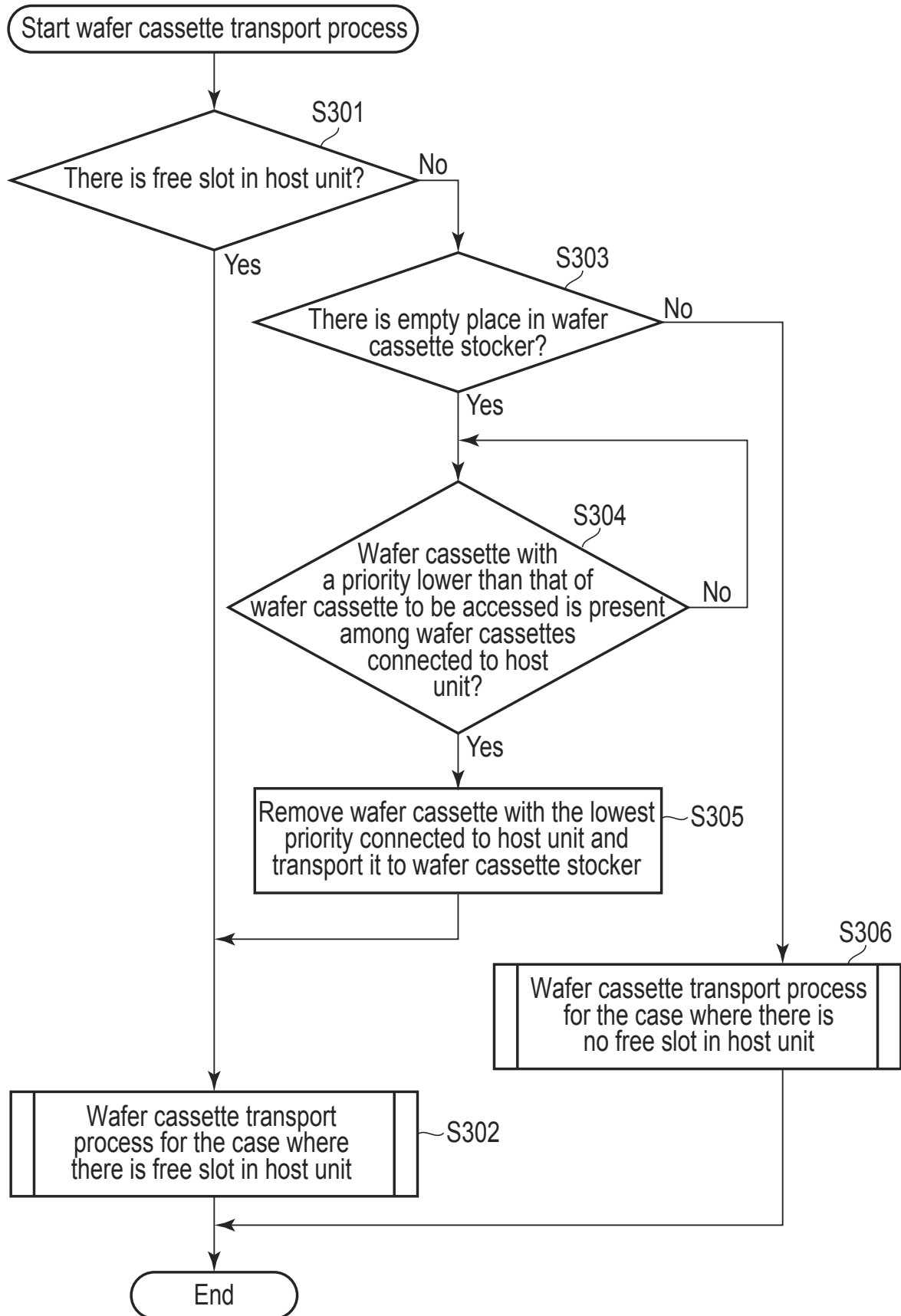


FIG. 19

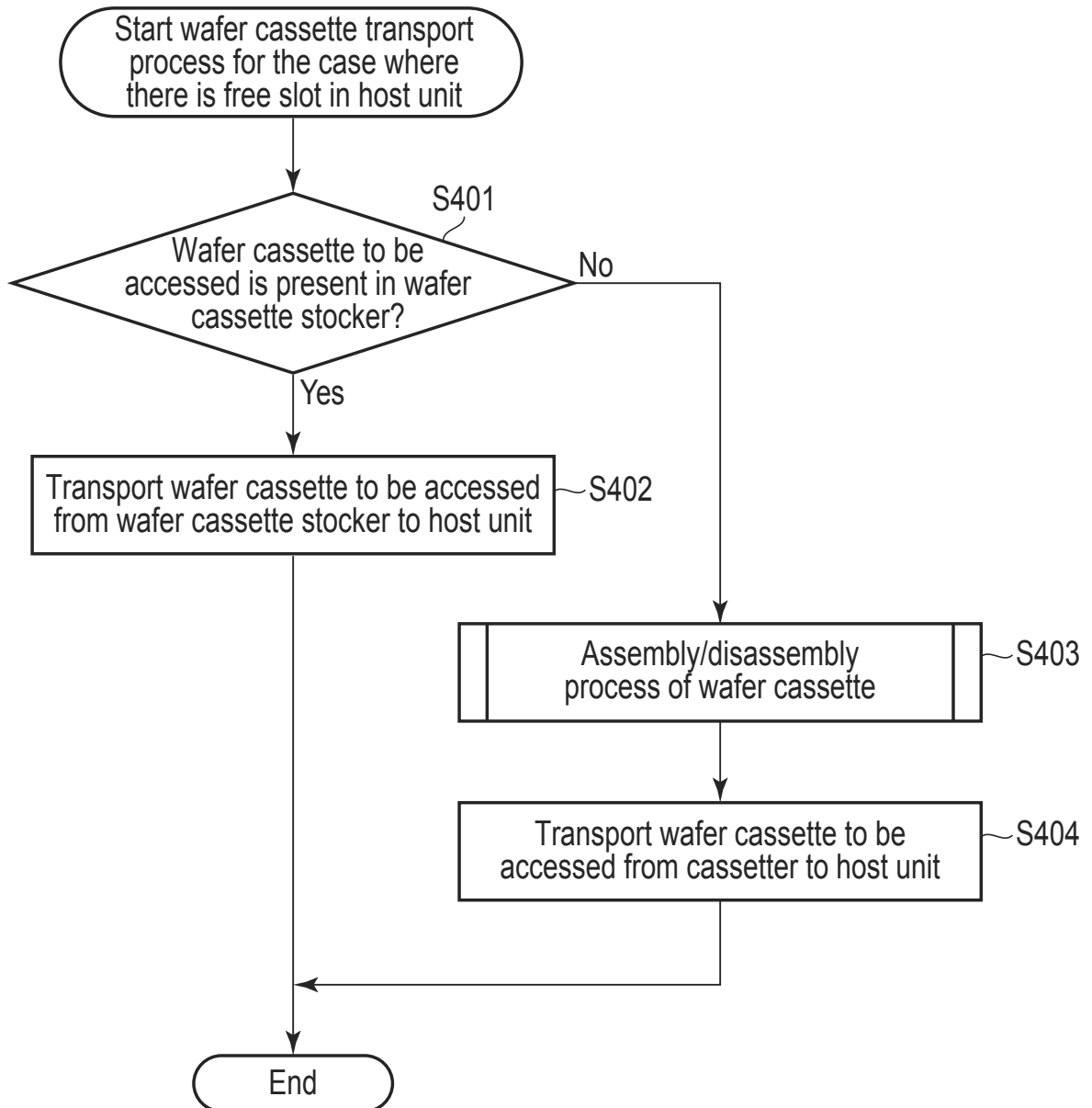


FIG. 20

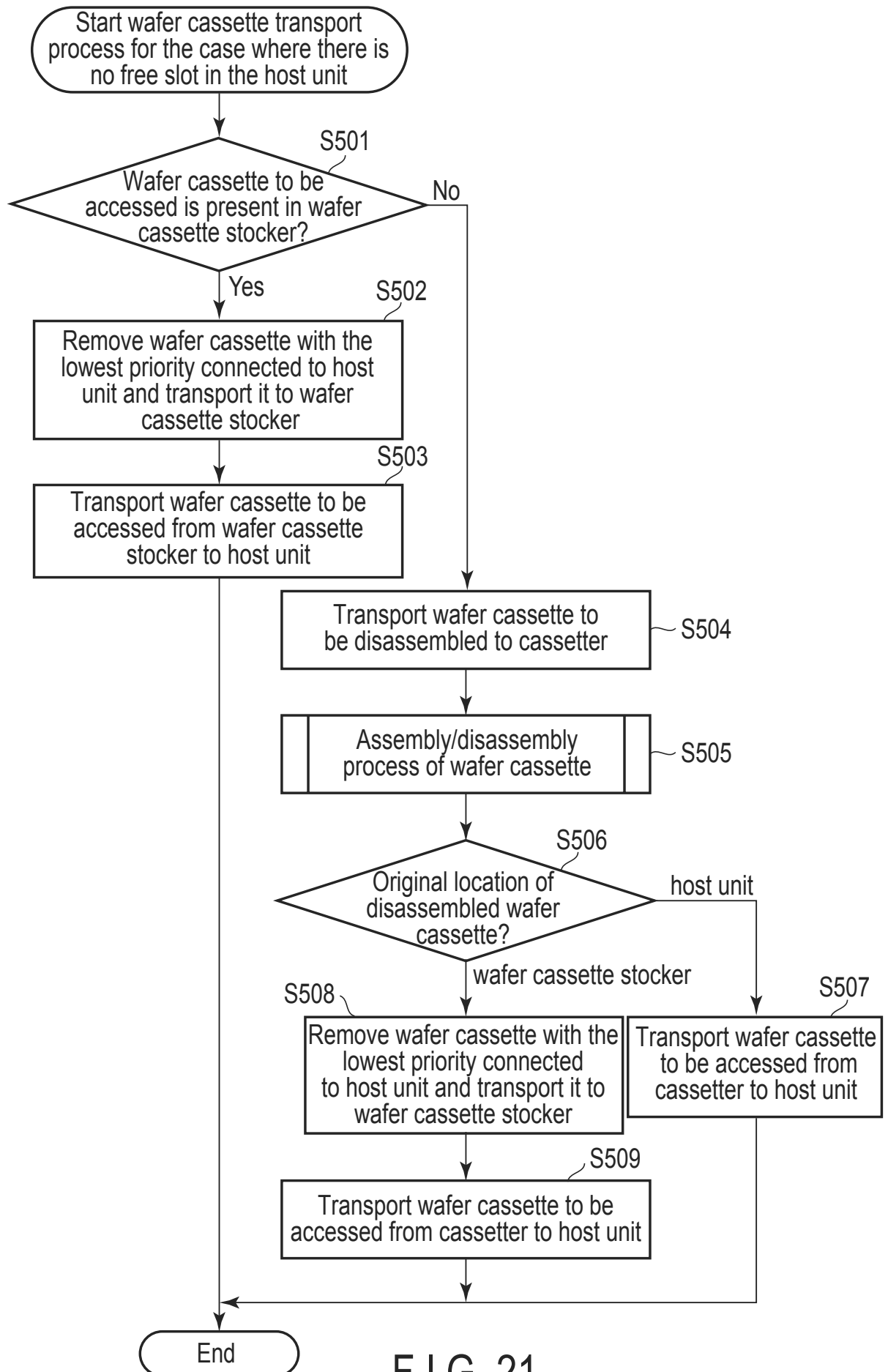


FIG. 21

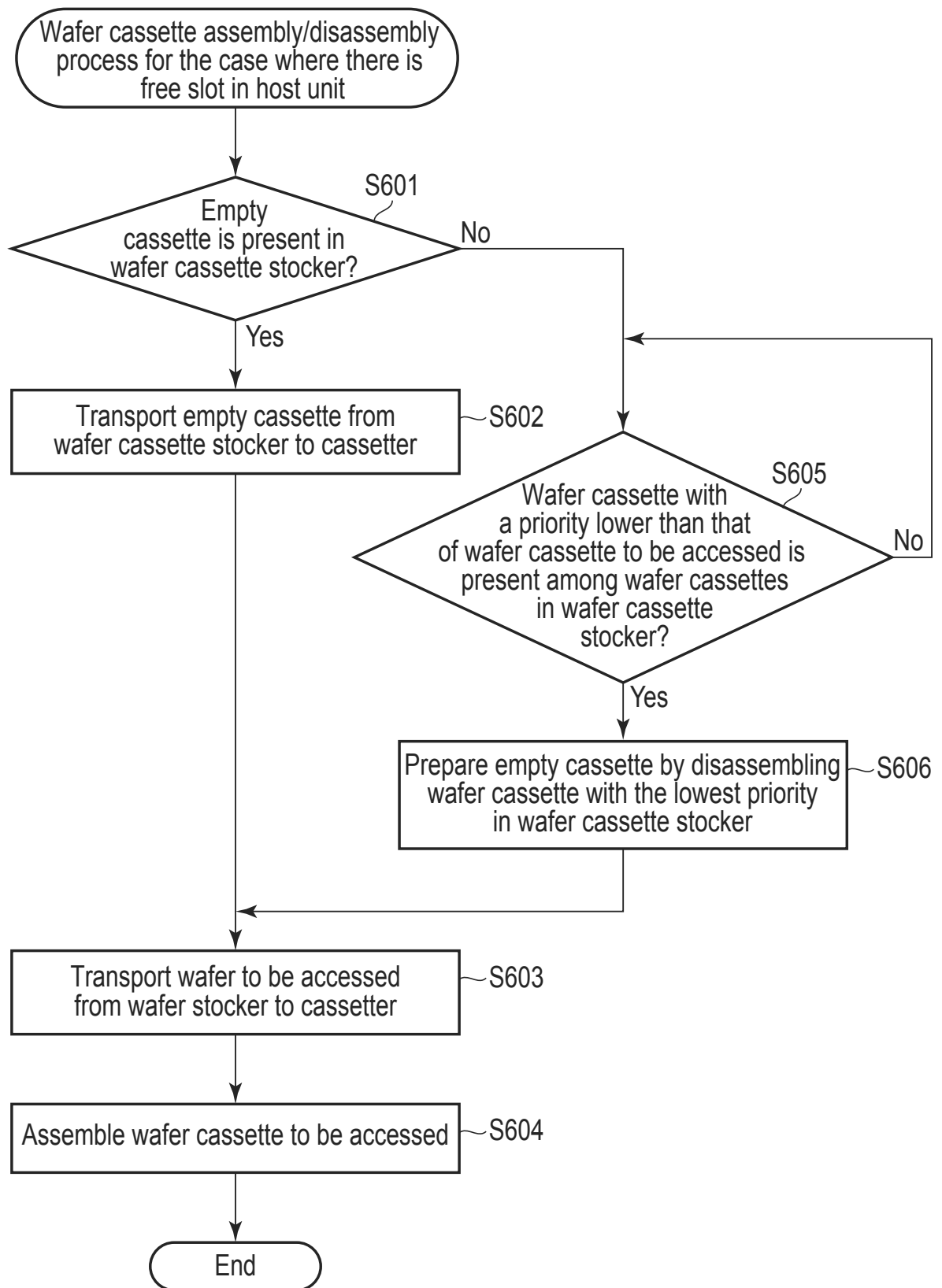


FIG. 22

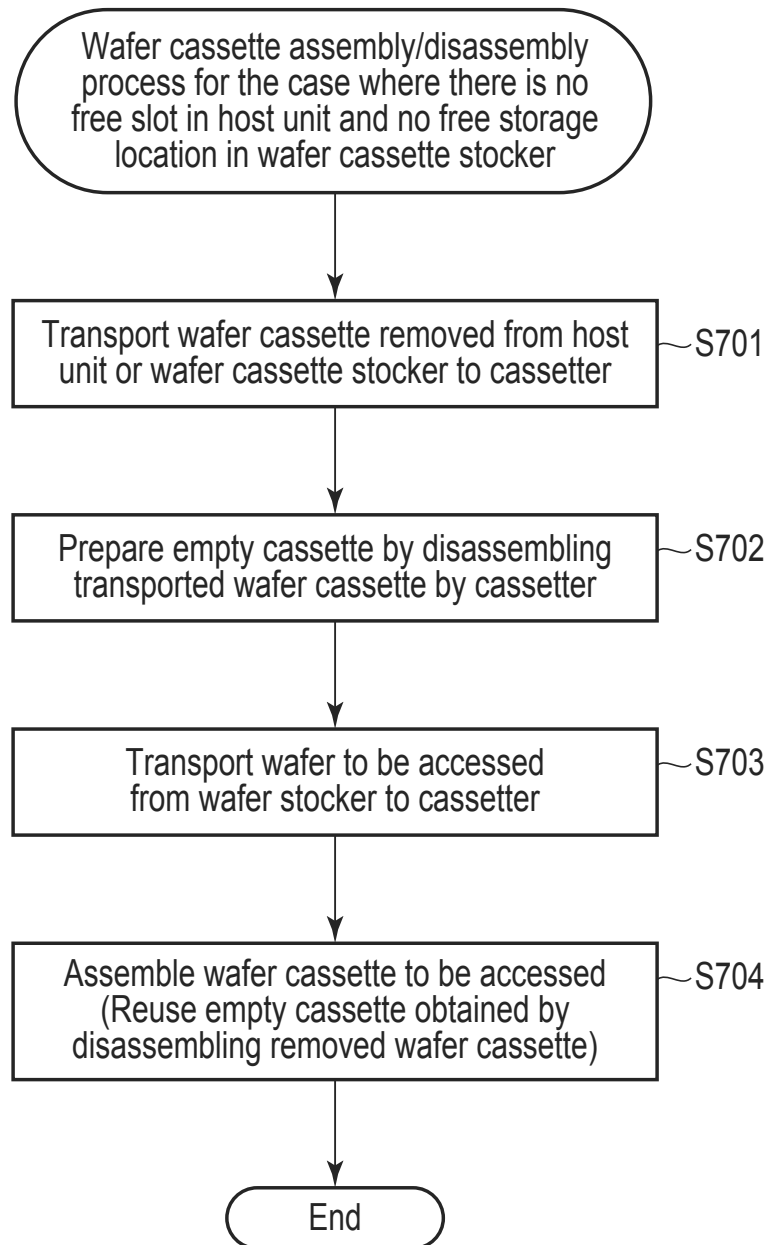


FIG. 23

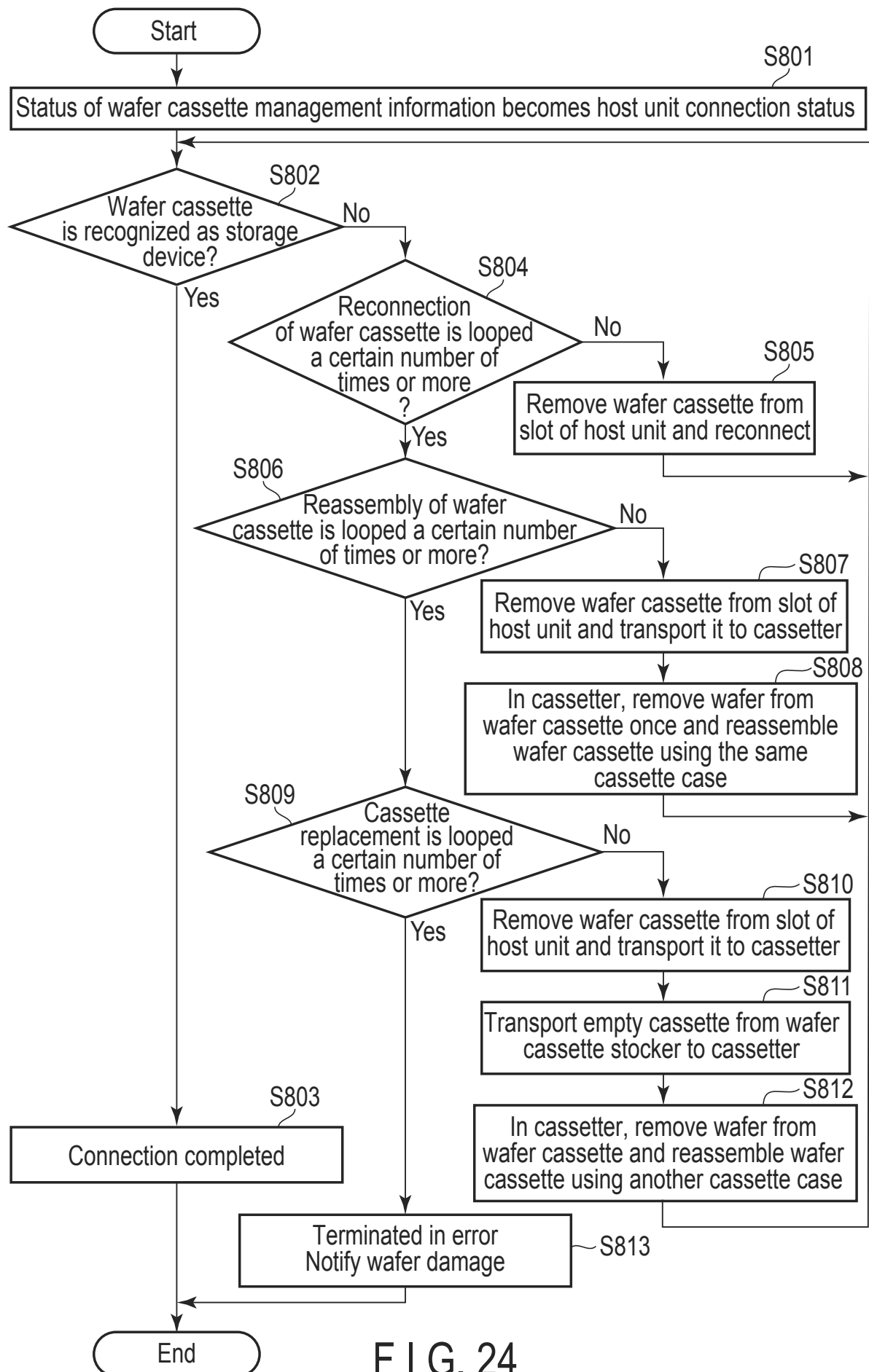


FIG. 24

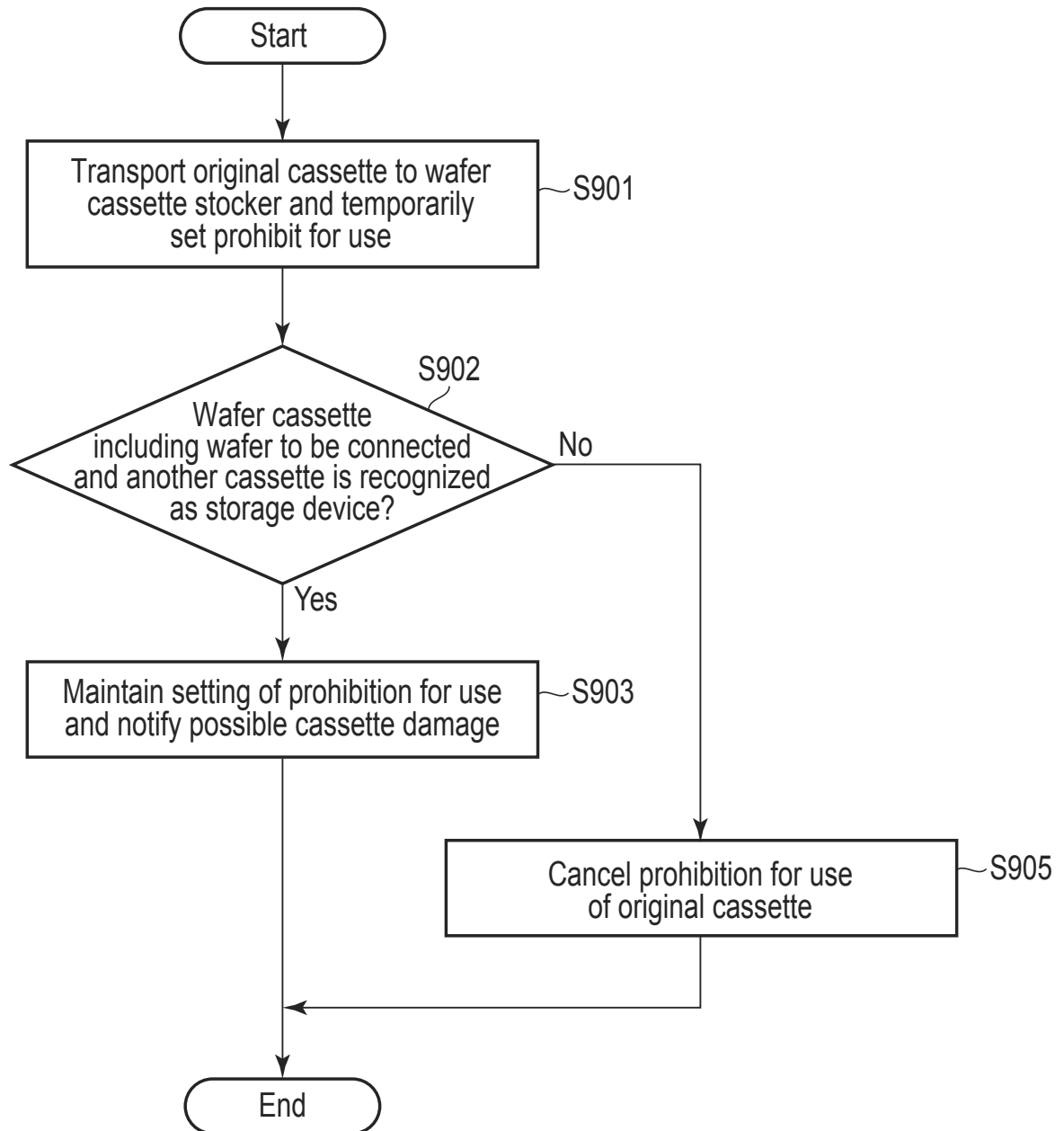


FIG. 25

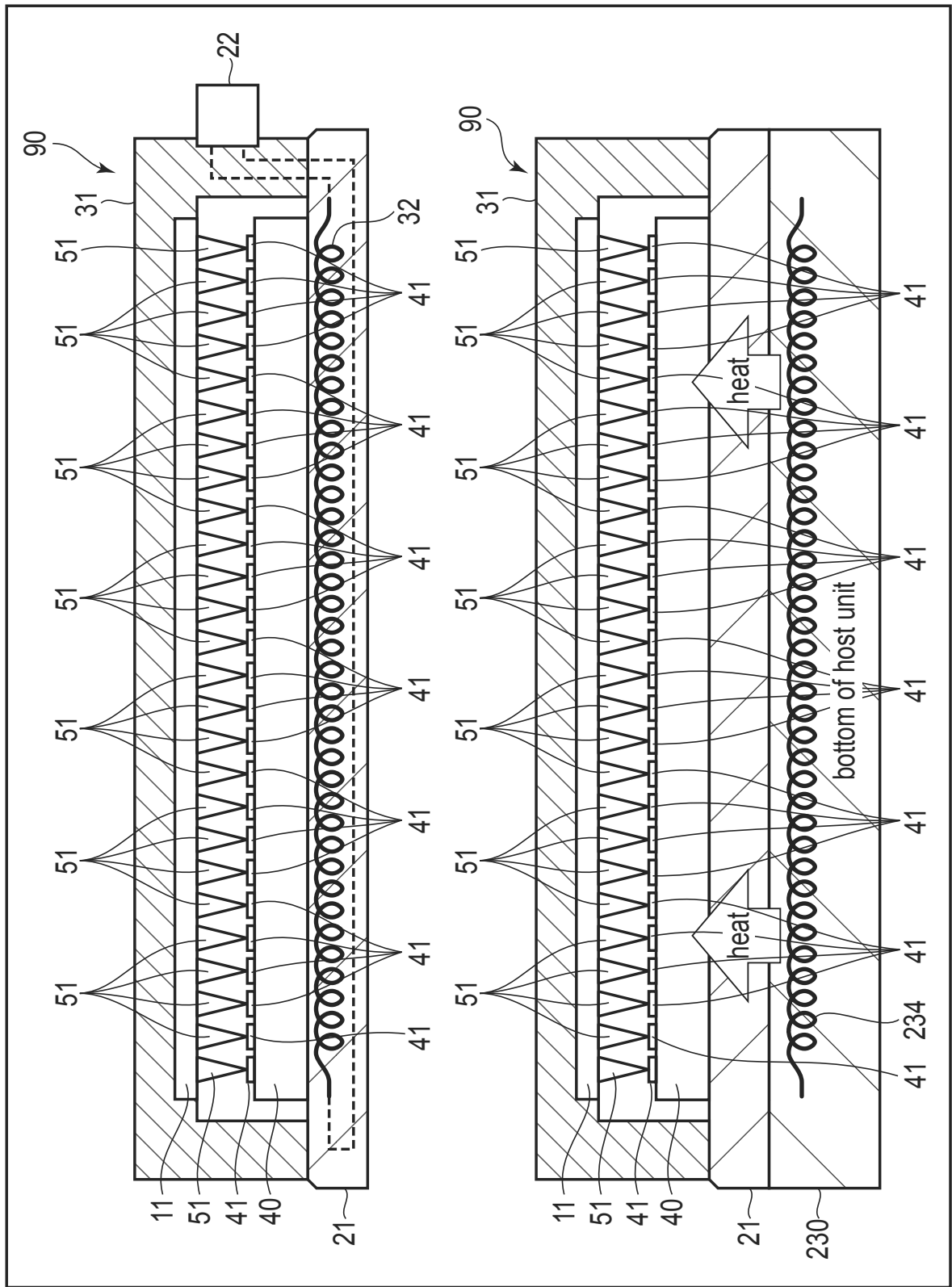


FIG. 26

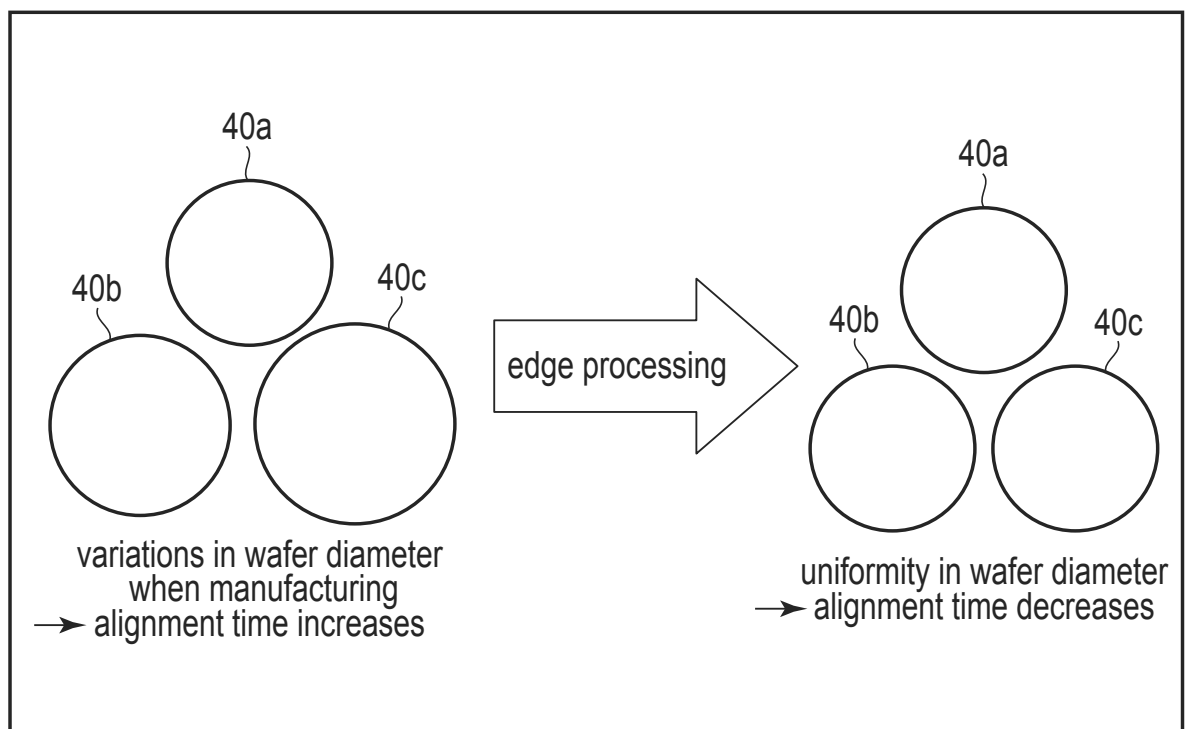


FIG. 27

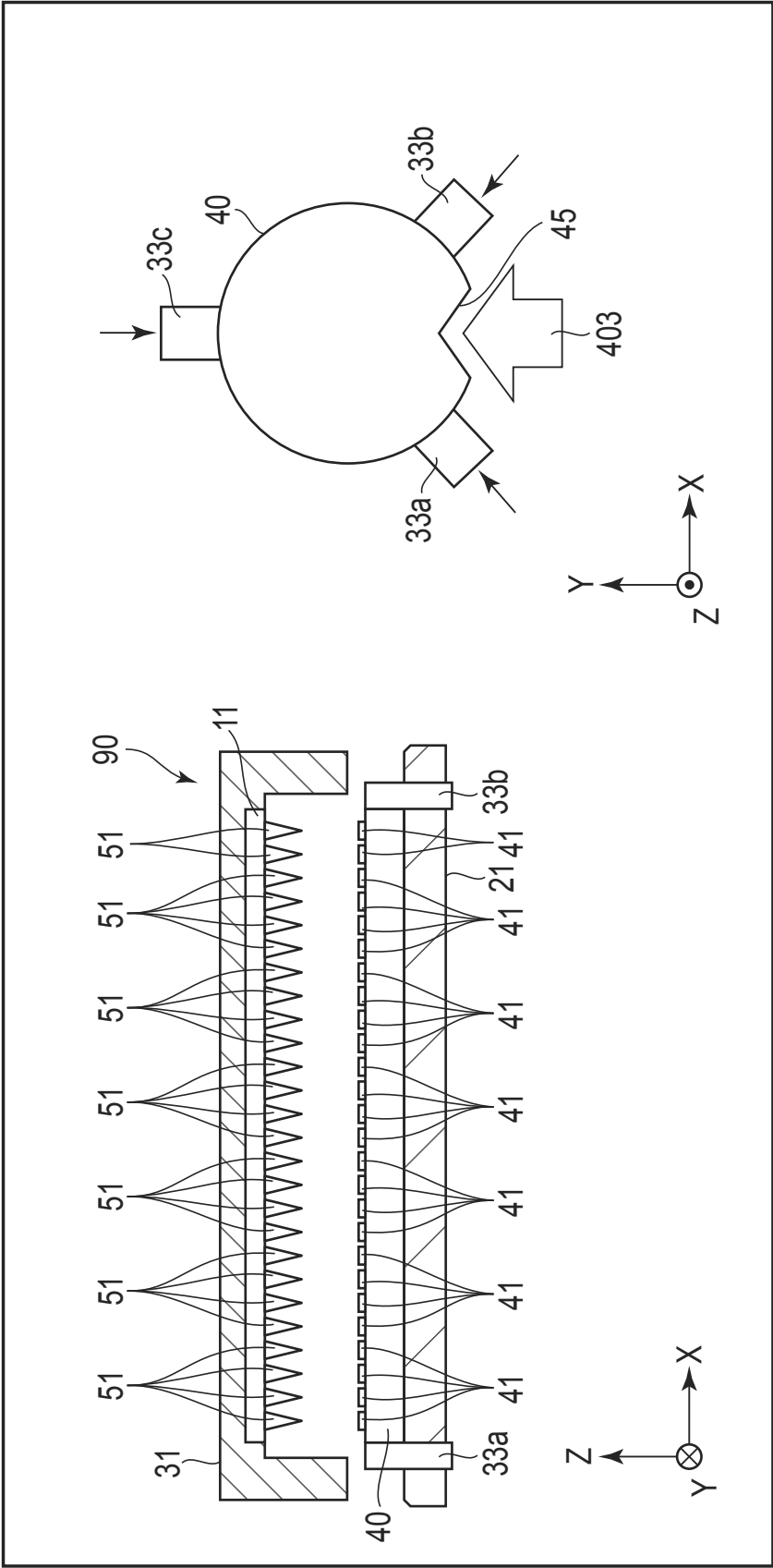


FIG. 28

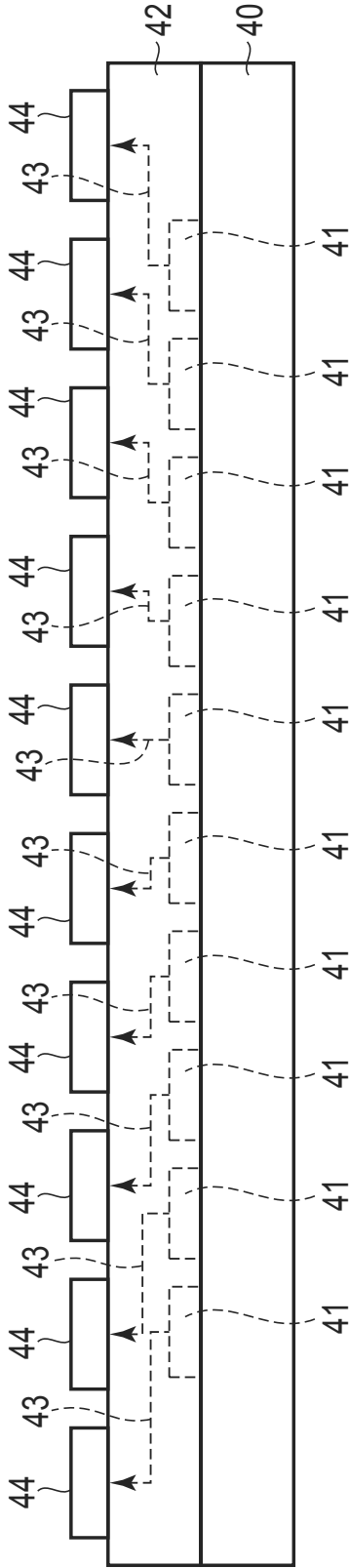
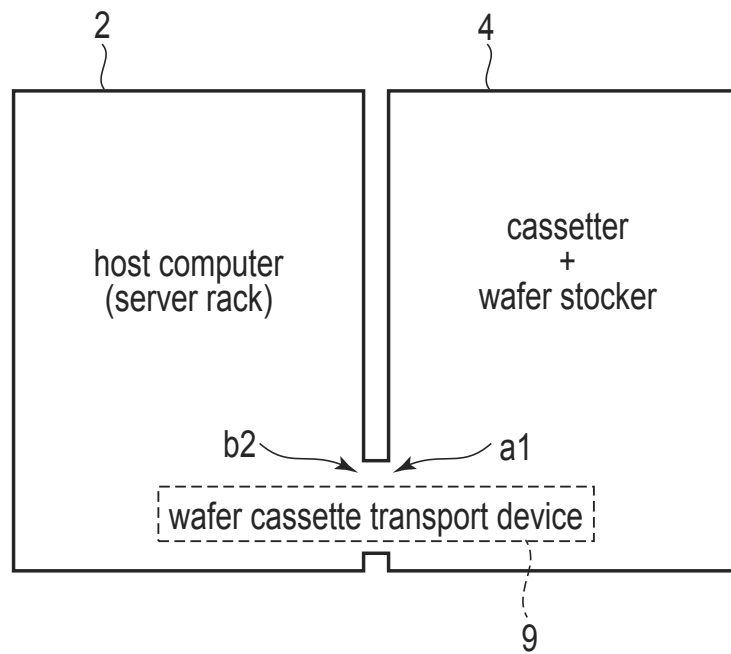


FIG. 29



F I G. 30

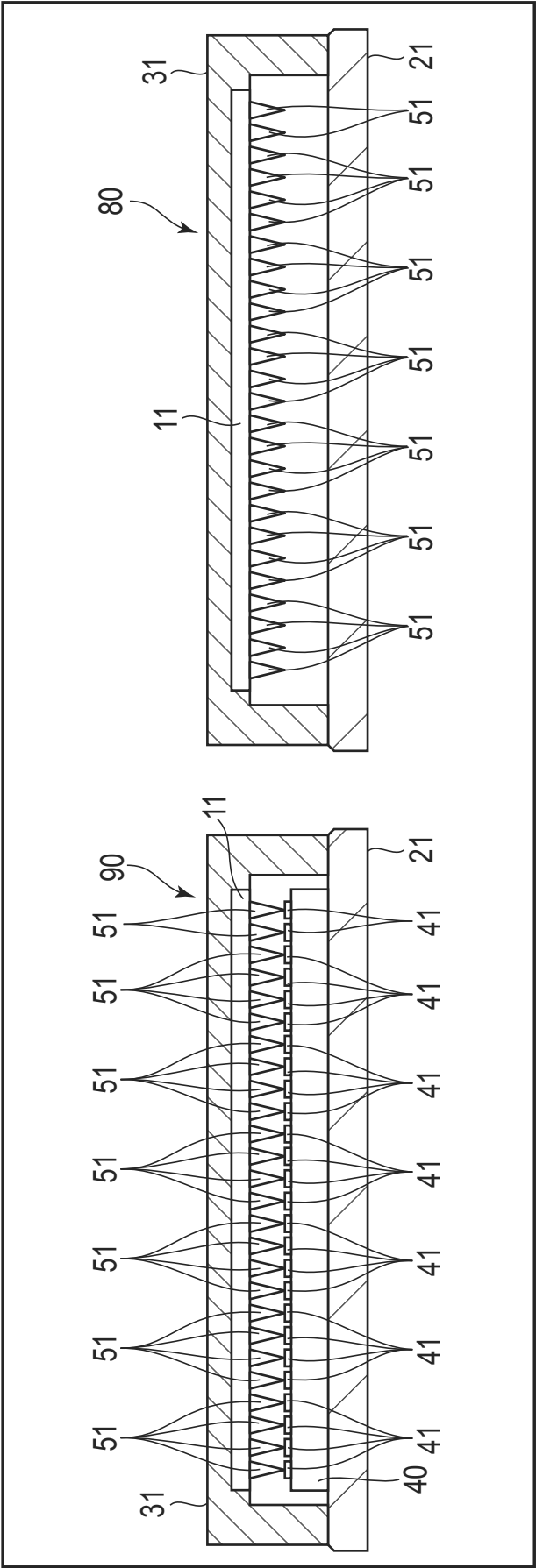


FIG. 31

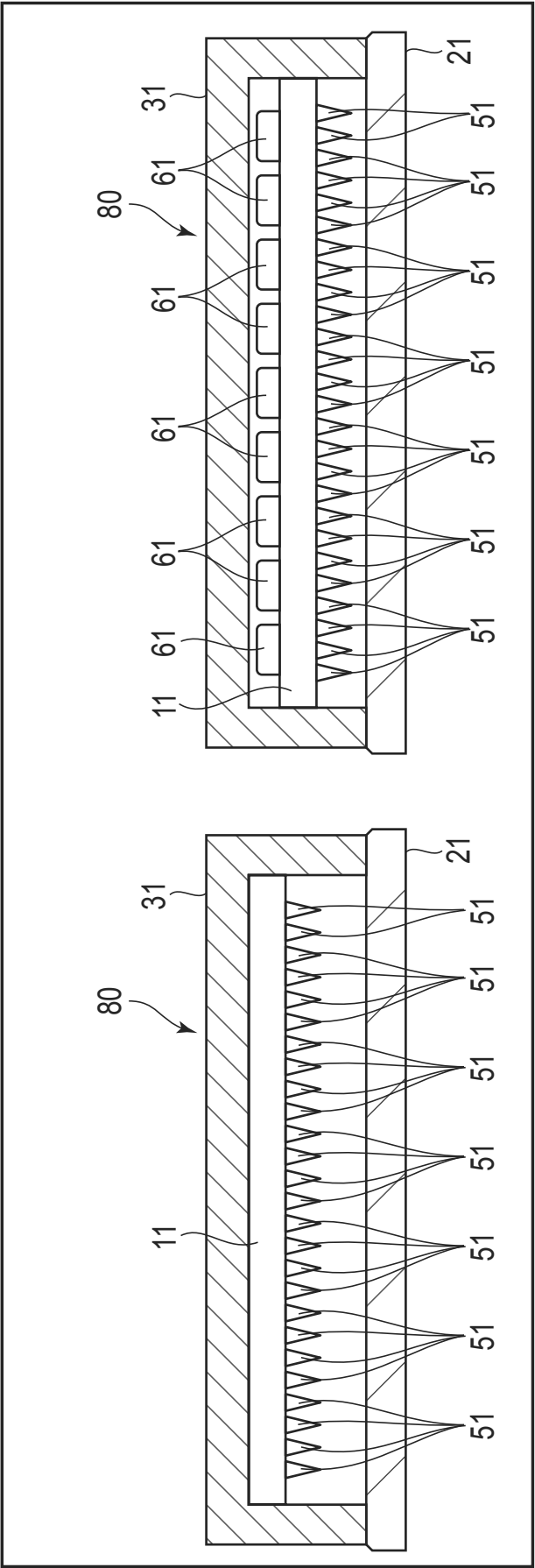


FIG. 32

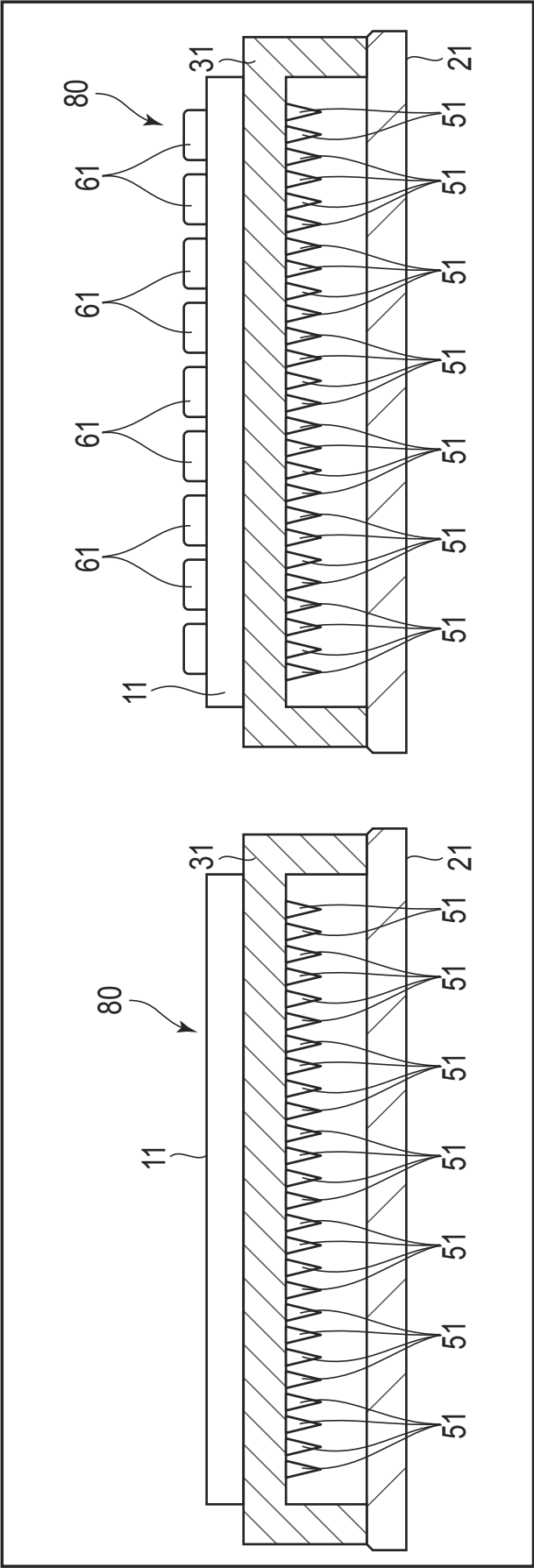


FIG. 33

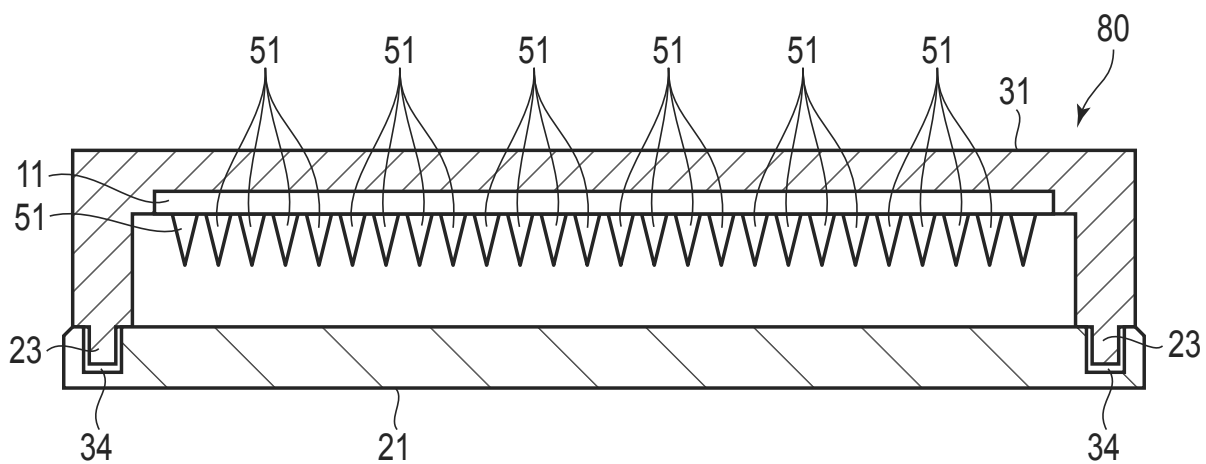


FIG. 34

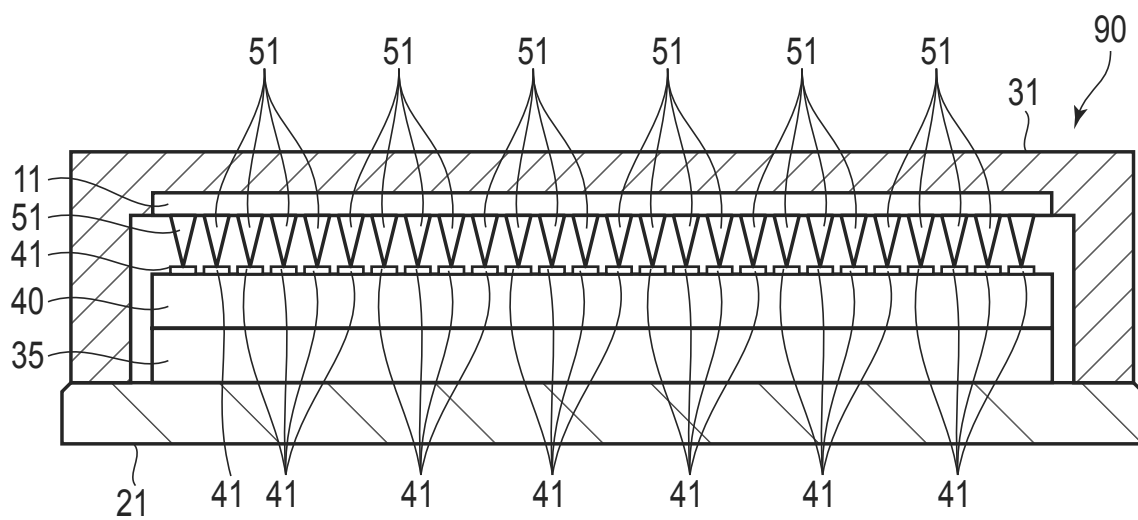


FIG. 35

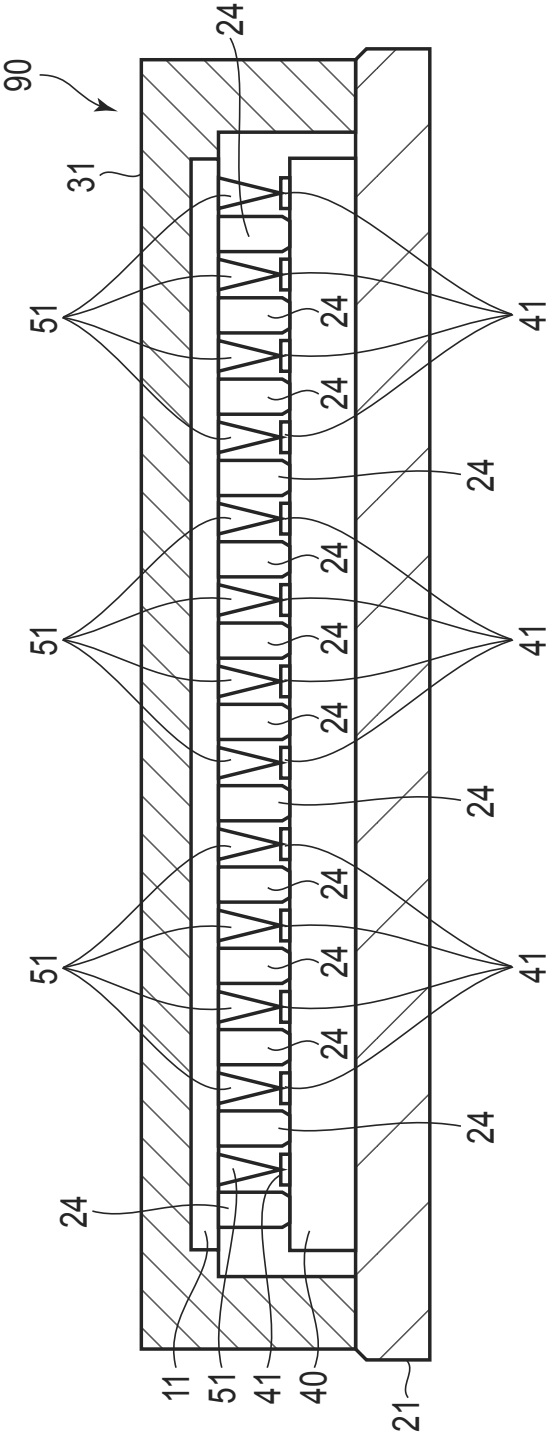


FIG. 36

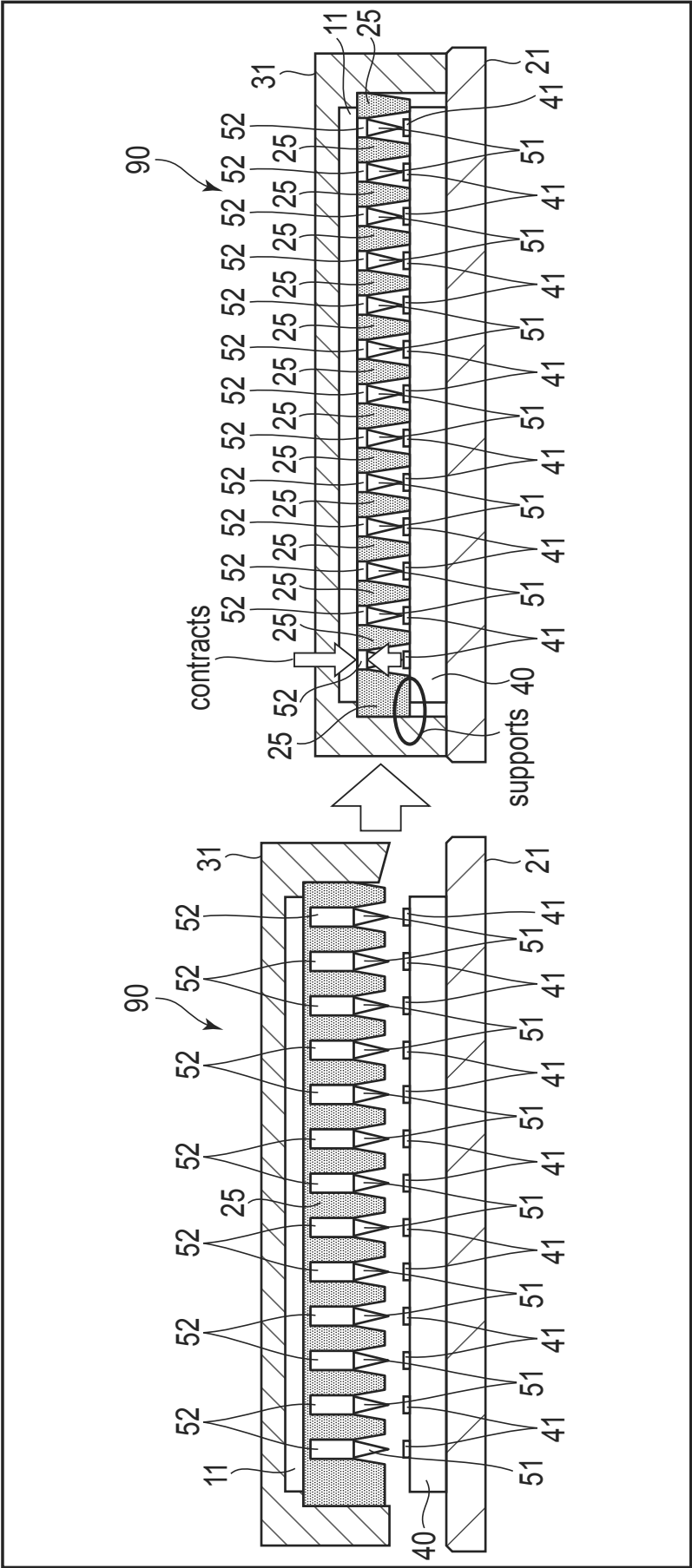


FIG. 37

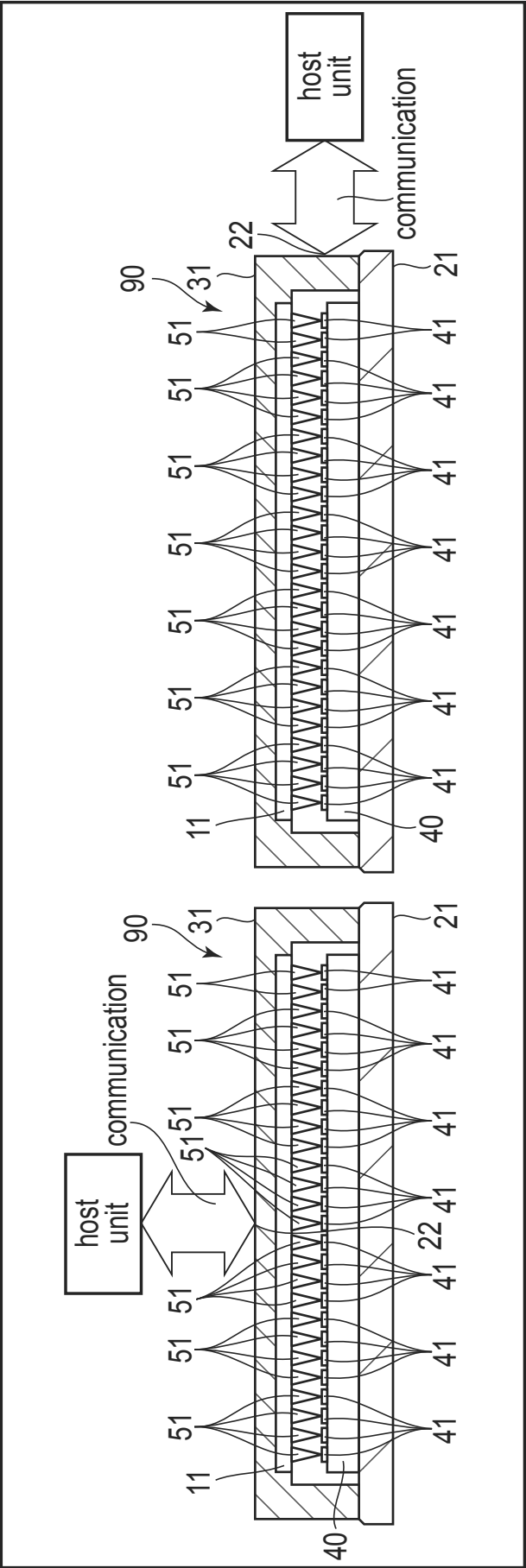


FIG. 38