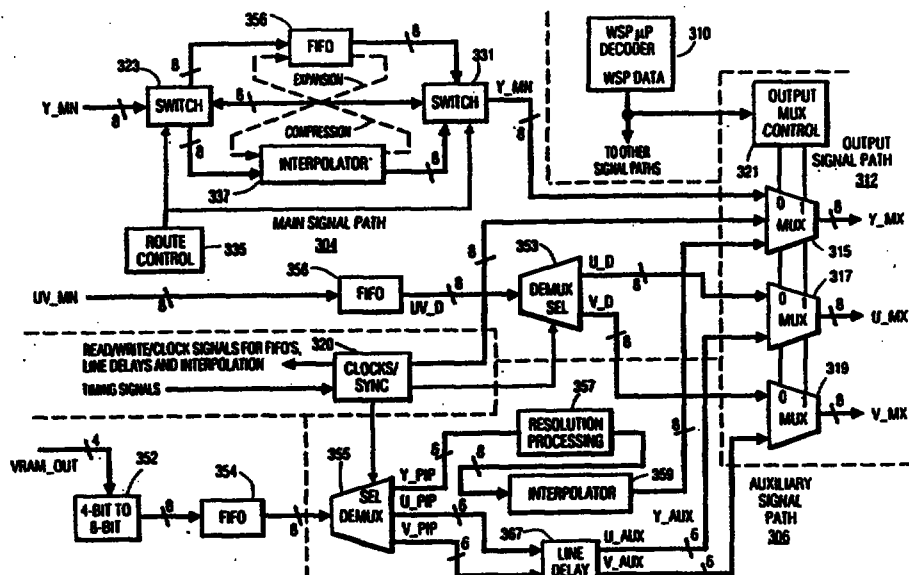




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 5 : H04N 5/262, 9/74, 3/223	A1	(11) International Publication Number: WO 91/19386 (43) International Publication Date: 12 December 1991 (12.12.91)
(21) International Application Number: PCT/US91/03815 (22) International Filing Date: 30 May 1991 (30.05.91) (30) Priority data: 9012326.6 1 June 1990 (01.06.90) GB (71) Applicant (for all designated States except US): THOMSON CONSUMER ELECTRONICS, INC. [US/US]; 600 North Sherman Drive, Indianapolis, IN 46201 (US). (72) Inventors; and (75) Inventors/Applicants (for US only): ERZOS, Nathaniel, Haluk [US/US]; 6565 East State Road 136, Brownsburg, IN 46112 (US). SAEGER, Timothy, William [US/US]; 8318 Nashua Drive, Indianapolis, IN 46260 (US).		(74) Agents: MELLER, Michael, N. et al.; M.N. Meller & Associates, P.O. Box 2198, Grand Central Station, New York, NY 10163 (US). (81) Designated States: AT (European patent), AU, BB, BE (European patent), BF (OAPI patent), BG, BJ (OAPI patent), BR, CA, CF (OAPI patent), CG (OAPI patent), CH (European patent), CI (OAPI patent), CM (OAPI patent), DE (European patent), DK (European patent), ES (European patent), FI, FR (European patent), GA (OAPI patent), GB (European patent), GN (OAPI patent), GR (European patent), HU, IT (European patent), JP, KP, KR, LK, LU (European patent), MC, MG, ML (OAPI patent), MR (OAPI patent), MW, NL (European patent), NO, PL, RO, SD, SE (European patent), SN (OAPI patent), SU, TD (OAPI patent), TG (OAPI patent), US. Published <i>With international search report.</i>

This Sheet Has Been Replaced

**(54) Title: PICTURE OVERLAY SYSTEM FOR TELEVISION****(57) Abstract**

A picture overlay system assures proper size and placement of picture overlays in simultaneous picture displays. The system comprises a video memory (350, 356, 358) and a control circuit (300) for writing and reading information from a video signal into and out of the video memory, the information relating to video data and field type. A reading circuit (680) supplies information from the video memory synchronously with a display for another video signal. An interpolator (337) selectively compresses and expands the information read from the reading circuit. The field type information undergoes the compression and expansion together with the video information. A decoding circuit (310) decodes the field type information to identify first and second types of fields and an absence of valid video data. A multiplexer (312) combines the video signals for simultaneous display, operating responsive to the decoding circuit. The reading circuit can comprise an asynchronous line memory for the information read from the video memory, having a write port operable synchronously with the reading of the video memory and a read port operable synchronously with the display for the other video signal.