

REMOTE CONTROL RECEIVER AND METHOD OF CONTROLLING THE SAME

Abstract

A remote control receiver of the present invention comprises power-on detecting block (7) operating on a low speed subclock during a standby mode of remote control receiver (10) on the side of electronic apparatus body (4) for decoding an instruction code signal through the medium of an infrared ray transmitted from remote control transmitter (1) in the standby mode and for outputting a code match pulse when the instruction thereof corresponds to "power-on," and interrupt controller (5) for acquiring the code match pulse output from power-on detecting block (7) to output an interrupt request signal for waking CPU (6) up. The remote control receiver suppresses an increase in power consumption caused by erroneous remote control input or input produced by a noise source in the standby mode state with the power turned off.

Fig. 3