

ABSTRACT

A HYBRID CMOS COMPATIBLE ELECTRO-OPTIC DEVICE

5 A hybrid photonic chip comprising a plurality of semiconductor materials arranged to define a chip providing a function, wherein at least a first part of the chip is formed of materials which can be fabricated using a CMOS technique; and at least a second part of the chip is not susceptible to be fabricated during a CMOS technique; wherein the second part of the chip in conjunction with the first part is configured to support a propagating low loss single mode waveguide.

10 [Figure 2b]