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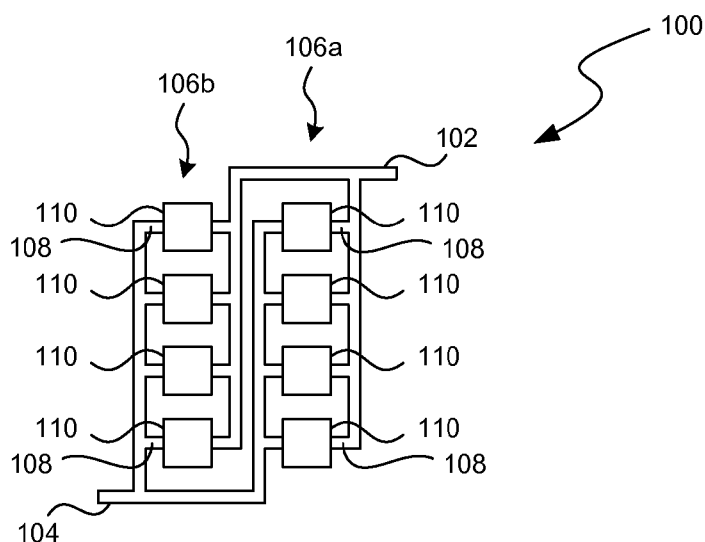


FIG. 1A

(57) Abstract: Various embodiments of solid state lighting ("SSL") assemblies with high voltage SSL dies and methods of manufacturing are described herein. In one embodiment, an array assembly of SSL dies includes a first terminal and a second terminal configured to receive an input voltage (V_o). The array assembly also includes a plurality of SSL dies coupled between the first terminal and the second terminal, at least some of which are high voltage SSL dies coupled in parallel.