FIG. 1

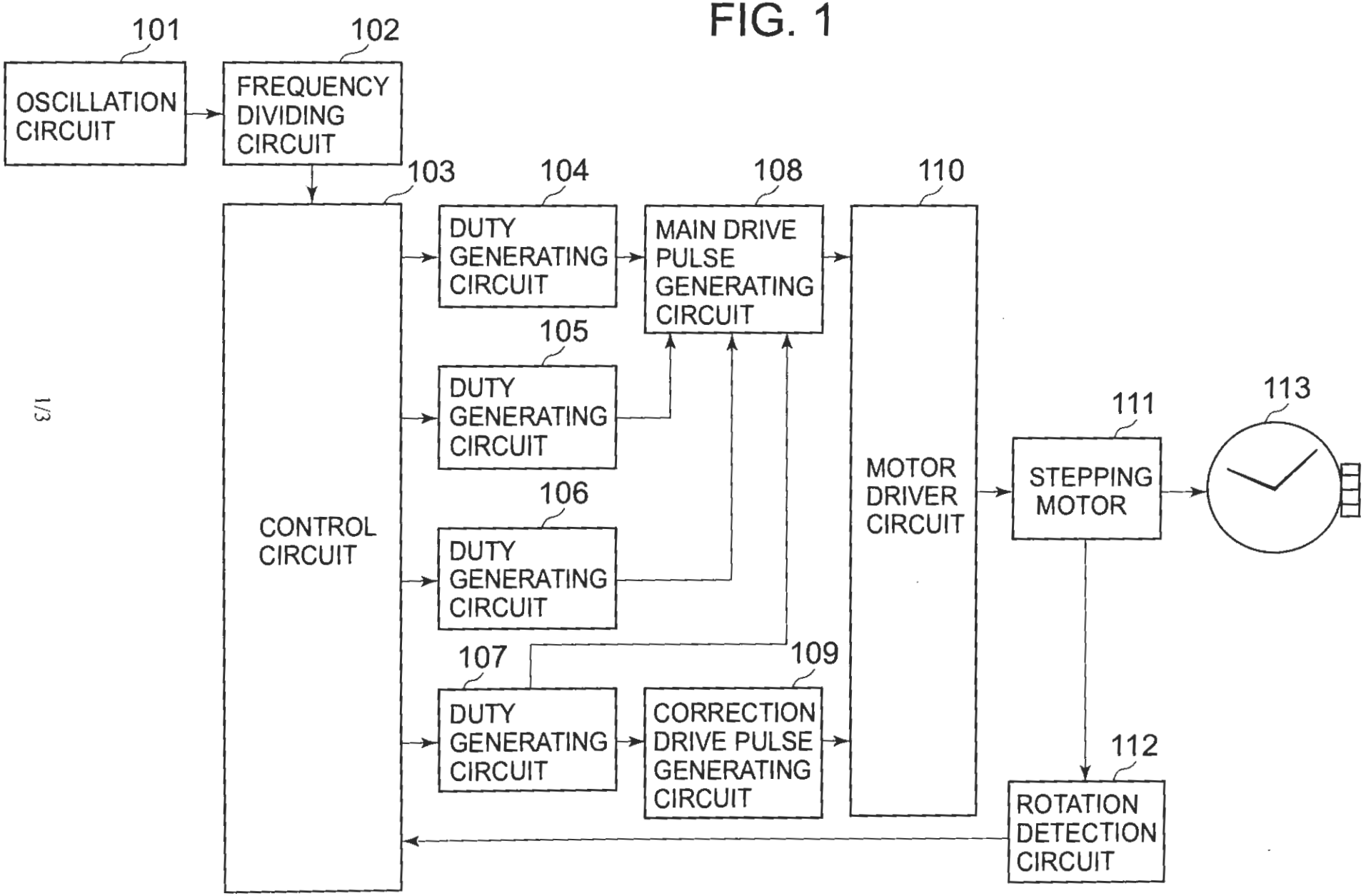

101
OSCILLATION CIRCUIT
102
FREQUENCY DIVIDING CIRCUIT
103
CONTROL CIRCUIT
104
DUTY GENERATING CIRCUIT
105
DUTY GENERATING CIRCUIT
106
DUTY GENERATING CIRCUIT
107
DUTY GENERATING CIRCUIT
108
MAIN DRIVE PULSE GENERATING CIRCUIT
109
CORRECTION DRIVE PULSE GENERATING CIRCUIT
110
MOTOR DRIVER CIRCUIT
111
STEPPING MOTOR
112
ROTATION DETECTION CIRCUIT
113

FIG. 2

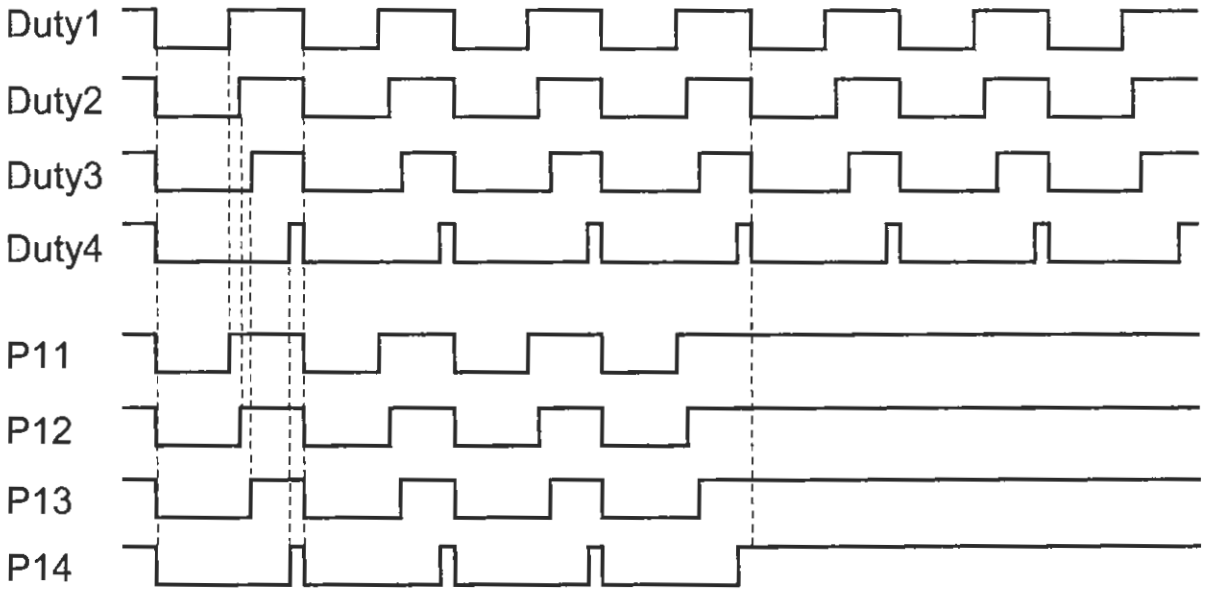

Duty1
Duty2
Duty3
Duty4
P11
P12
P13
P14

FIG. 3

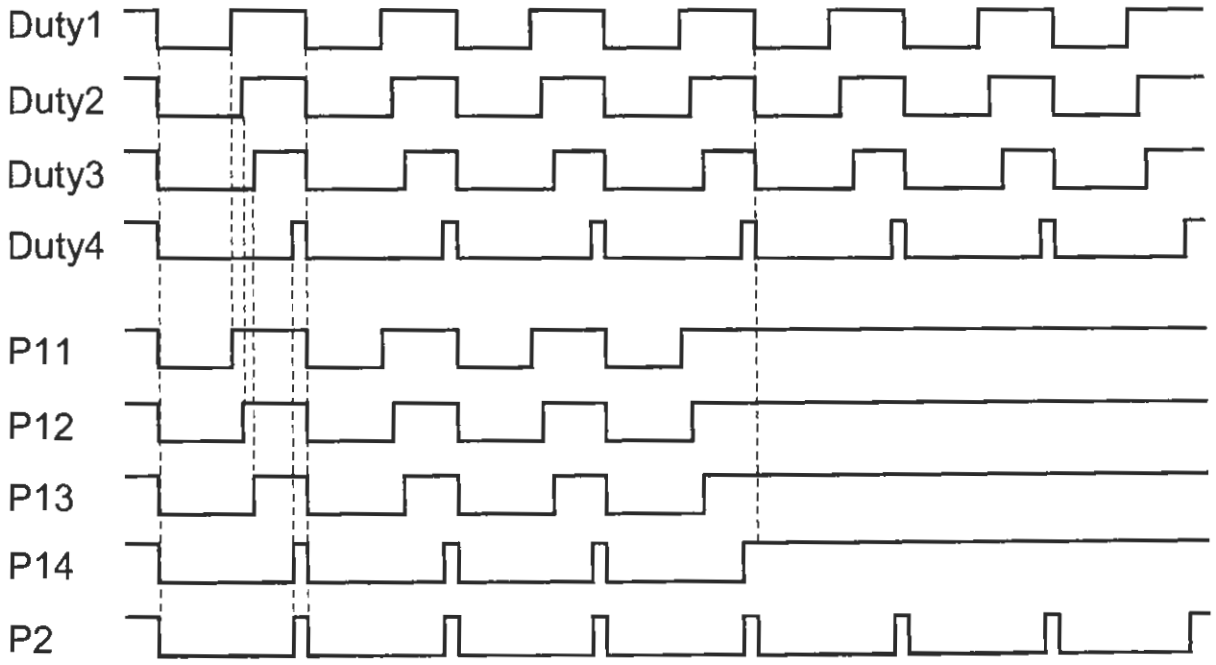

Duty1
Duty2
Duty3
Duty4
P11
P12
P13
P14
P2

FIG. 4

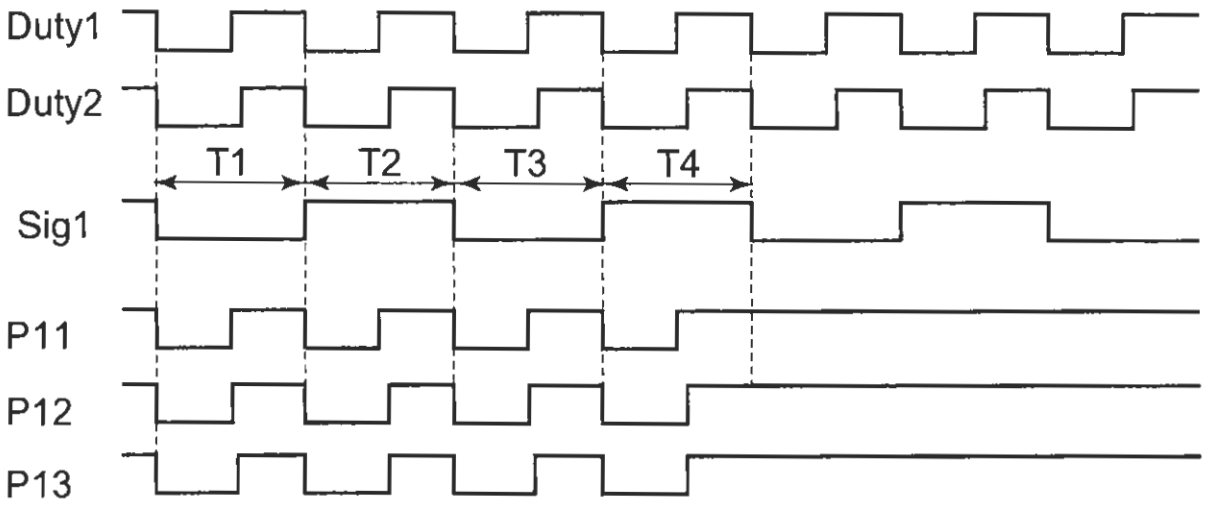


FIG. 5A PRIOR ART



FIG. 5B PRIOR ART



FIG. 5C PRIOR ART

