DATA RECEIVING APPARATUS, DEMODULATOR CIRCUIT AND INTEGRATED CIRCUIT

ABSTRACT OF THE DISCLOSURE

A data receiving apparatus, a demodulator circuit and an integrated circuit that are simplified in the general constitution and enable the consumed power to be reduced. After decomposed into both the comparison result (S7) between an optional threshold controlled corresponding to the DC offset of the demodulated signal (S3) and the absolute value of the amplitude of the demodulated signal (S3) and the detected result (S8) of detecting the polarity of the demodulated signal (S3), transmitted data (S5) represented by the amplitude of a demodulated signal (S3) containing the amplitude component corresponding to the transmitted data (S5) are outputted and the transmitted data (S5) are regenerated by using these comparison result (S7) and detection result (S8).

FIG 2.