

ABSTRACT

A Power Loss Protection (PLP) circuit may include a control logic circuit configured to generate first and second path control signals, an auxiliary power supply including a first capacitor associated with a first charge allowance voltage level, a second capacitor associated with a second charge allowance voltage level lower than the first charge allowance voltage level, and a path control circuit configured to control an electrical connection between the first capacitor and the second capacitor based on the first path control signal and the second path control signal, and a PLP circuit connected to the first capacitor and configured to provide a charging power to the auxiliary power supply by using an external power, and, charge the first capacitor to the first charge allowance voltage level after charging the first capacitor and the second capacitor to the second charge allowance voltage level by providing the charging power.

FIG. 1